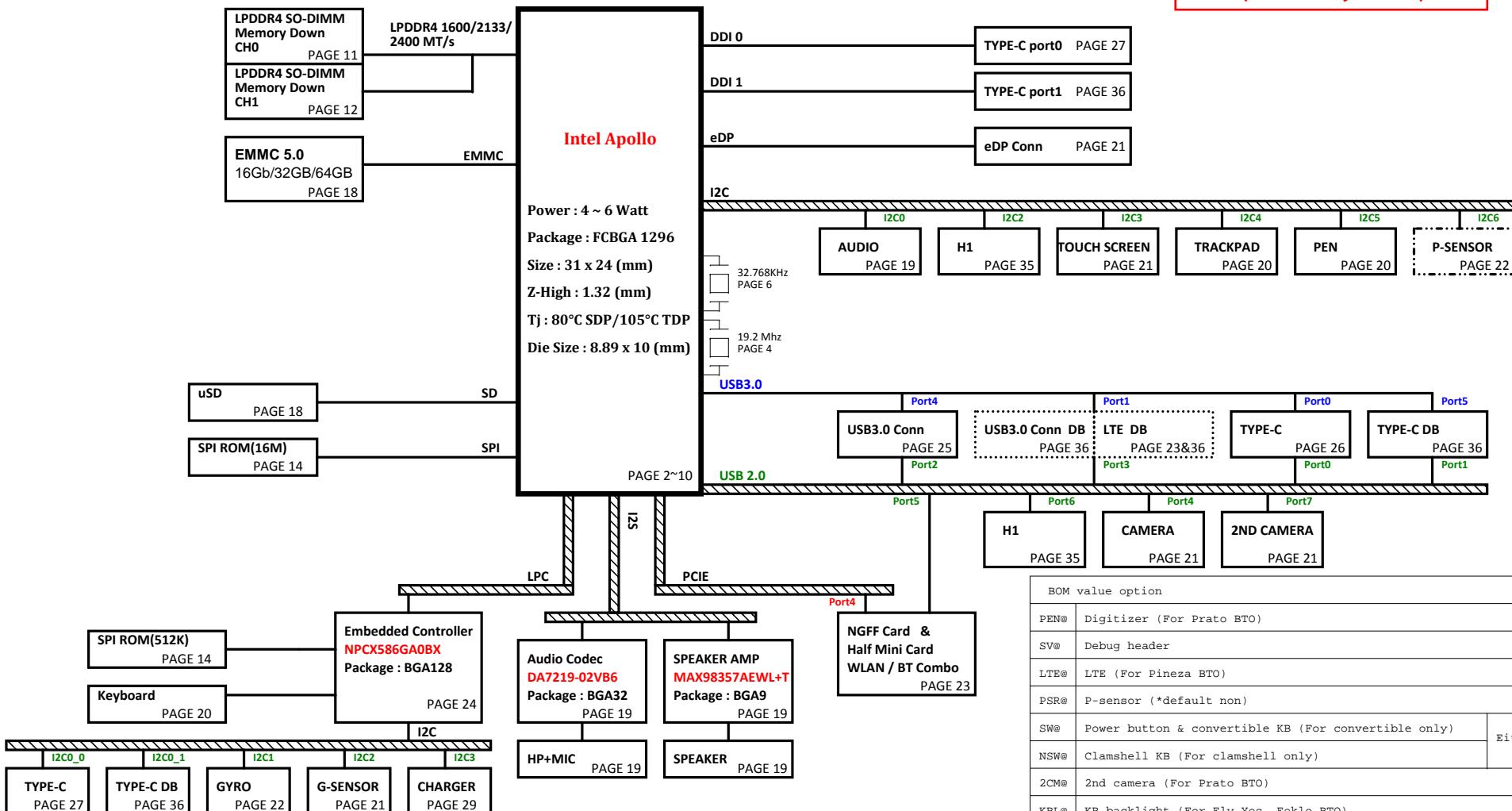


Intel Apollo Platform Block Diagram



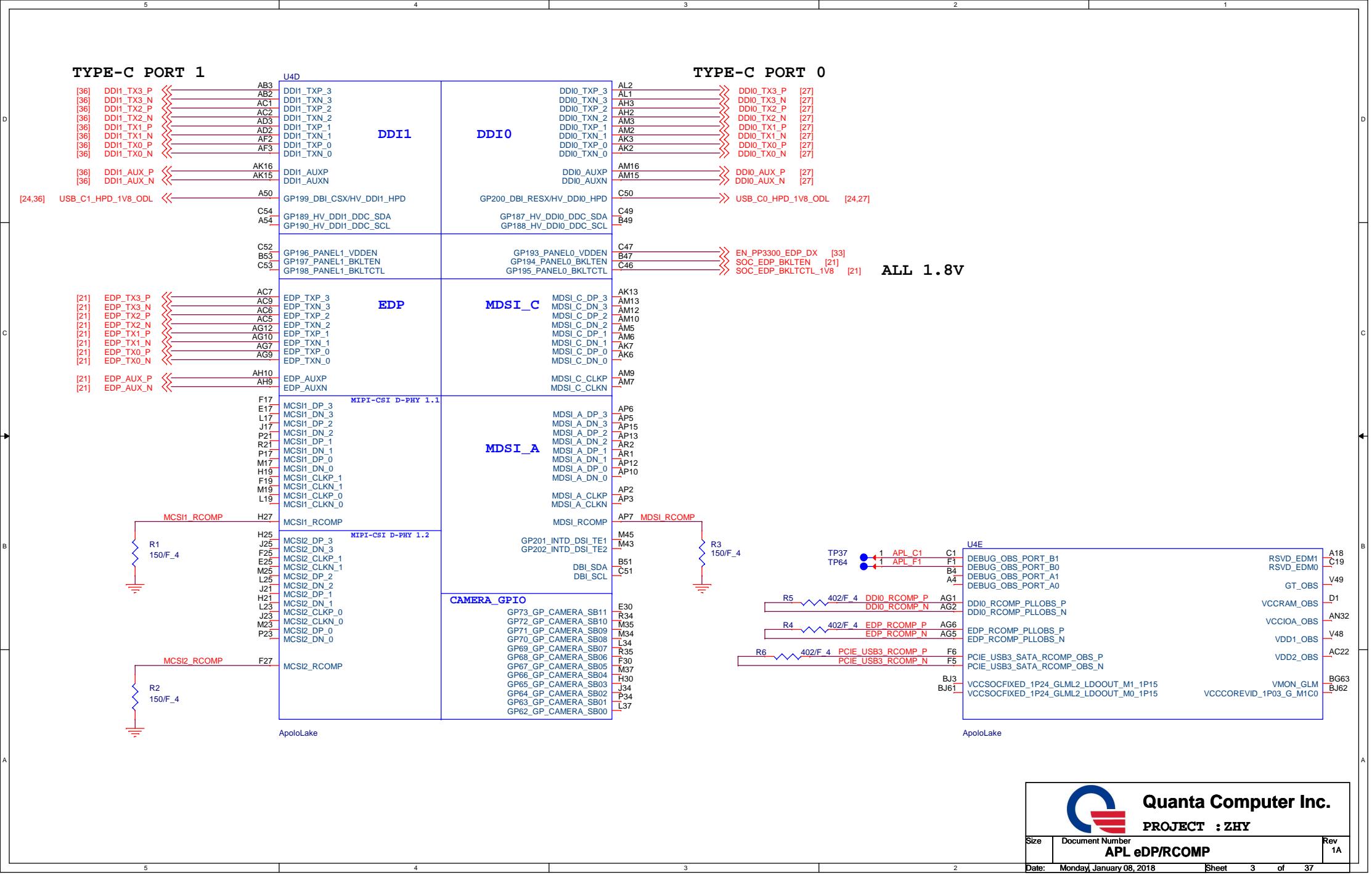
ZAH_proto_zhy_mb_pvt

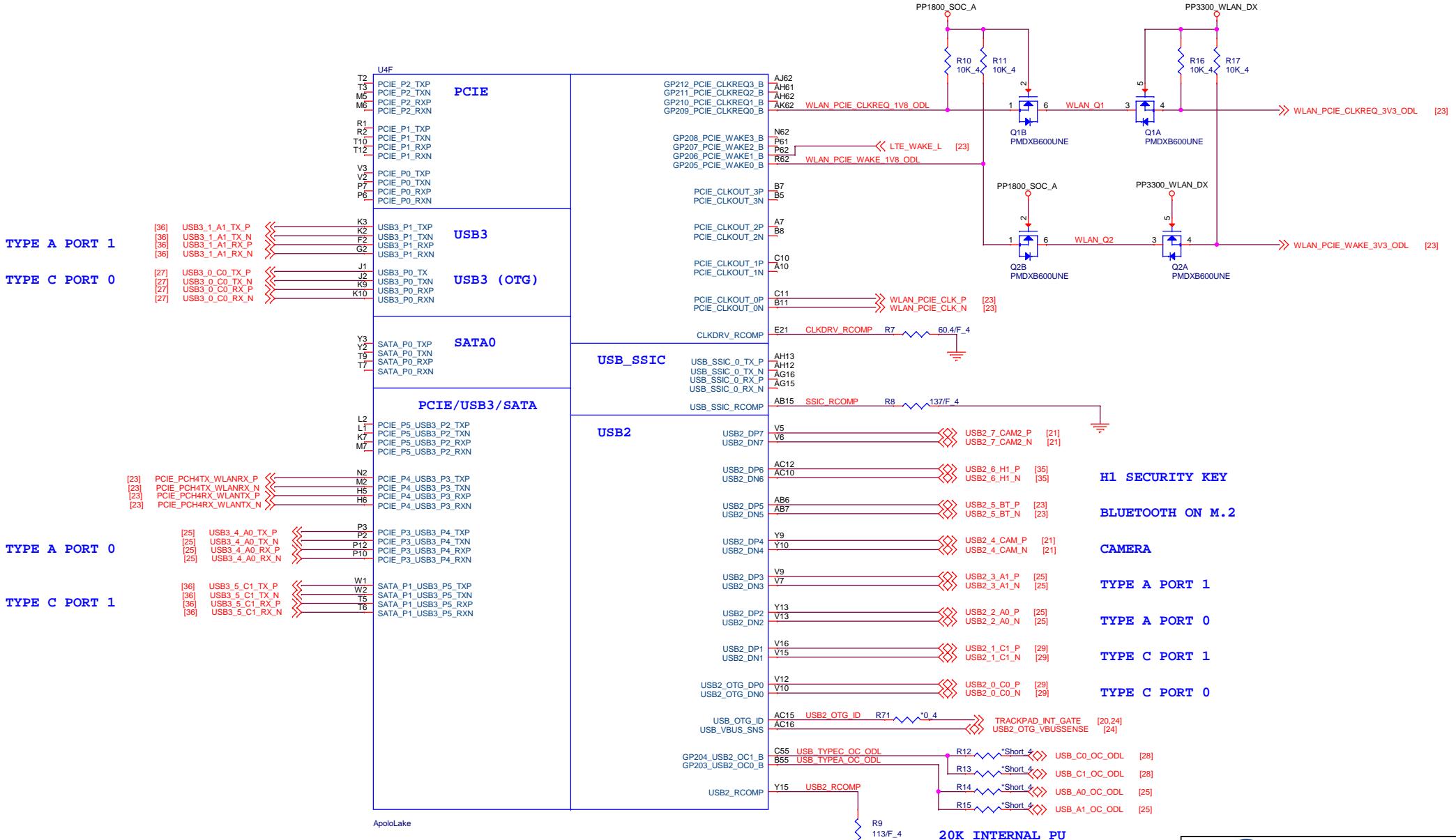
PCB 6L STACK UP

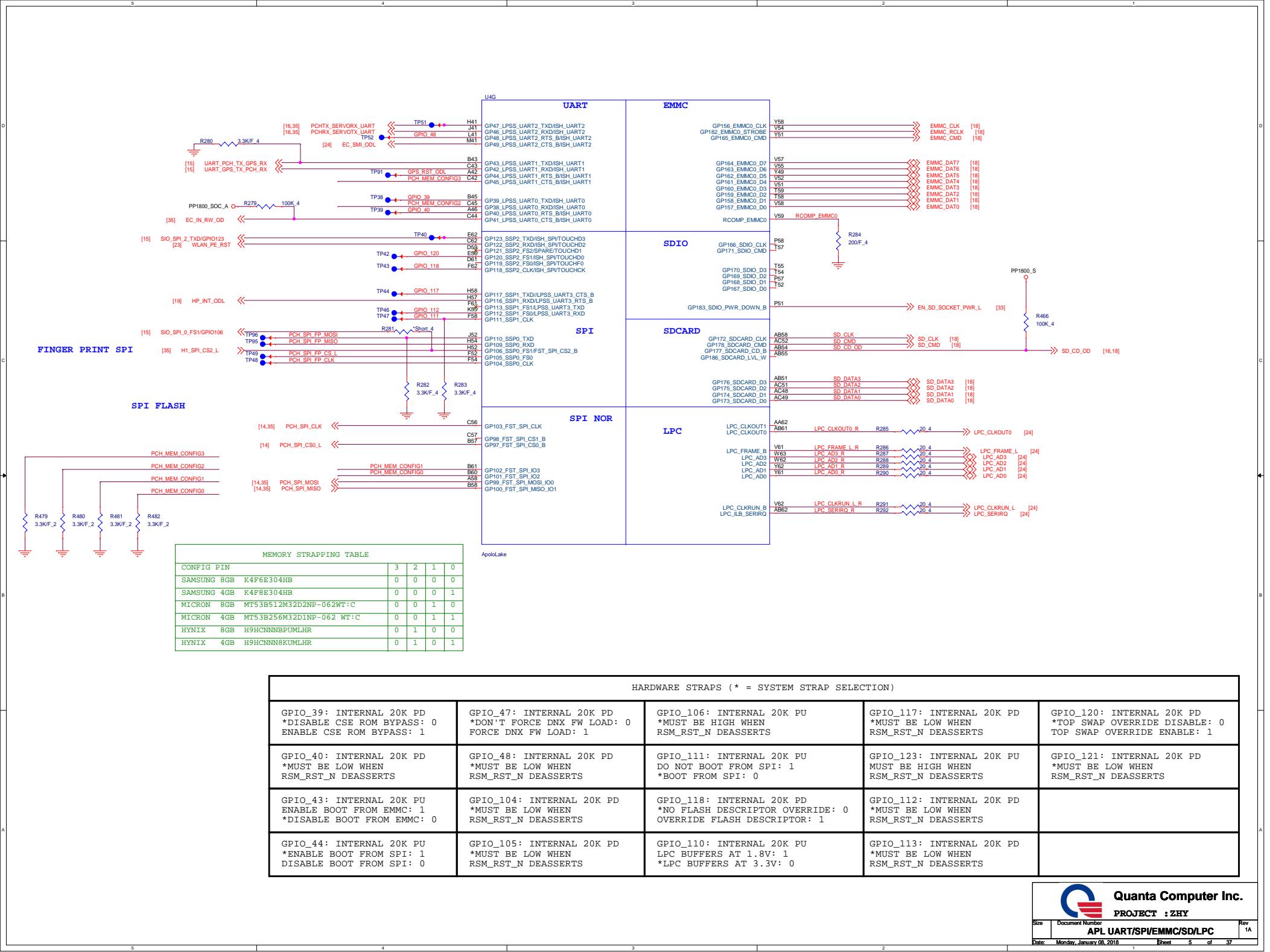
LAYER 1 : TOP
 LAYER 2 : VCC
 LAYER 3 : IN1
 LAYER 4 : IN2
 LAYER 5 : GND
 LAYER 6 : BOT

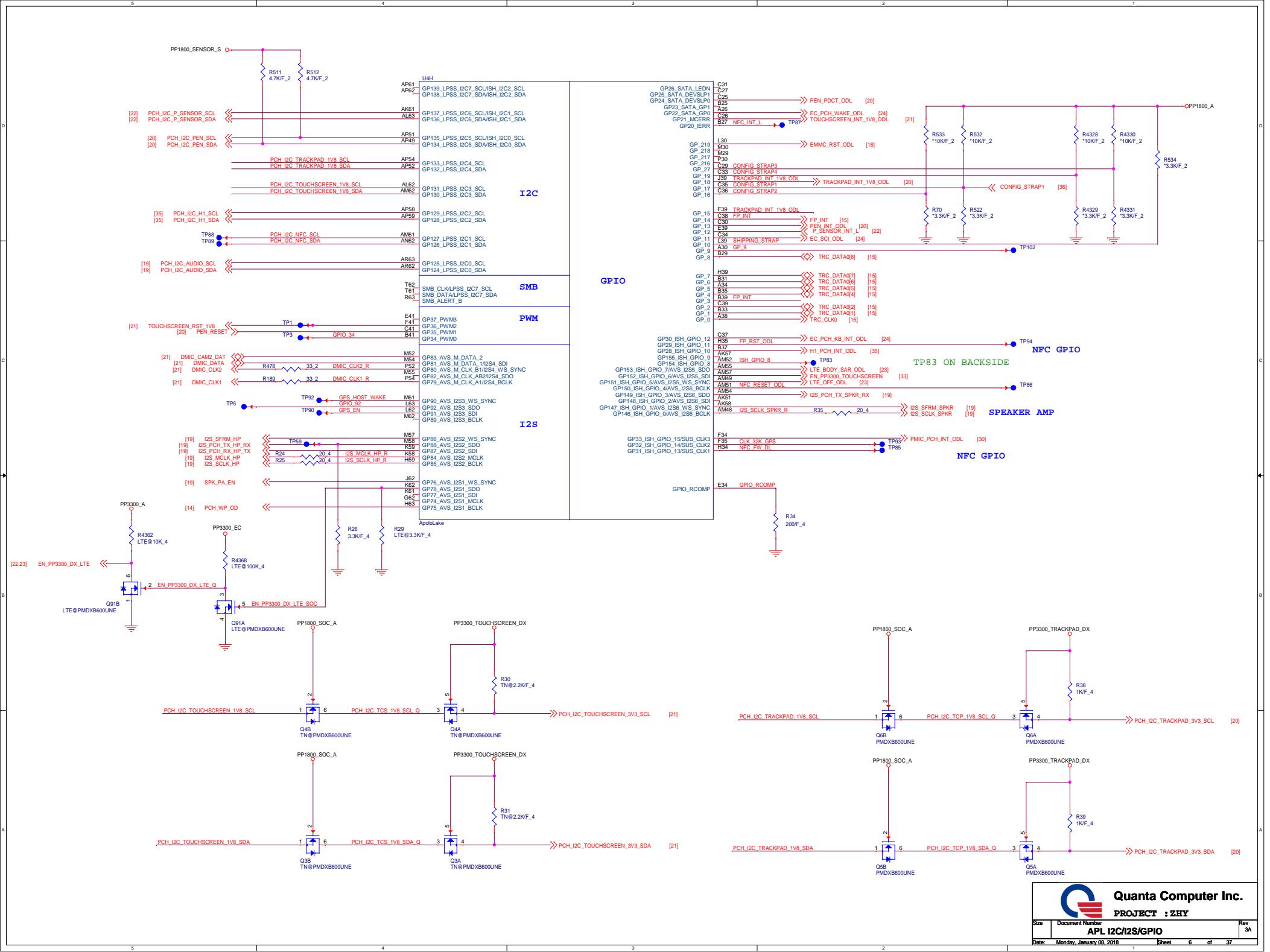
BOM value option		
PEN@	Digitizer (For Prato BTO)	
SV@	Debug header	
LTE@	LTE (For Pineza BTO)	
PSR@	P-sensor (*default non)	
SW@	Power button & convertible KB (For convertible only)	Either one
NSW@	Clamshell KB (For clamshell only)	
2CM@	2nd camera (For Prato BTO)	
KBL@	KB backlight (For Ely Yes, Eeklo BTO)	
GS@	G-sensor (For convertible only)	
TN@	Touchscreen (For convertible Yes, clamshell BTO)	
GY@	Gyro (For convertible only)	
ZAL@	For Astronaut only (acoustic noise)	

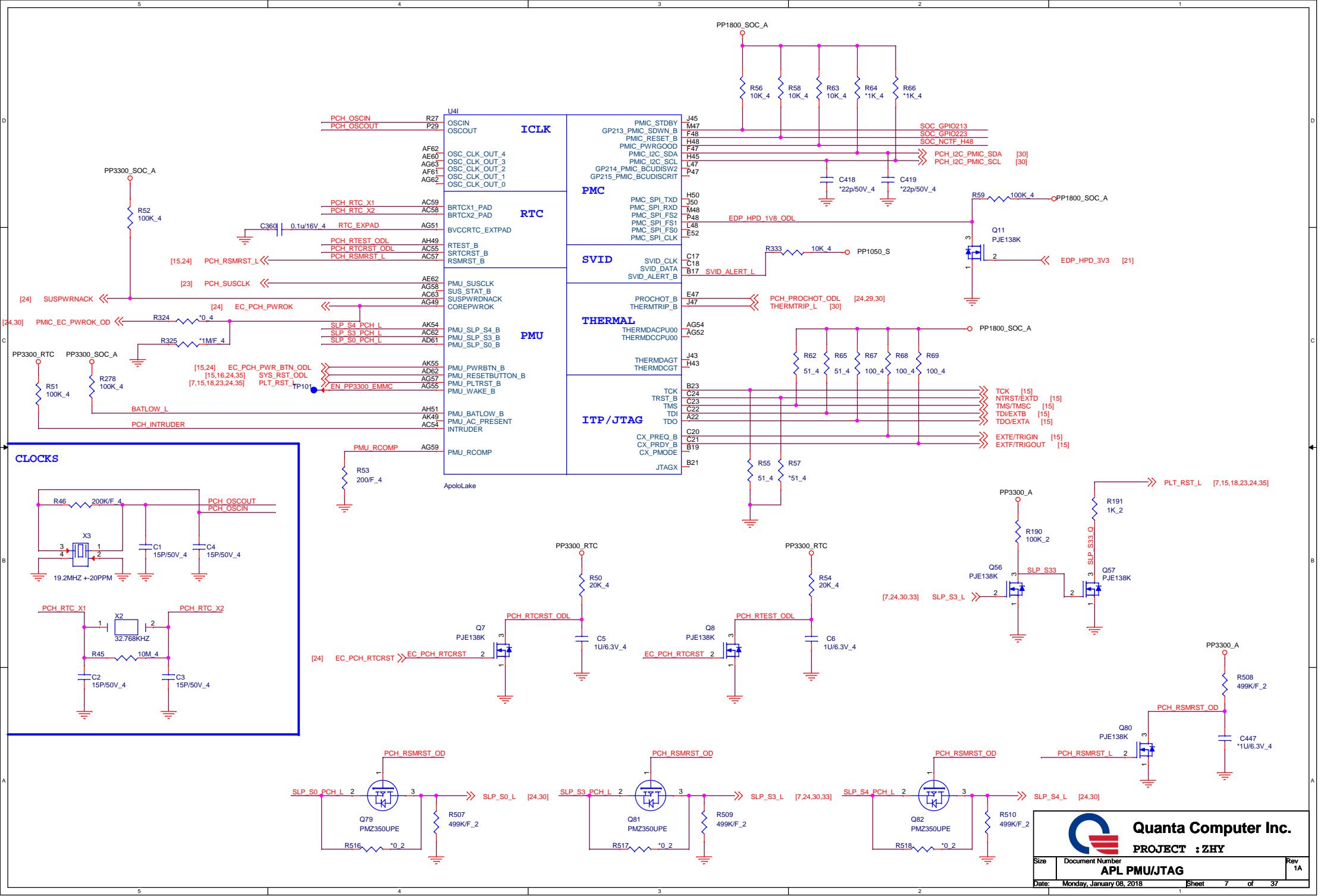
*11" and 15" USB3 TYPE-A connector and audio jack are different

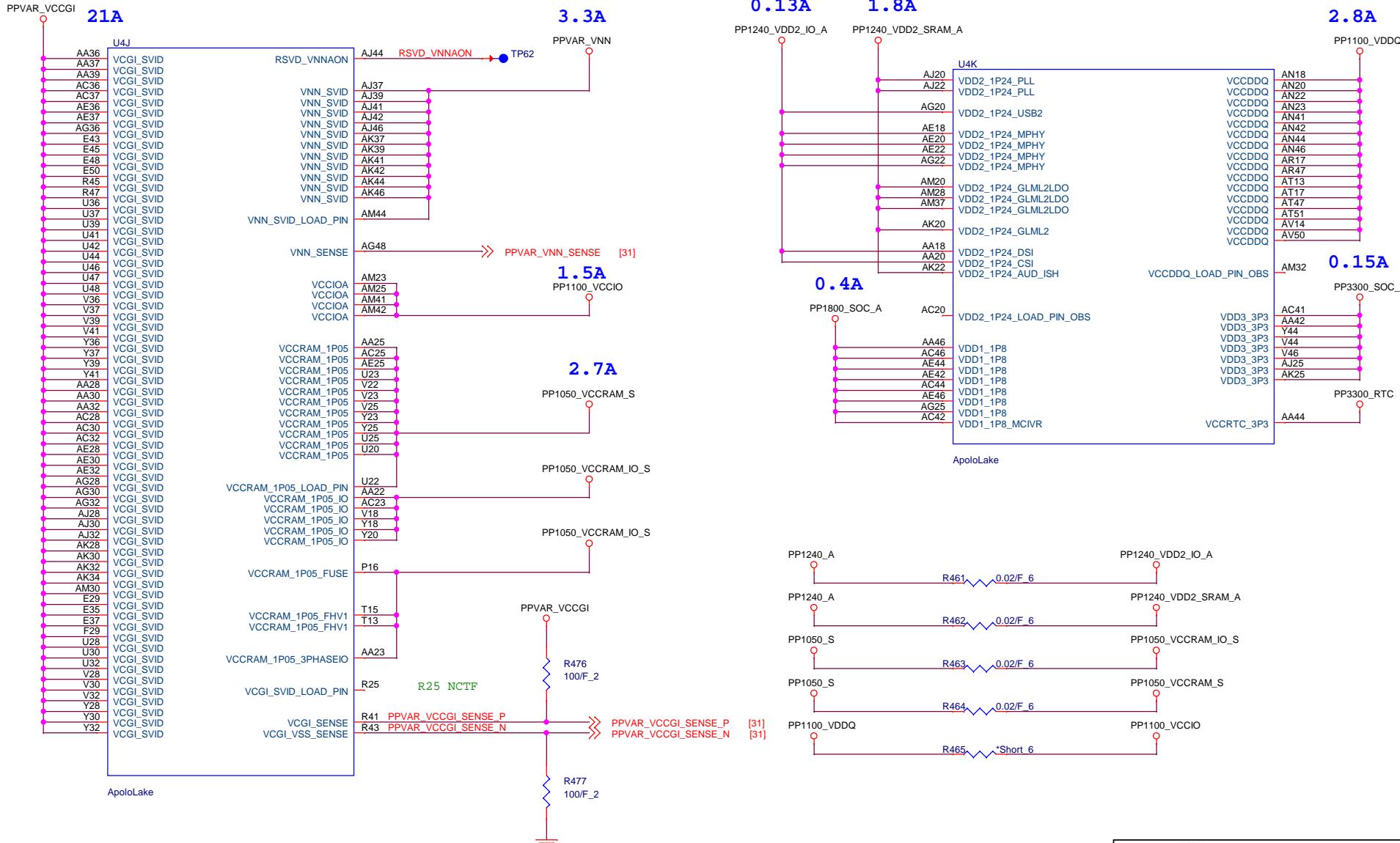


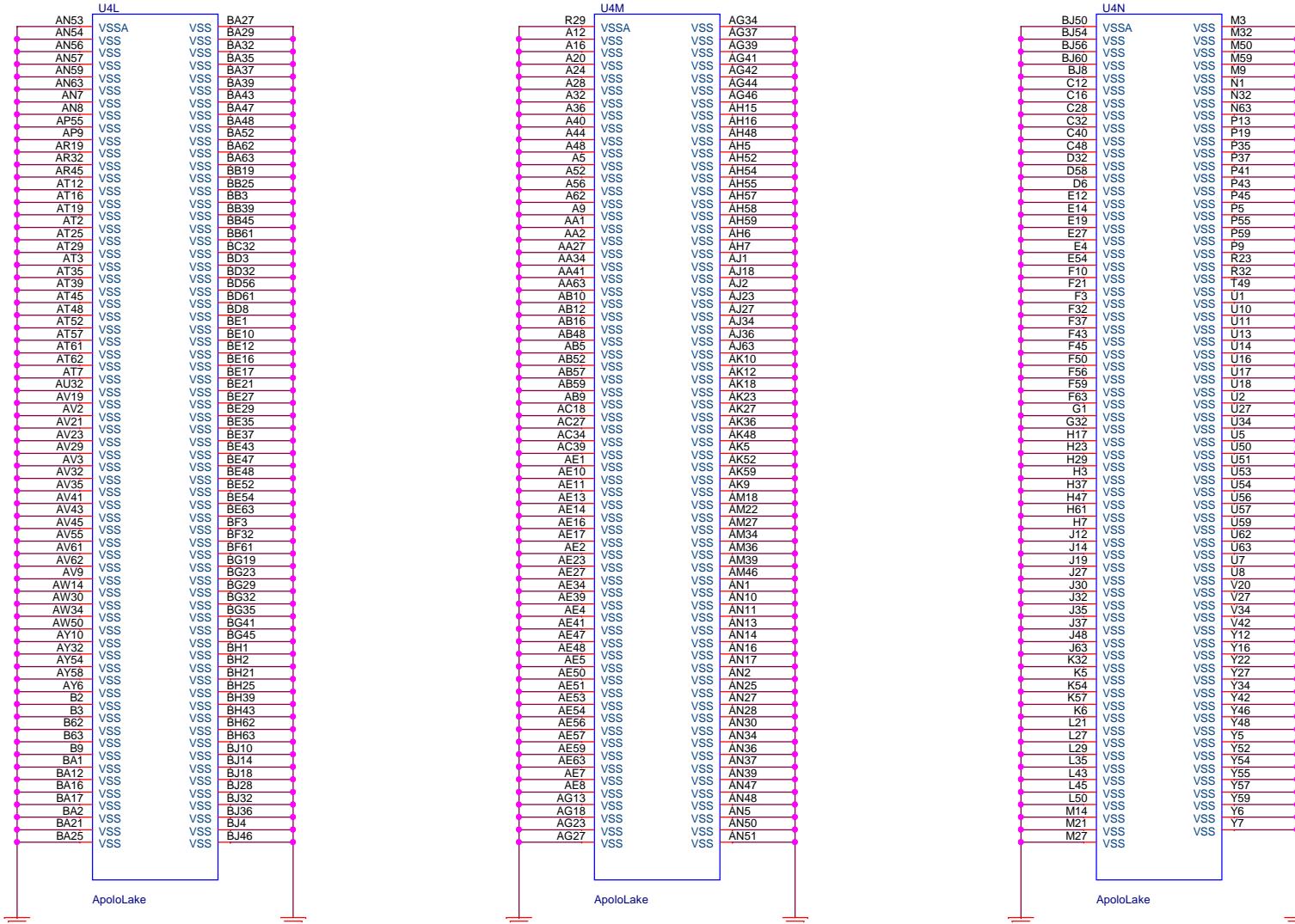


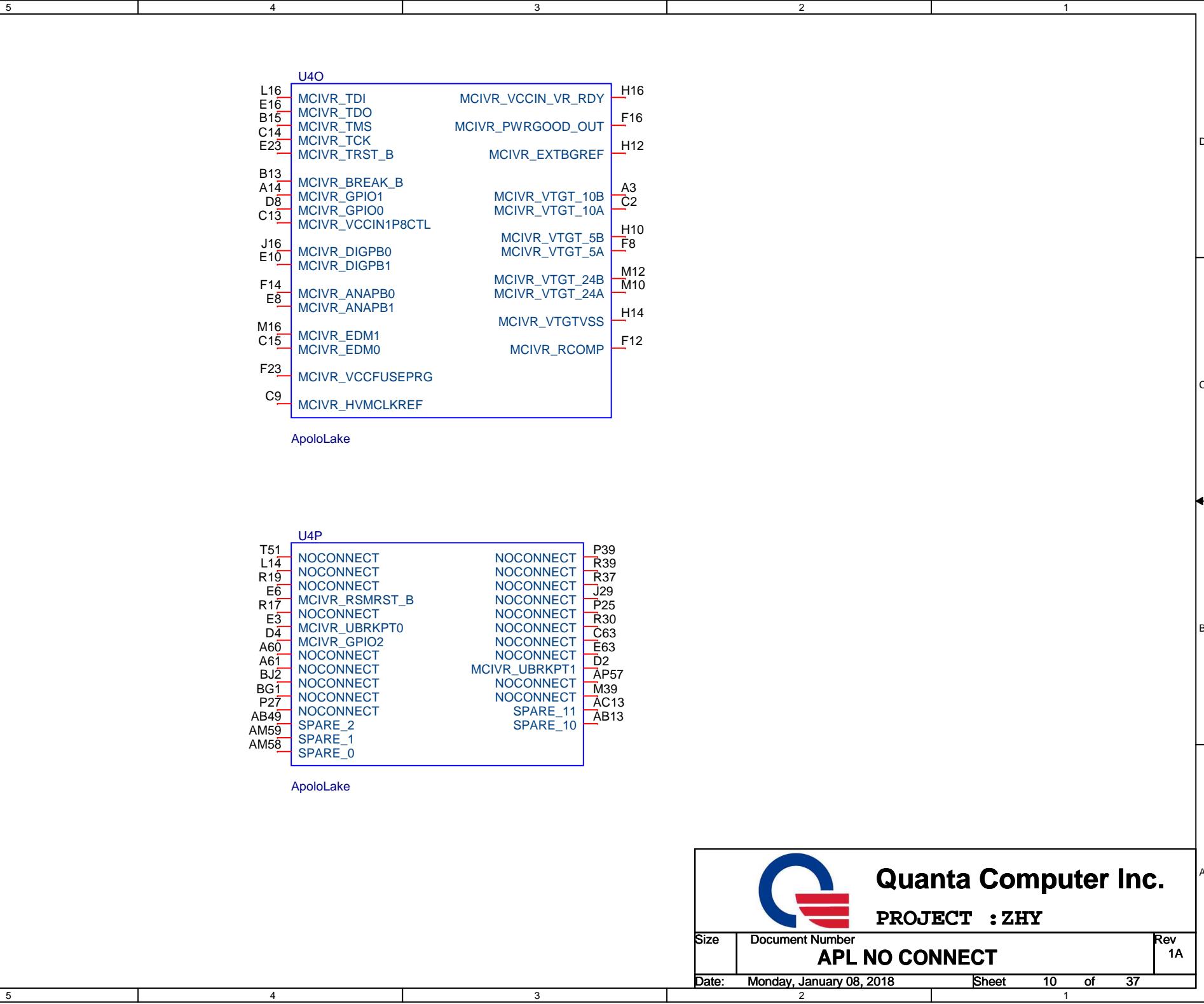


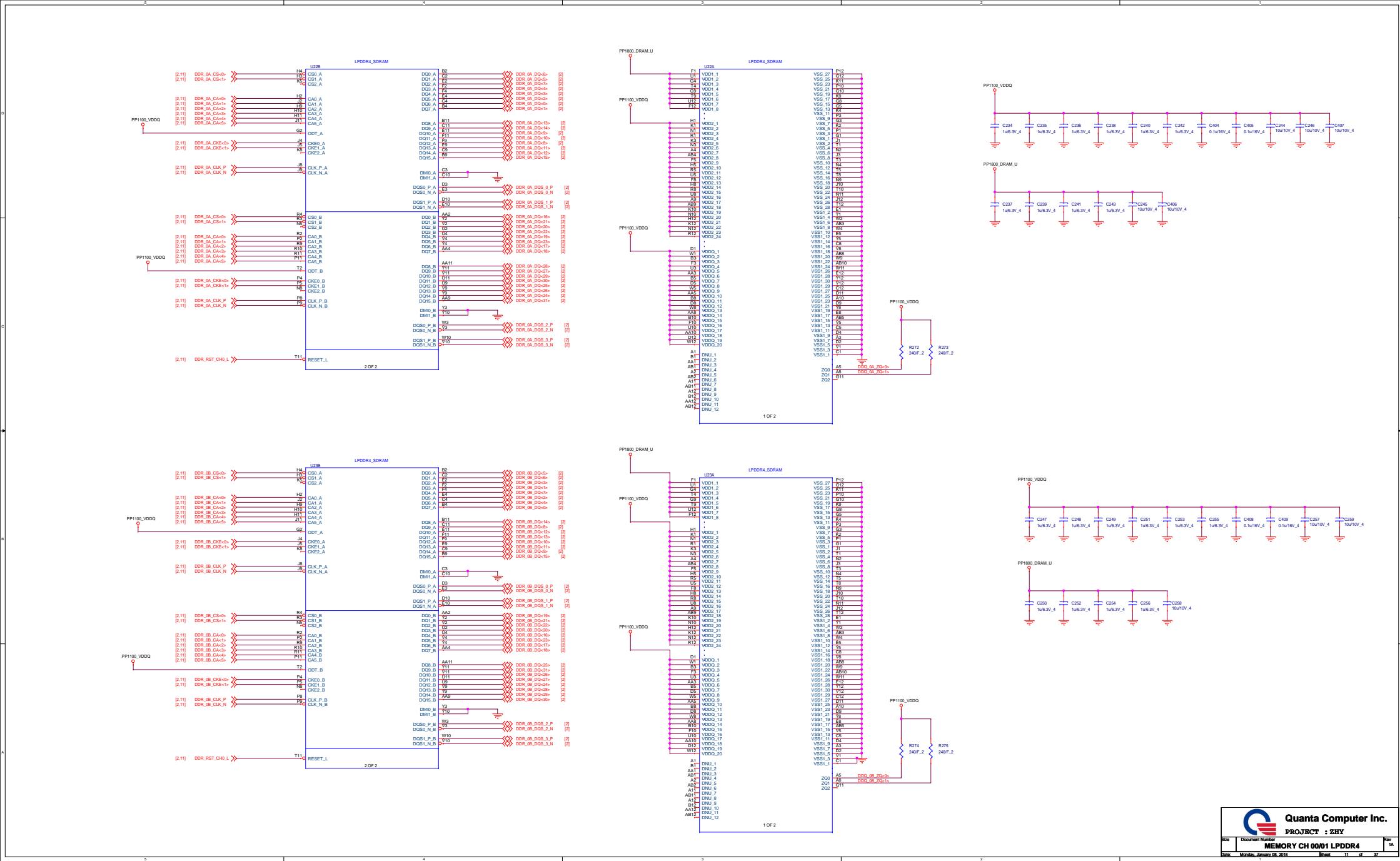


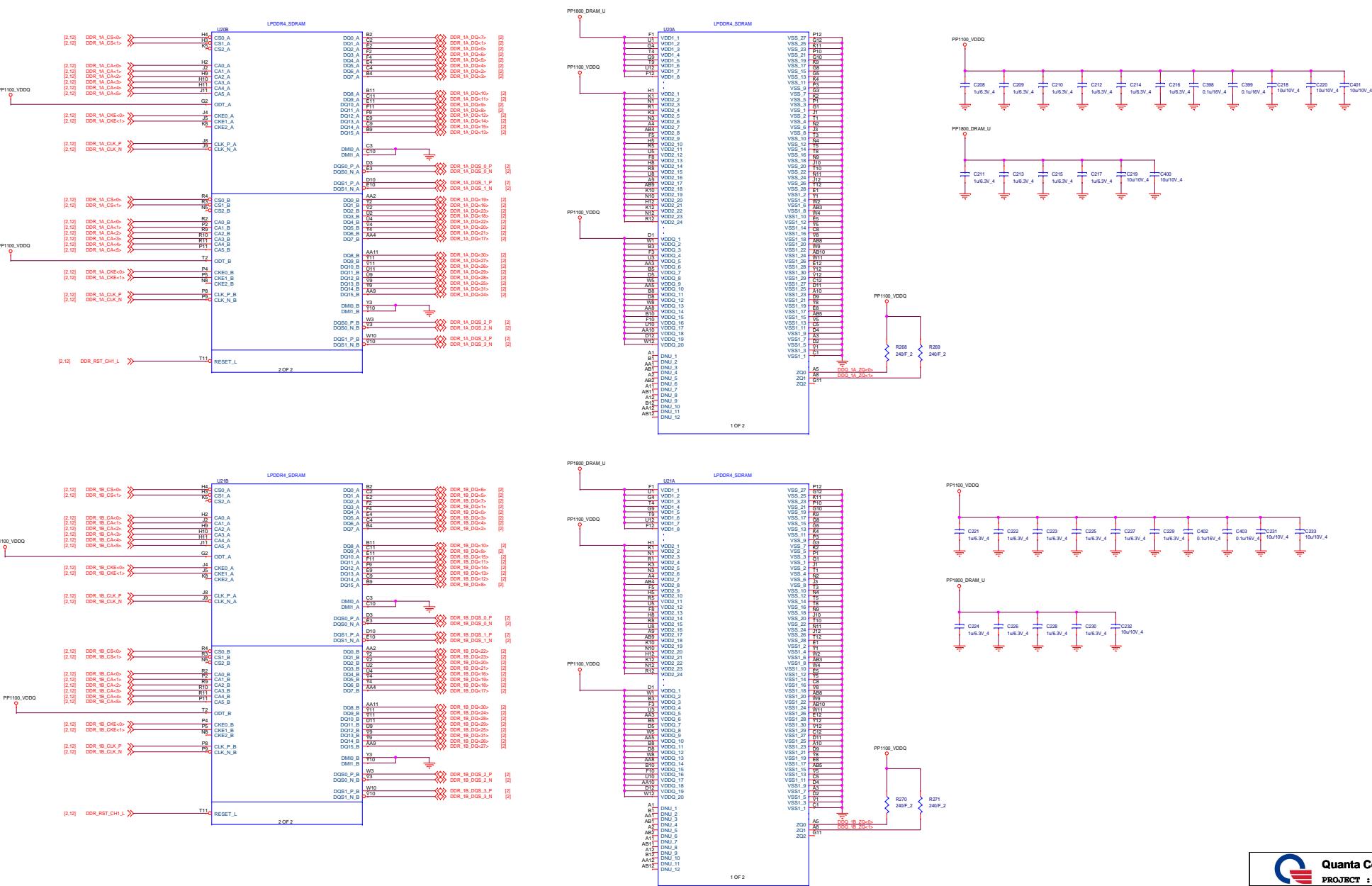






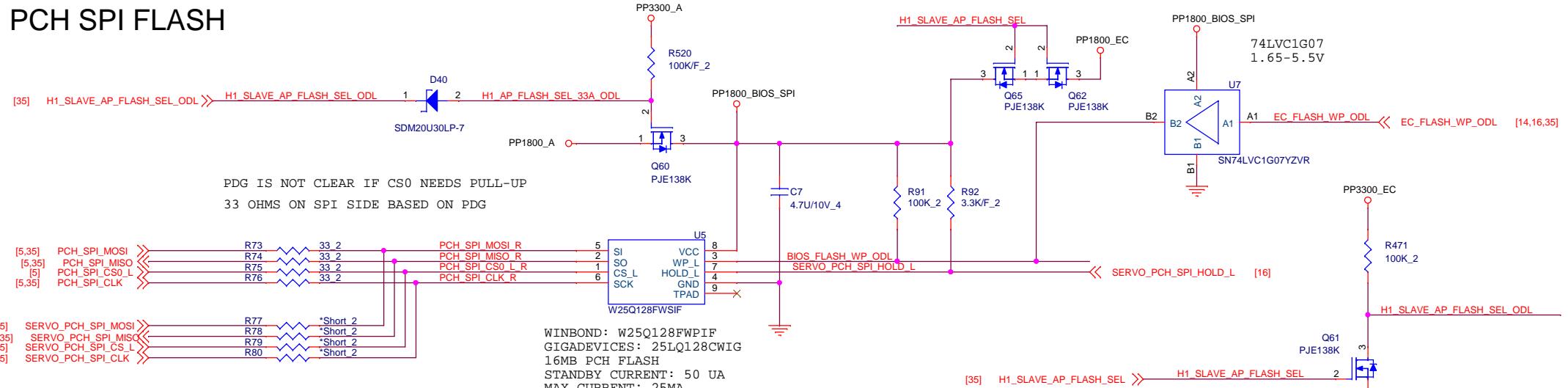




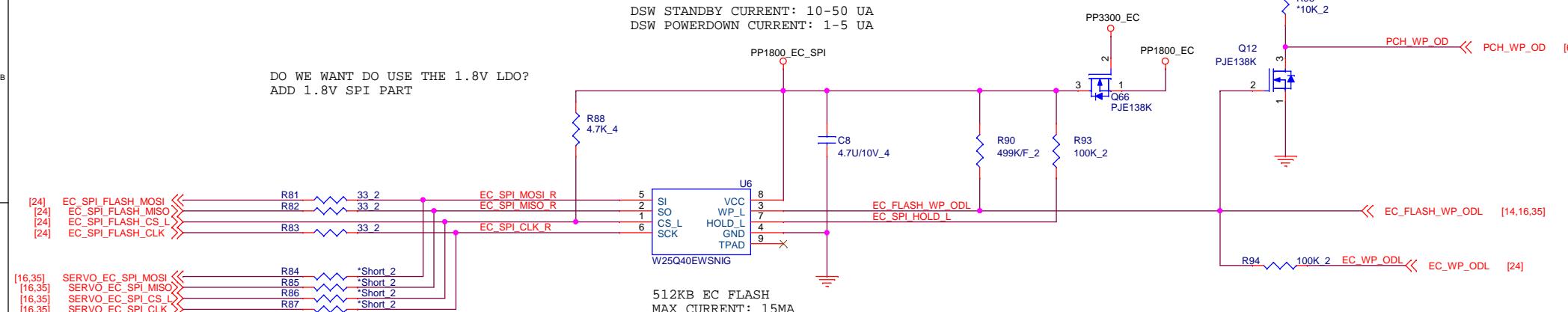




PCH SPI FLASH



EC SPI FLASH

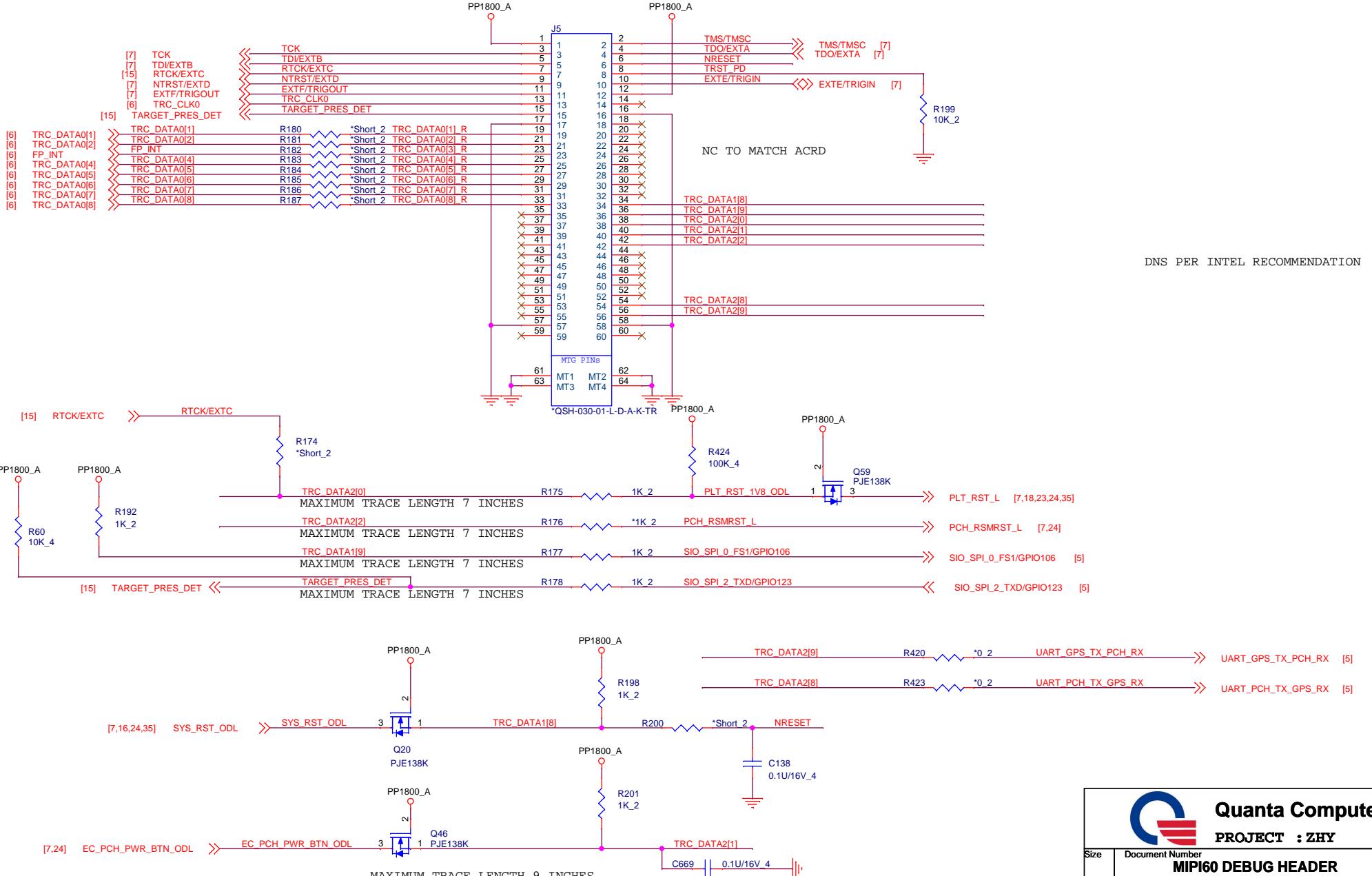


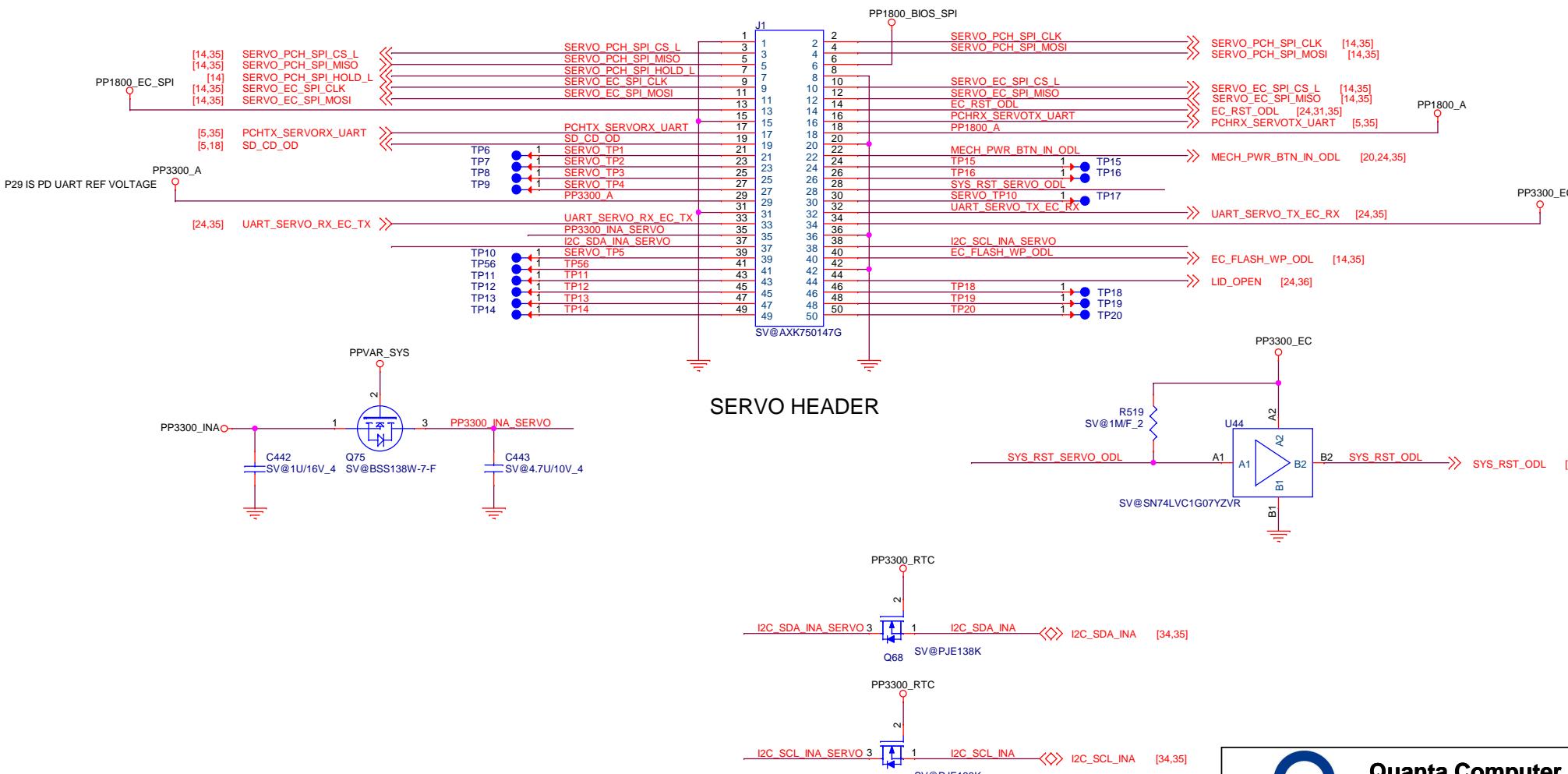
Quanta Computer Inc.

PROJECT : ZHY

Size	Document Number	Rev
		1A
	SPI ROM	

Date: Monday, January 08, 2018 Sheet 14 of 37





D

D

C

C

B

B

A

A



Quanta Computer Inc.

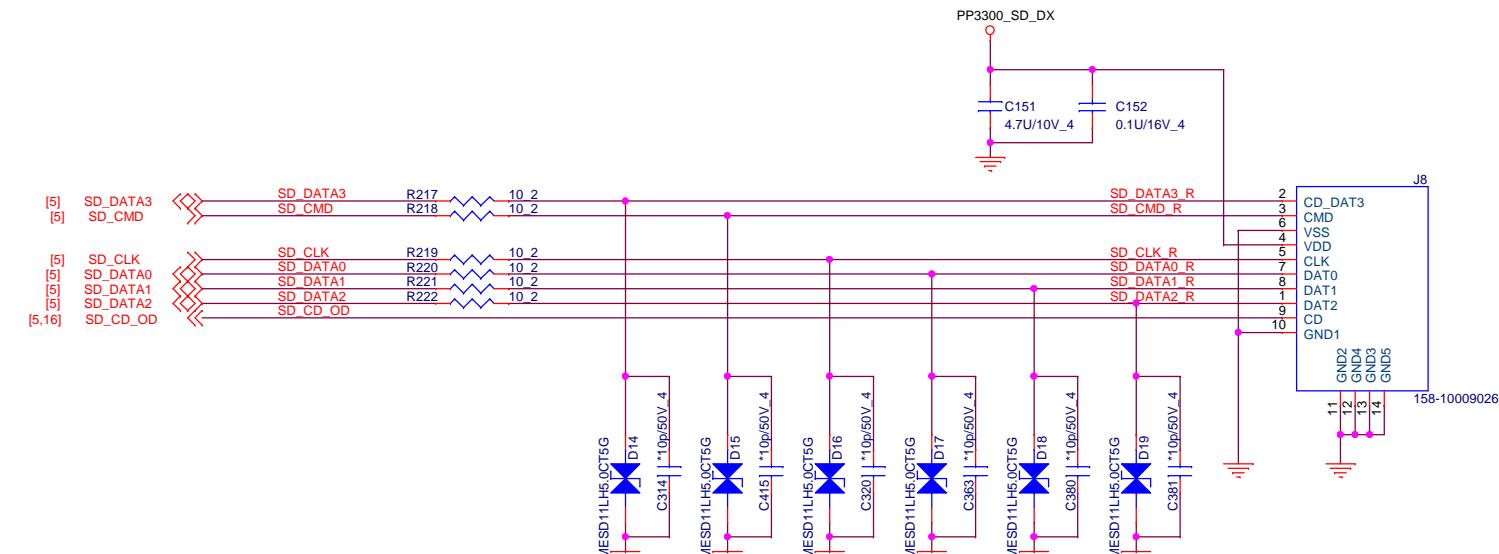
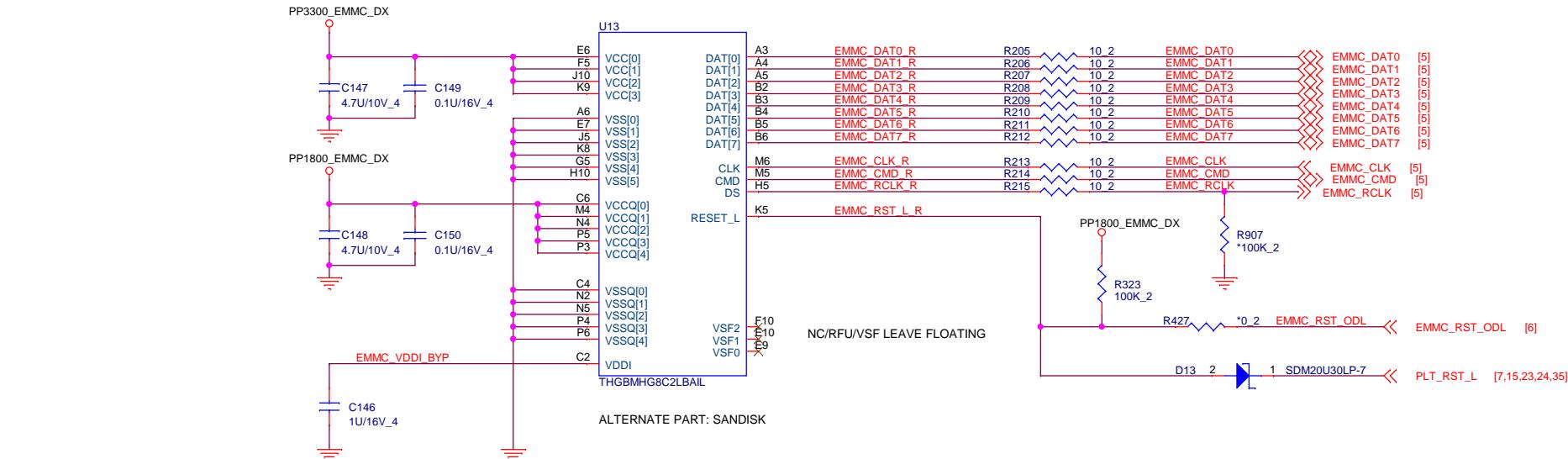
PROJECT : ZHY

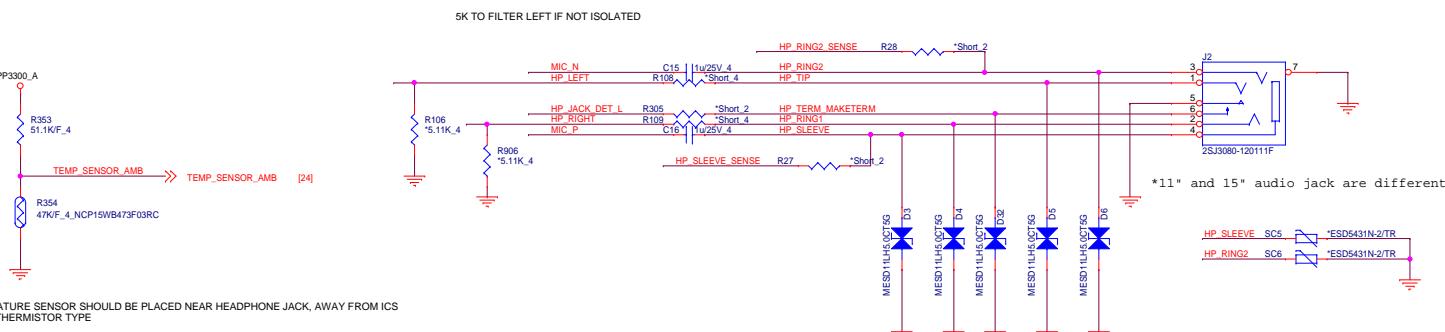
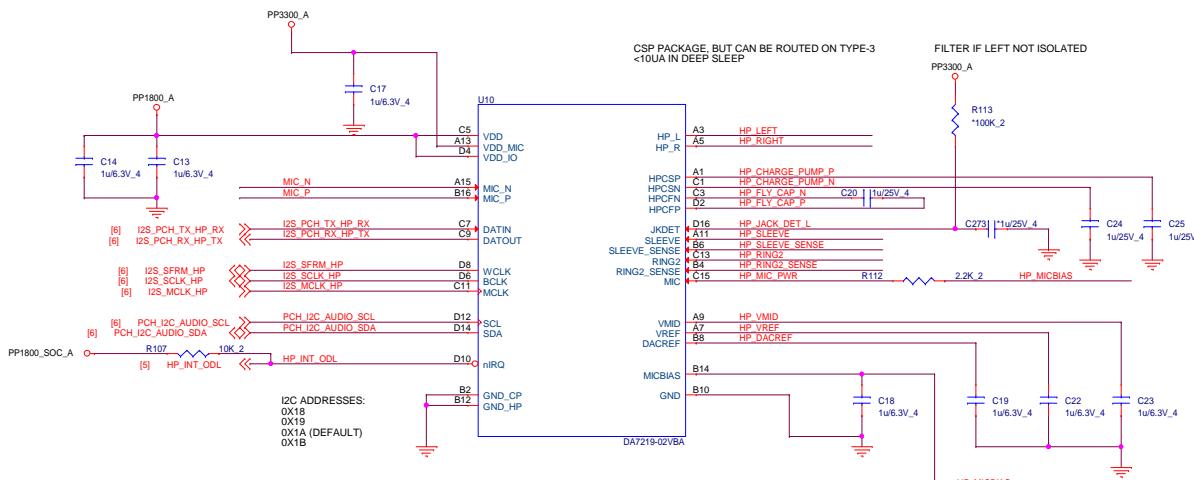
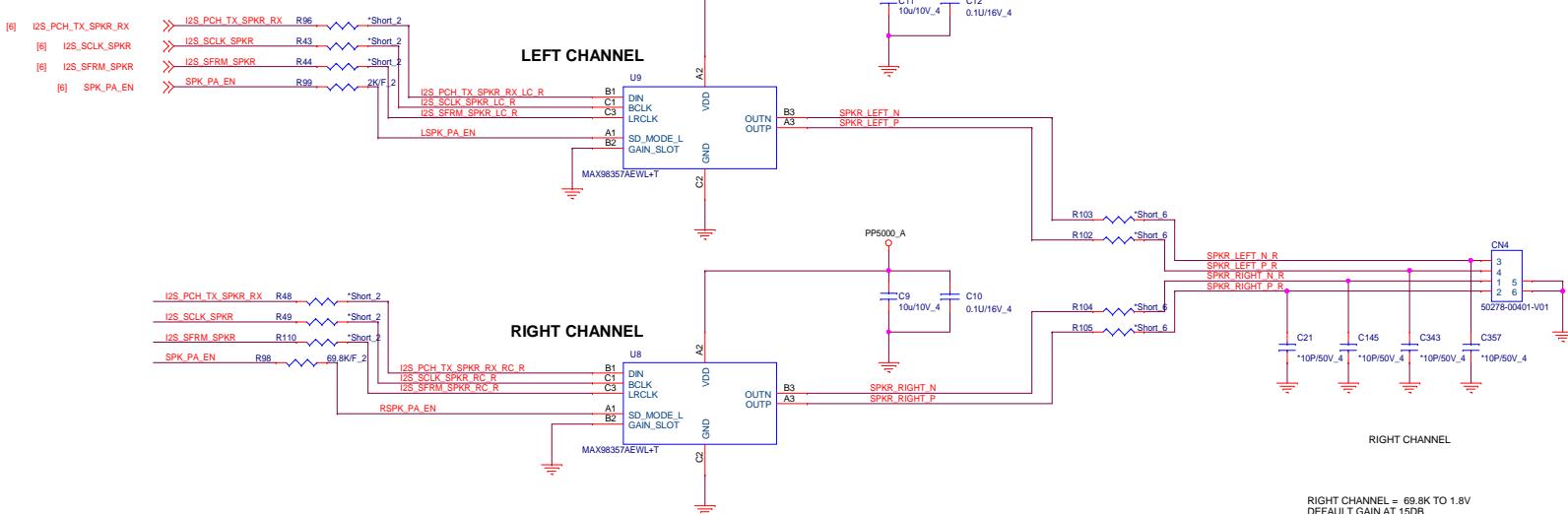
Size	Document Number	Rev
RESERVE		1A

Date: Monday, January 08, 2018 Sheet 17 of 37

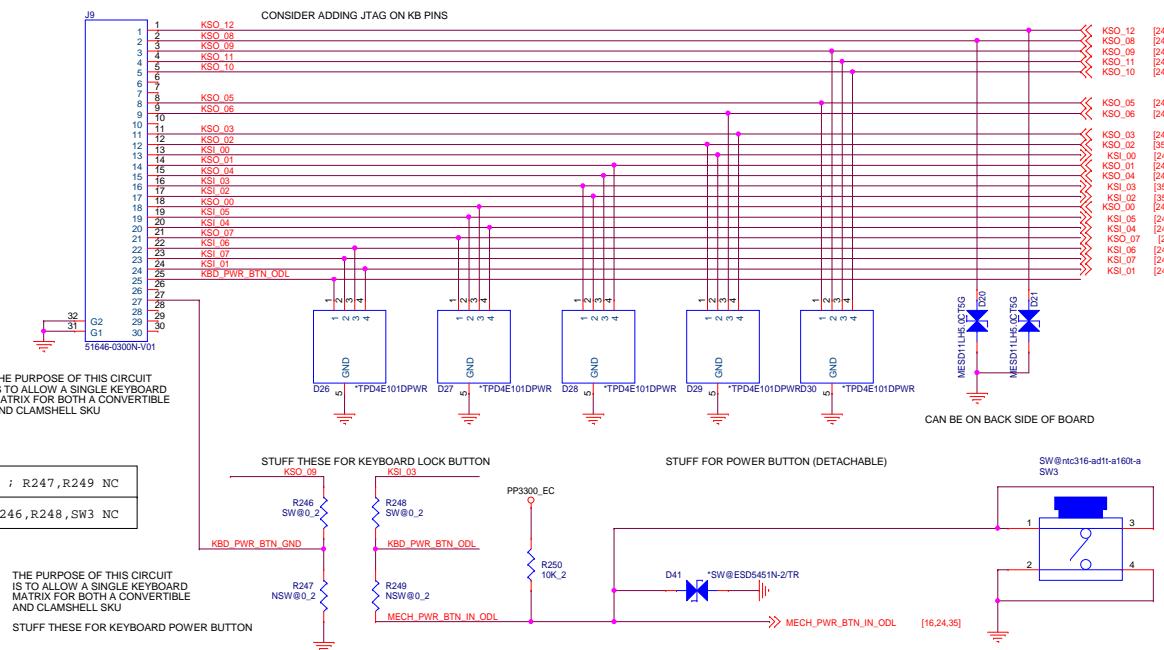
32 GB EMMC SD STORAGE

150 uA SLEEP CURRENT

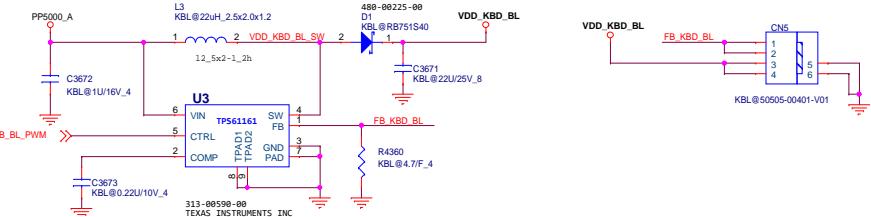




BSW REF DESIGN ERRATA MENTIONED
A LEAKAGE PROBLEM INTO THE EC
VIA THE KSO SIGNALS. THIS FIX WAS
TO ADD 100K PULLUPS TO 3P3A, EC
ON THE KSO SIGNALS
0C2A COMPATIBLE

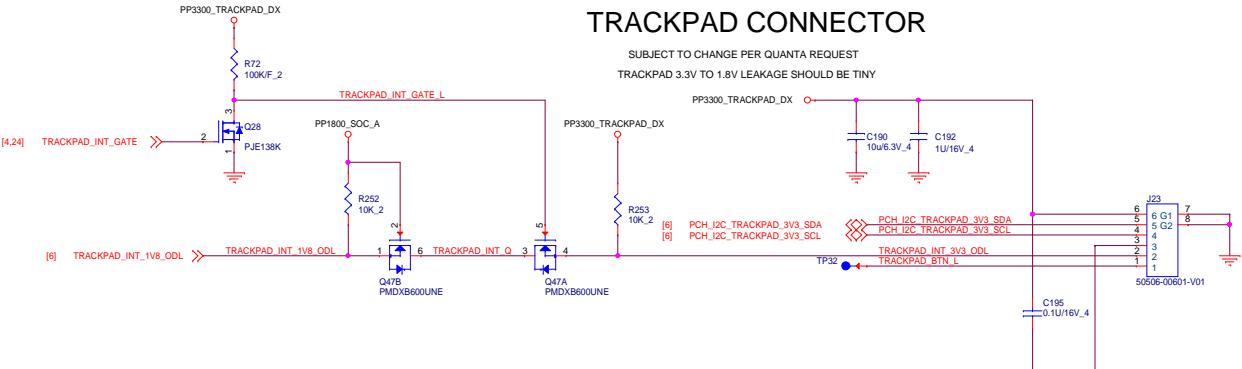


KB BACKLIGHT

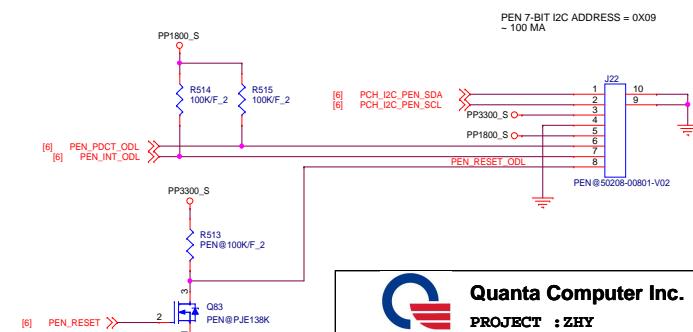


TRACKPAD CONNECTOR

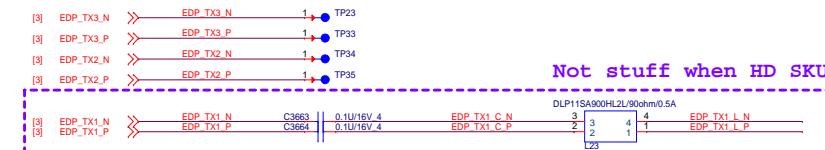
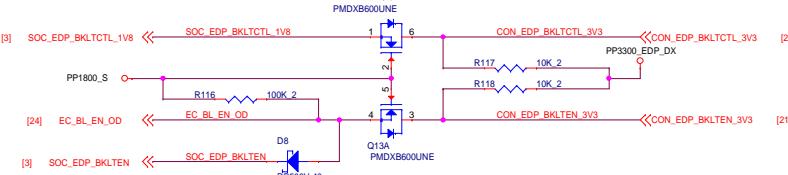
SUBJECT TO CHANGE PER QUANTA REQUEST
TRACKPAD 3.3V TO 1.8V LEAKAGE SHOULD BE TINY



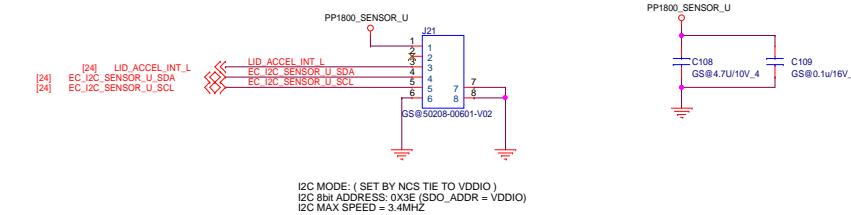
PEN CONNECTOR



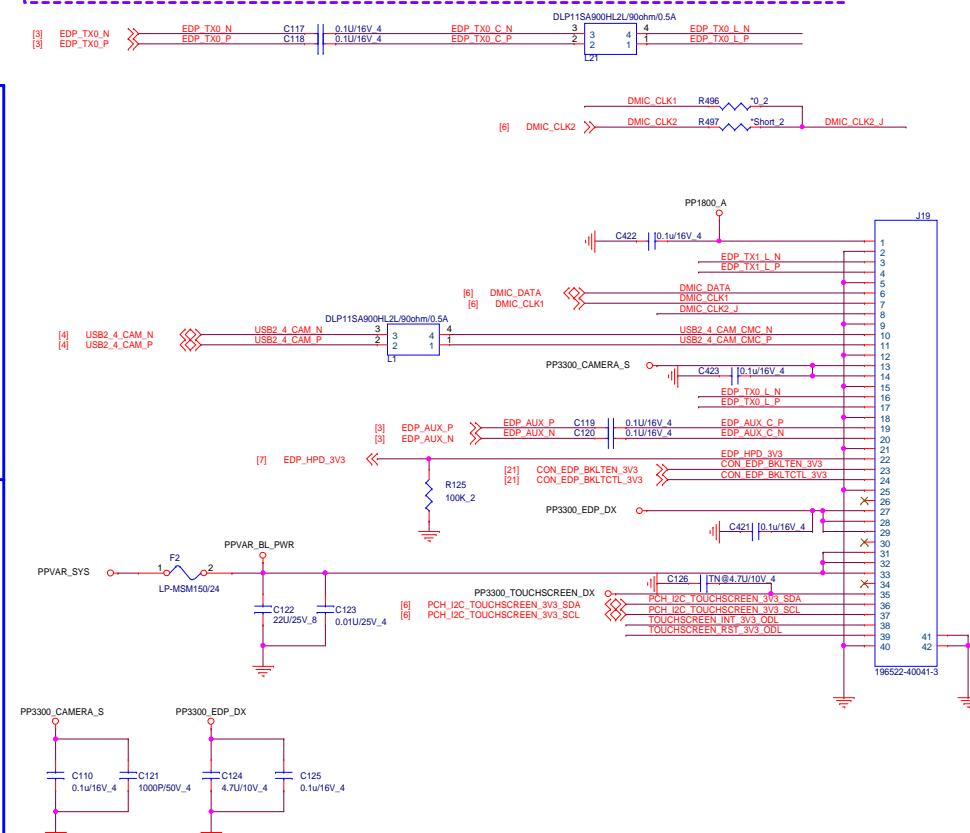
EDP2-EDP3 DOES NOT NEED TO ROUTE TO CONNECTOR



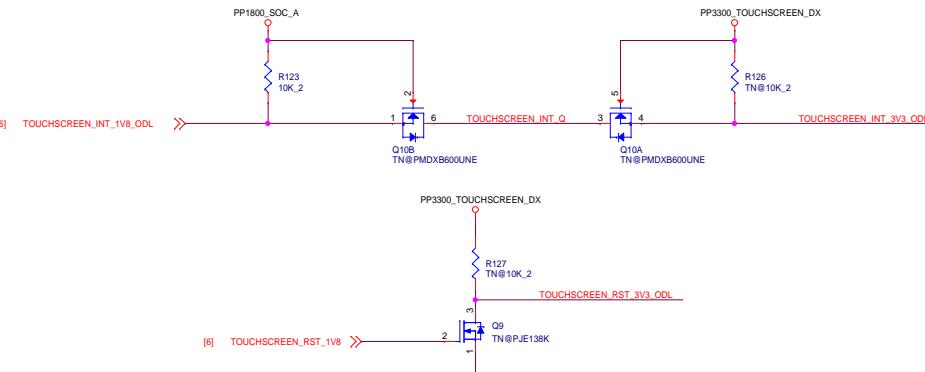
SENSOR BOARD (KX022)



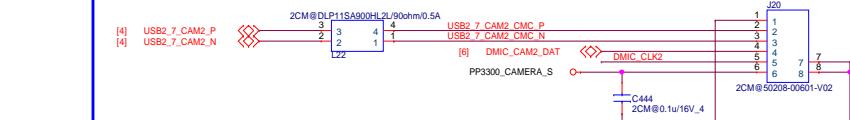
NOTE: SEE WHITE PAPER ON ACCEL PLACEMENT FOR CONVERTIBLES



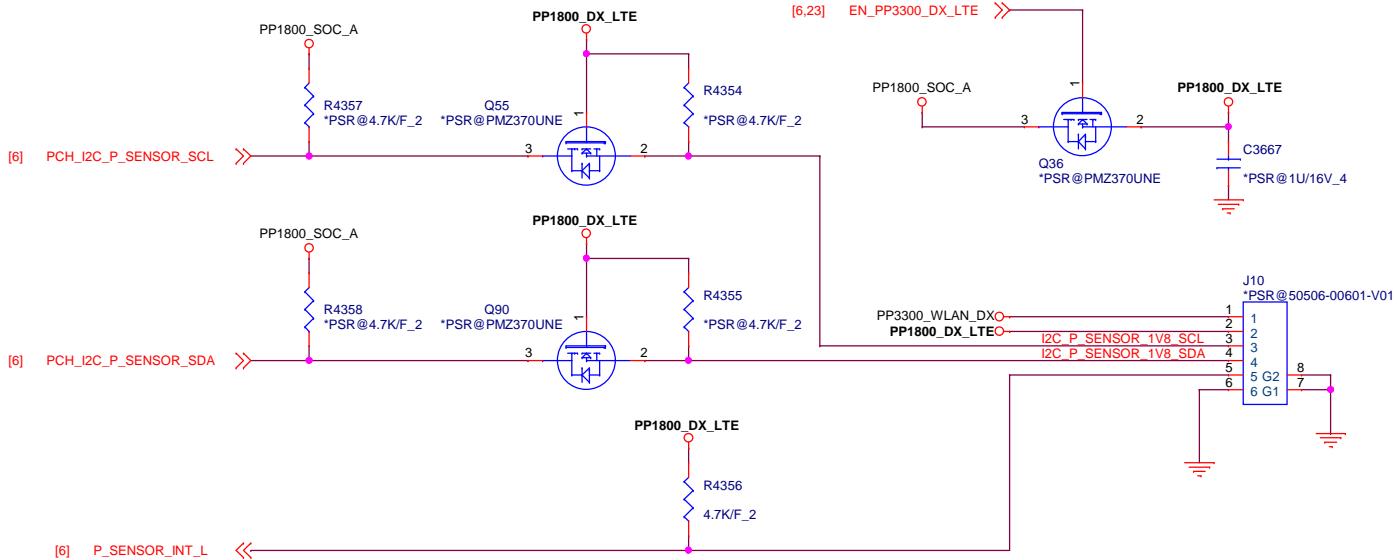
TOUCH SCREEN



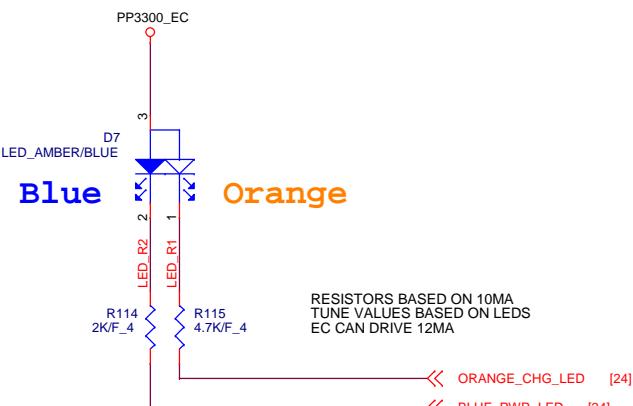
C-PANEL CAMERA



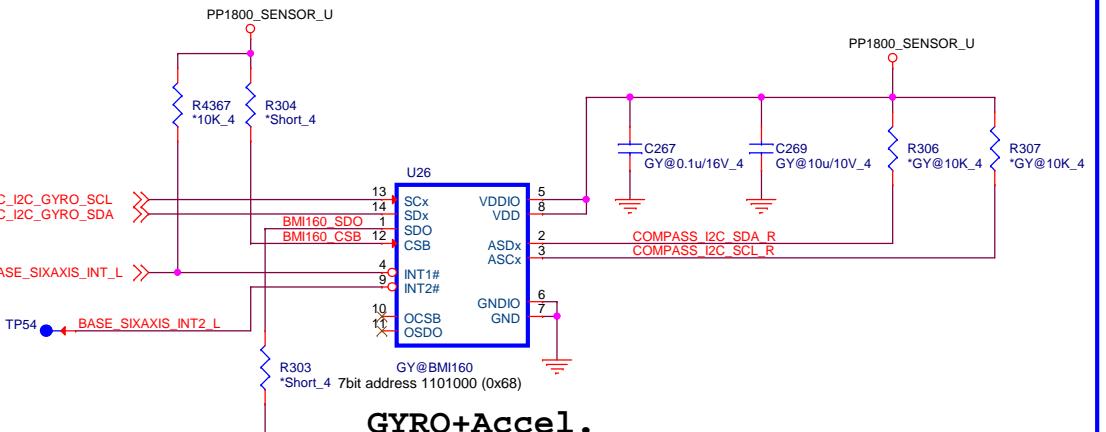
P-SENSOR



CHARGE/BATTERY LED

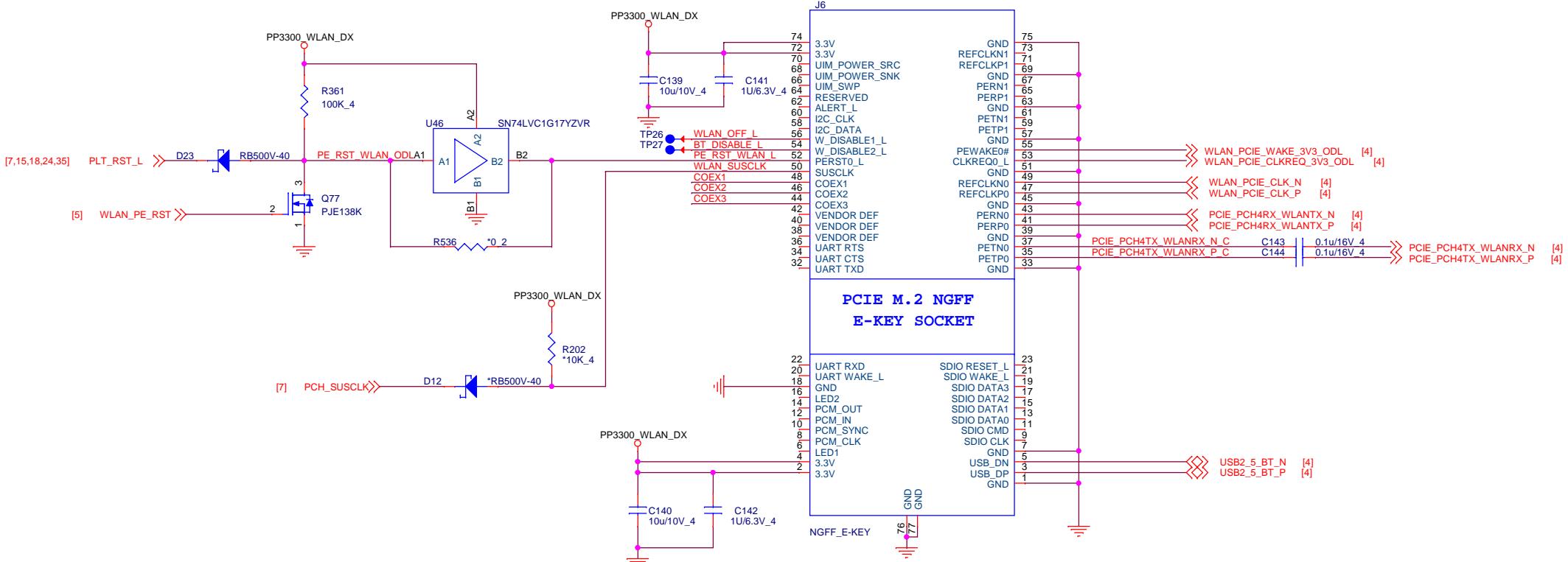


GYRO+Accel.

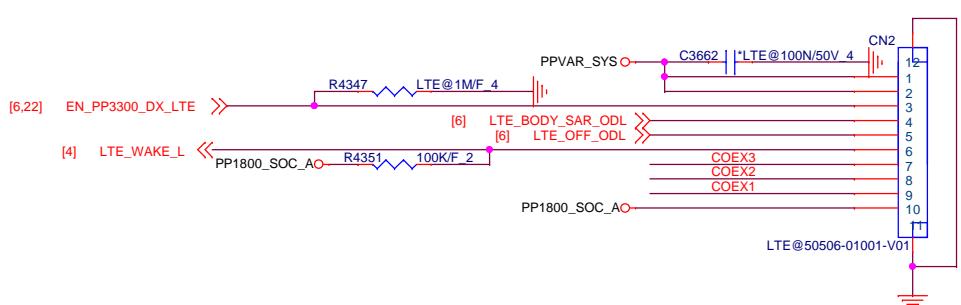


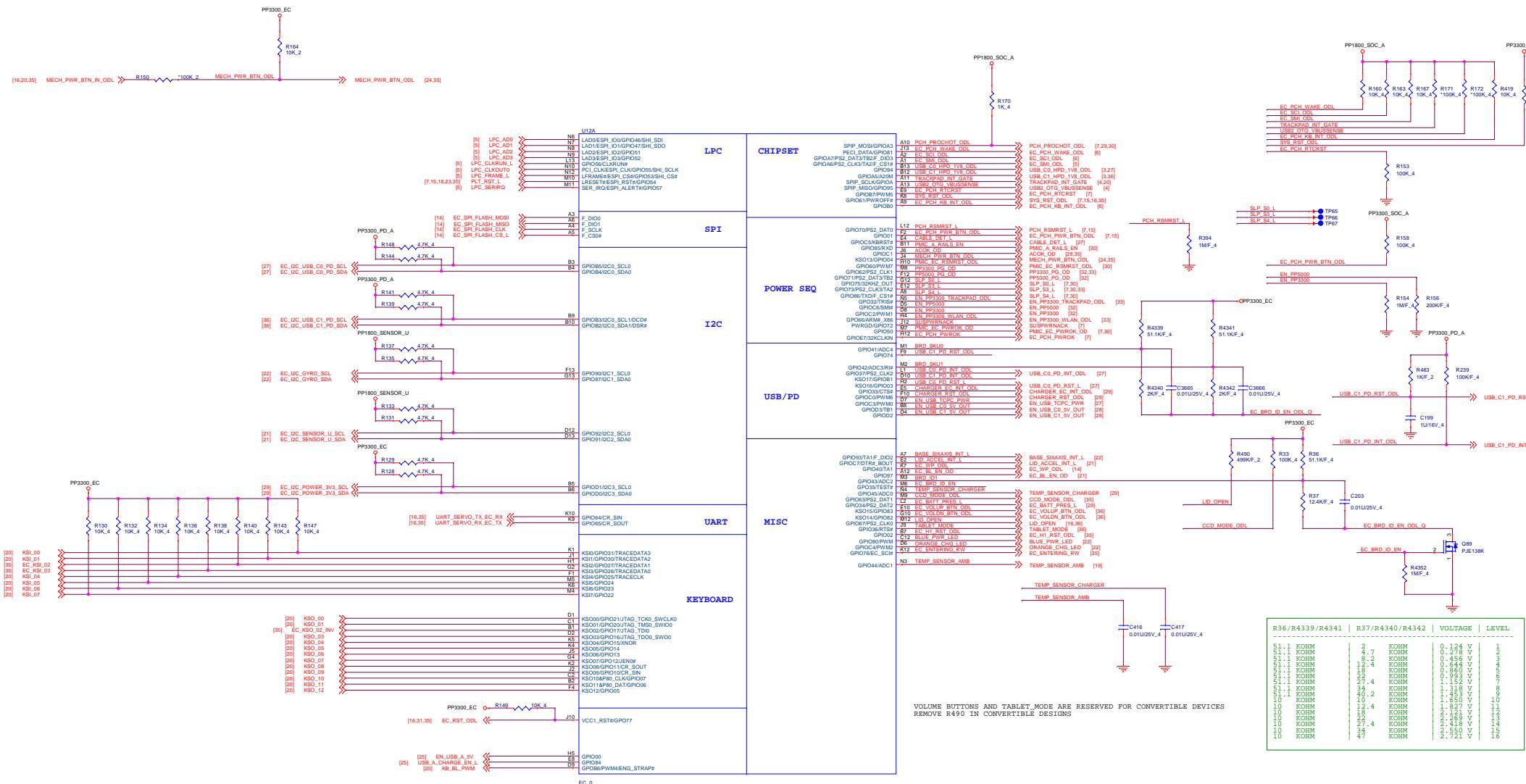
Quanta Computer Inc.
PROJECT : ZHY

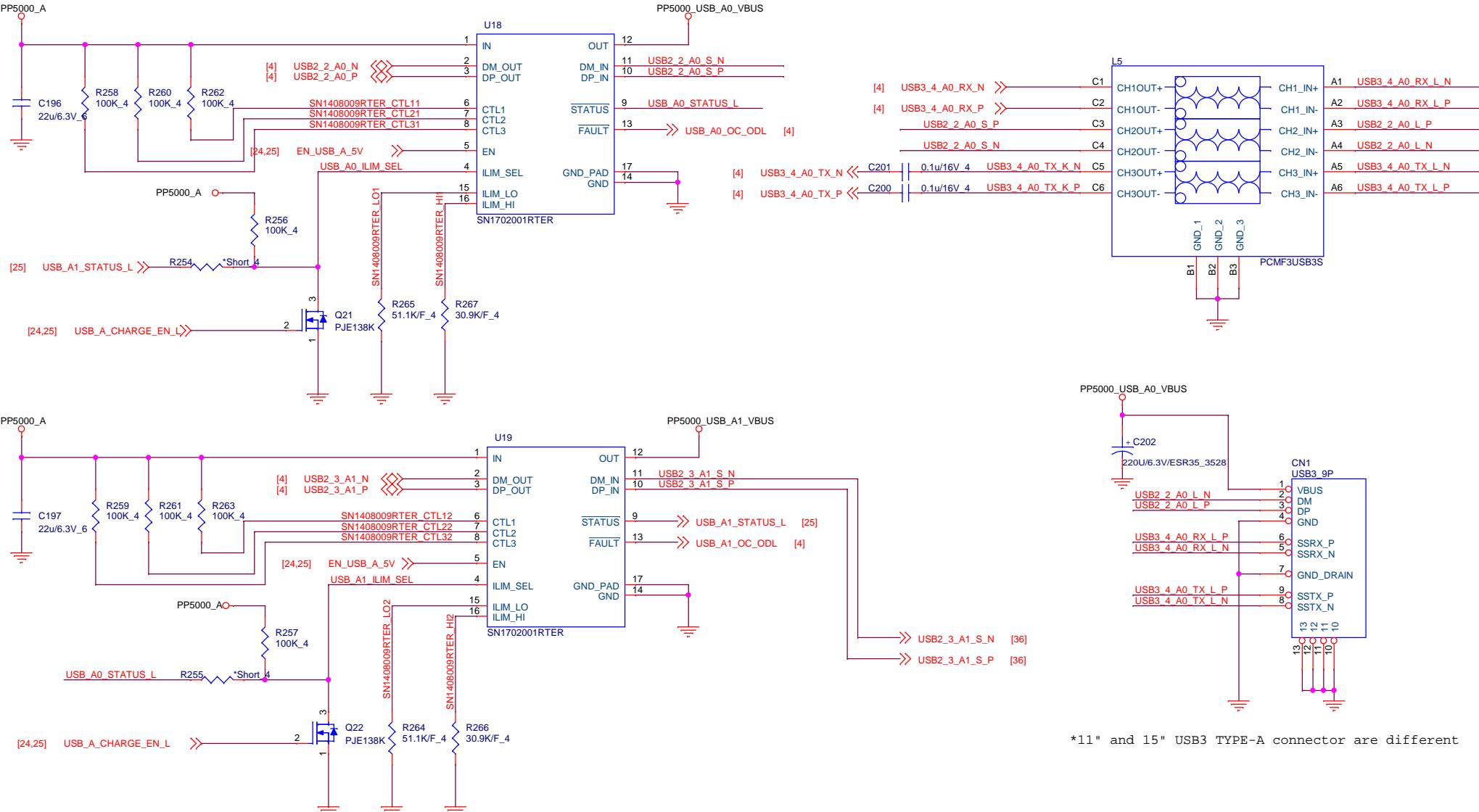
M.2 connector

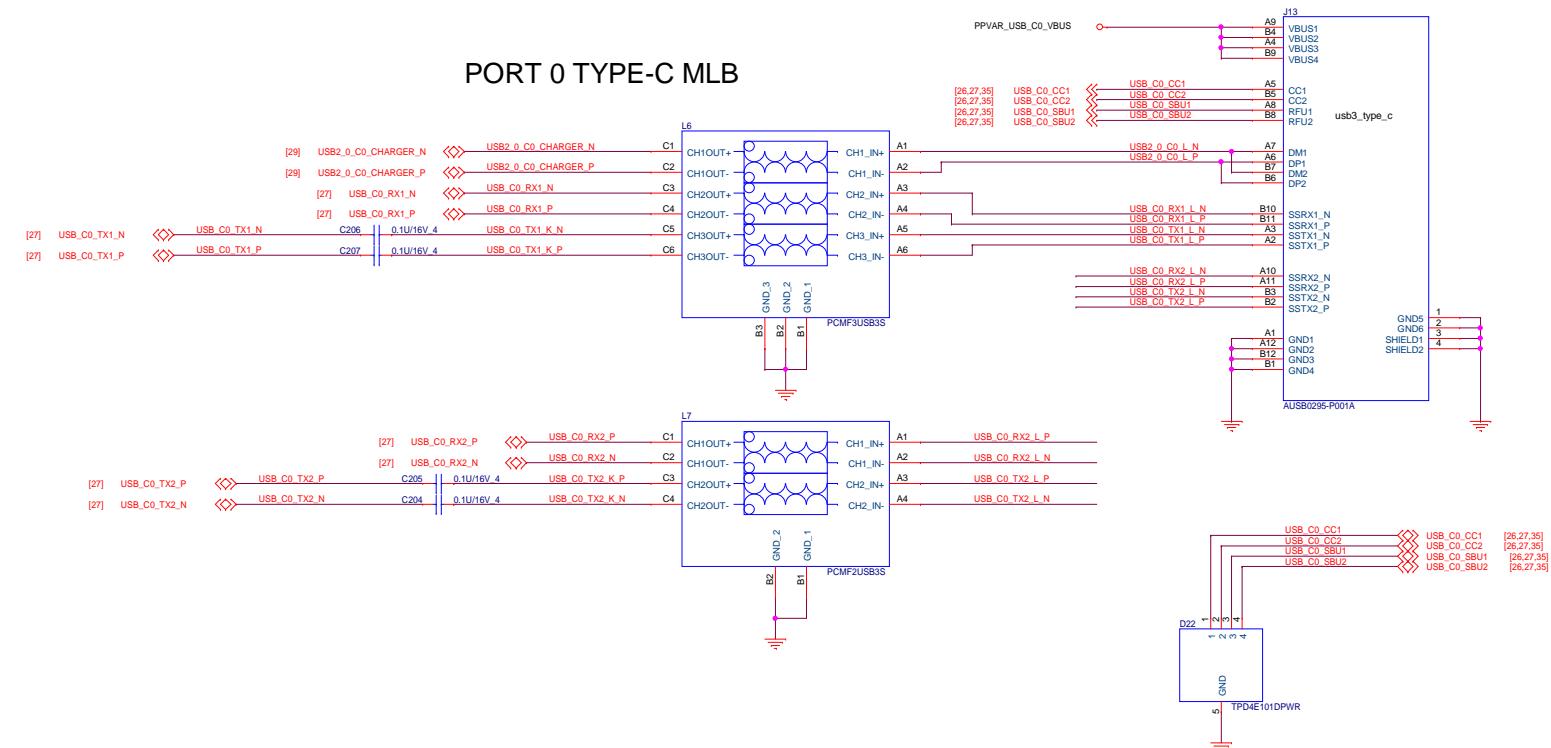


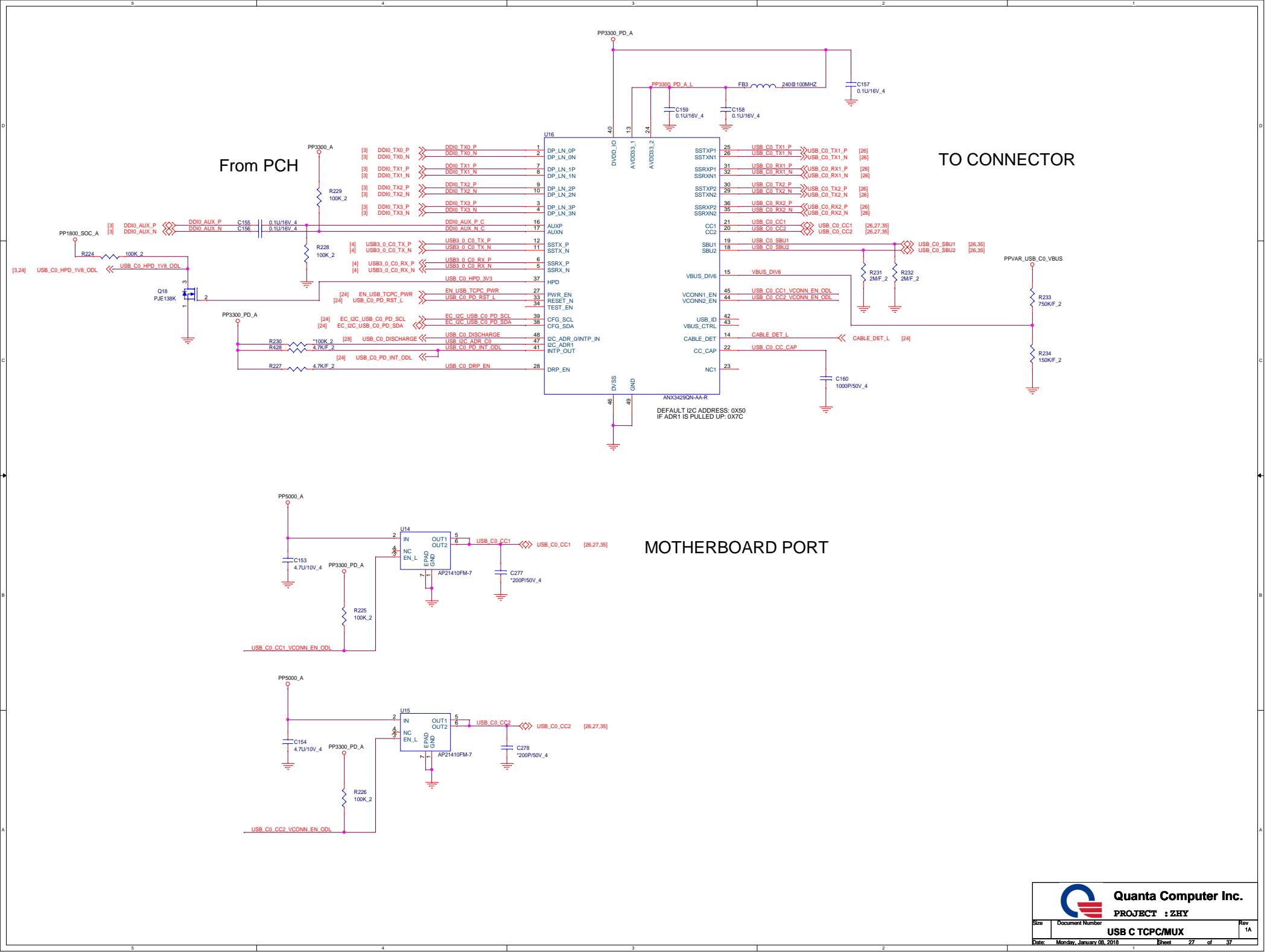
To LTE/B connector





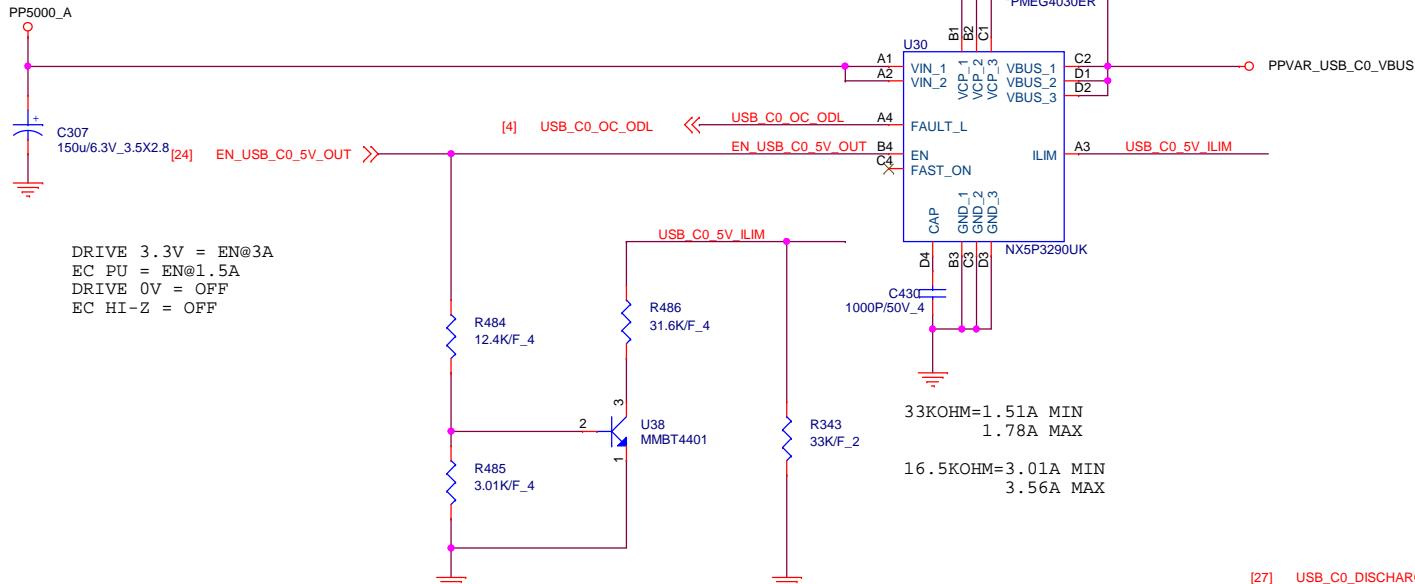






PORT 0

PROVIDES ESD PROTECTION, PLACE CLOSE TO CONNECTOR



VBUS DISCHARGE

PPVAR_USB_C0_VBUS

R753 1.2K_6

USB_C0_DISCHARGE

[27] USB_C0_DISCHARGE

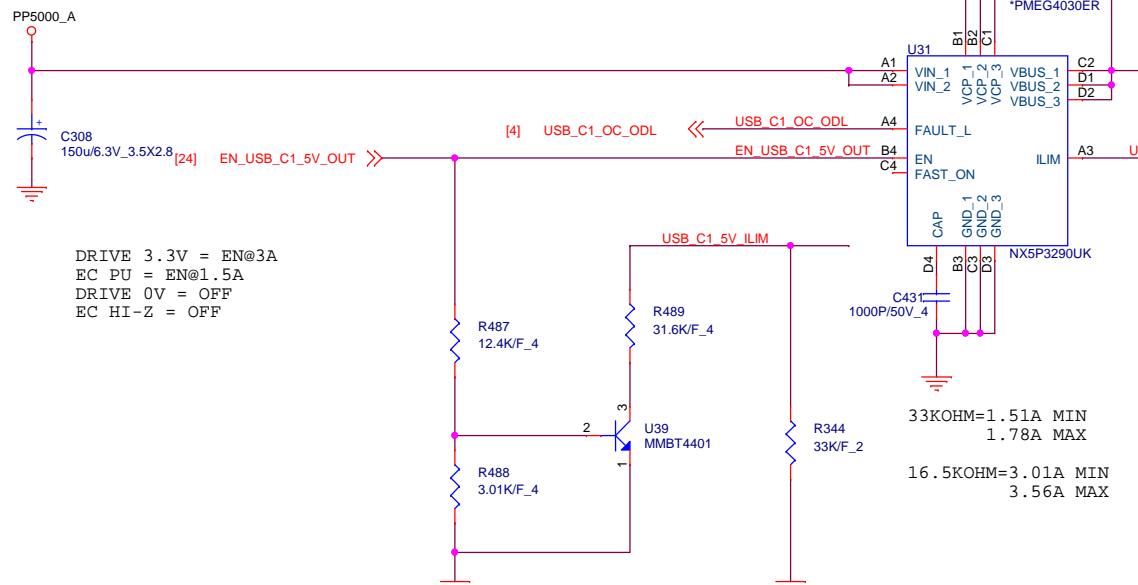
Q25 PJE138K

R752 100K_2

20UF LOAD, 21V TO <0.8V IN <80MS
20UF LOAD, 5V TO <0.8V IN <45MS
0402 CAN DISSIPATE 400MW FOR 80MS
0603 CAN DISSIPATE 400MW FOR 500MS
0805 CAN DISSIPATE 400MW FOR 2000MS

PORT 1

PROVIDES ESD PROTECTION, PLACE CLOSE TO CONNECTOR



Quanta Computer Inc.

PROJECT : ZHY

Size Document Number

Rev

USB C 5V OUT

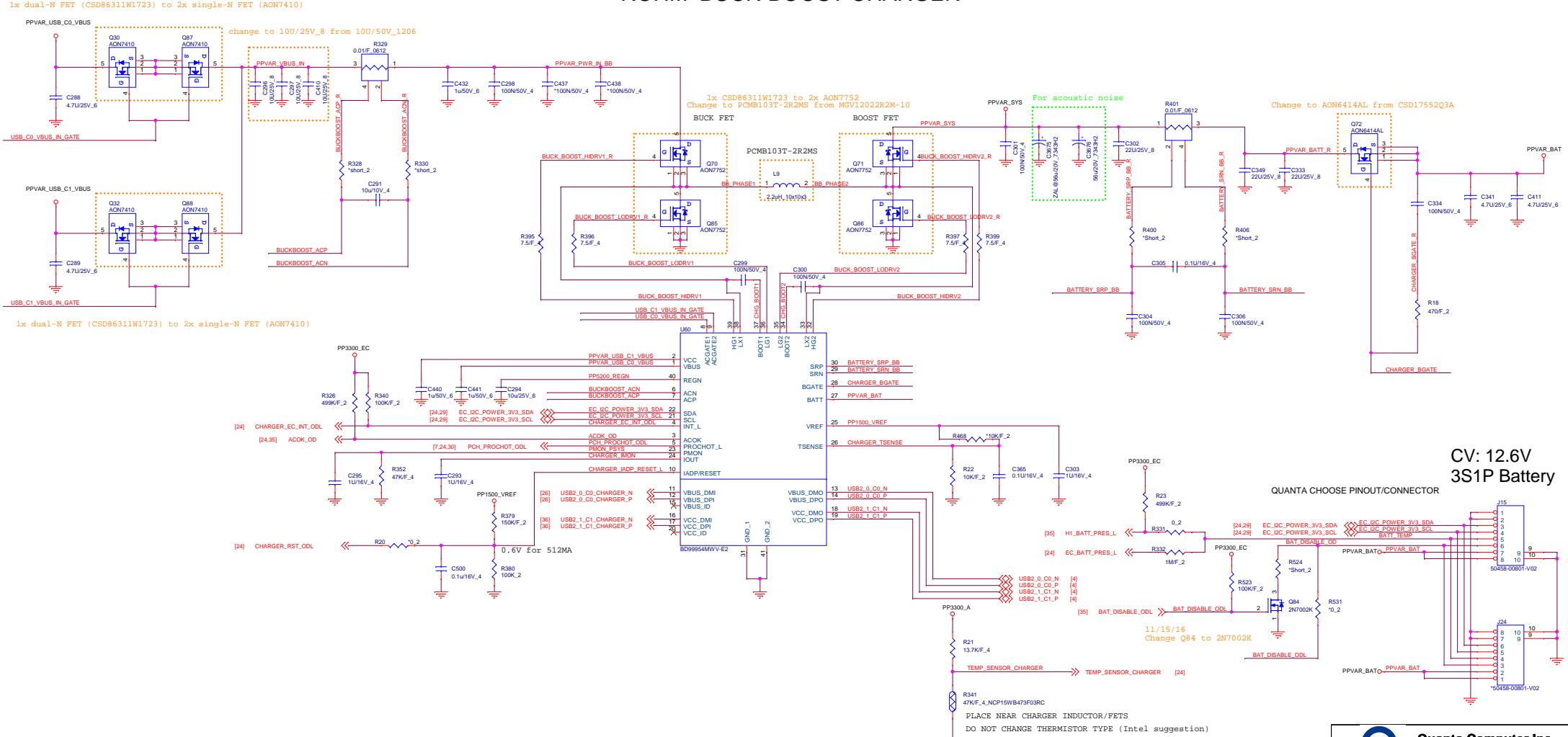
Date: Monday, January 08, 2018

1A

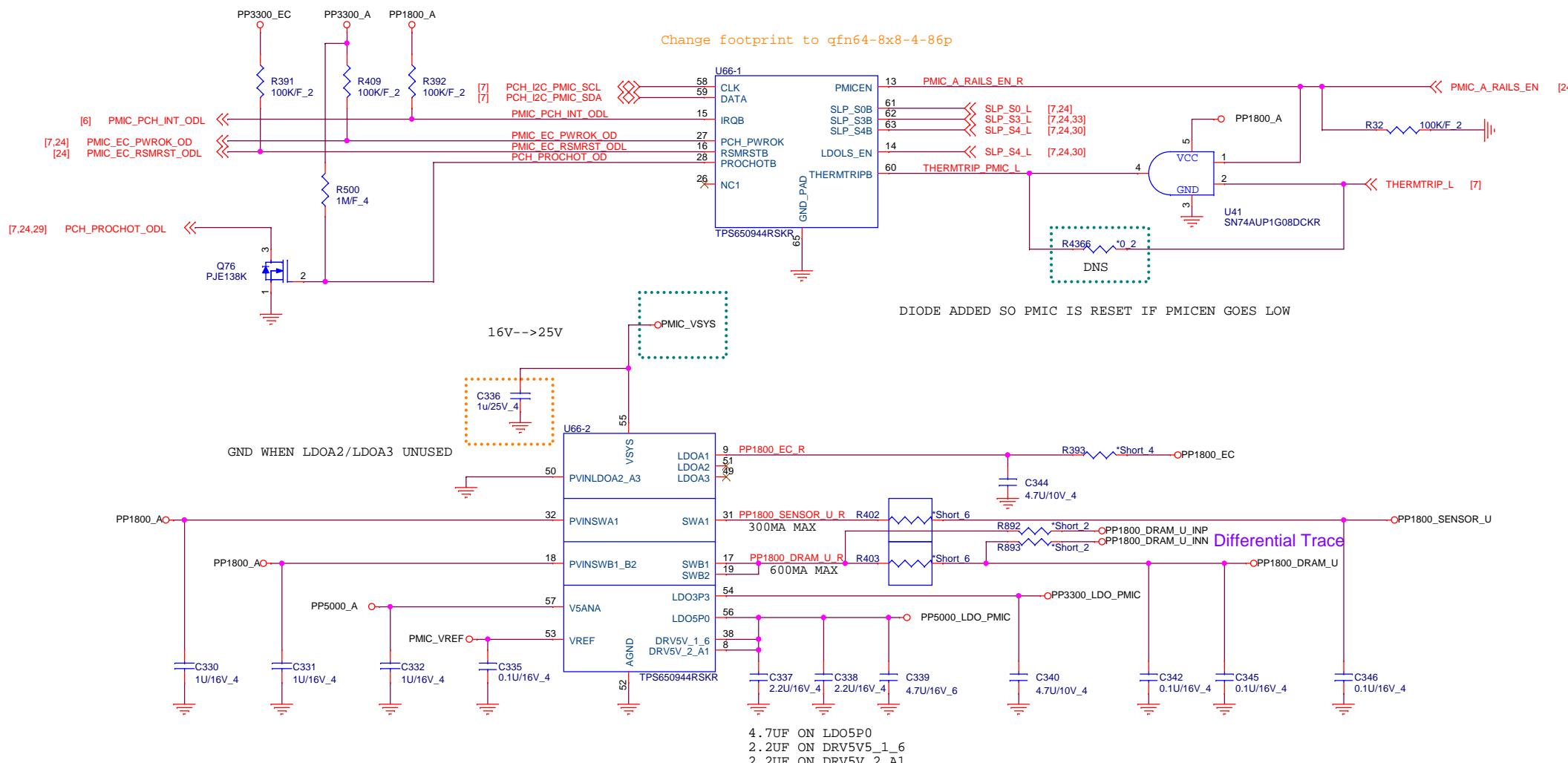
Sheet 28 of 37

POWER - BATTERY CHARGER

ROHM BUCK-BOOST CHARGER



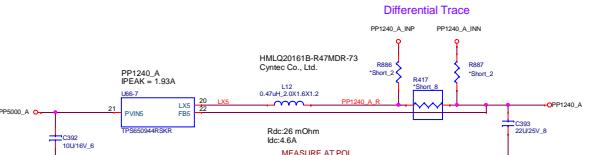
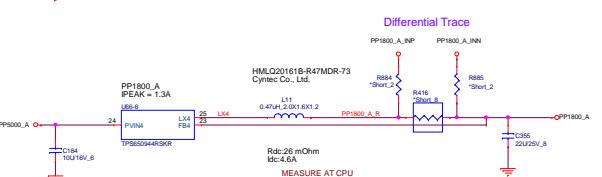
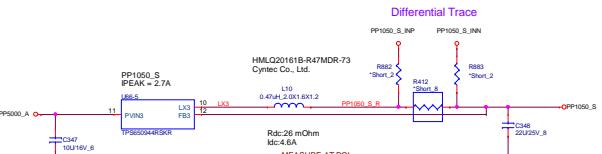
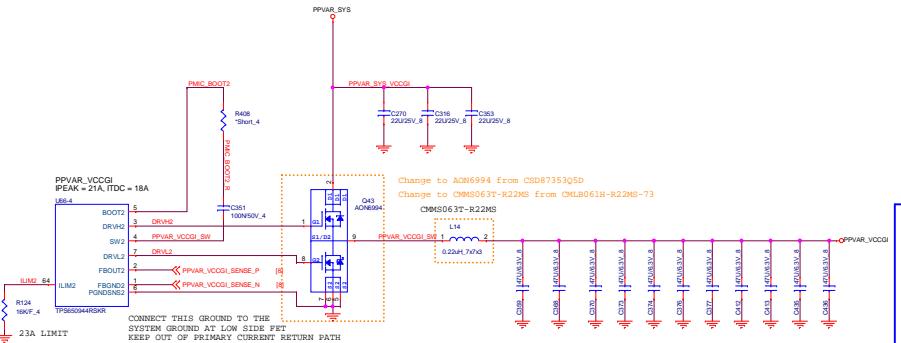
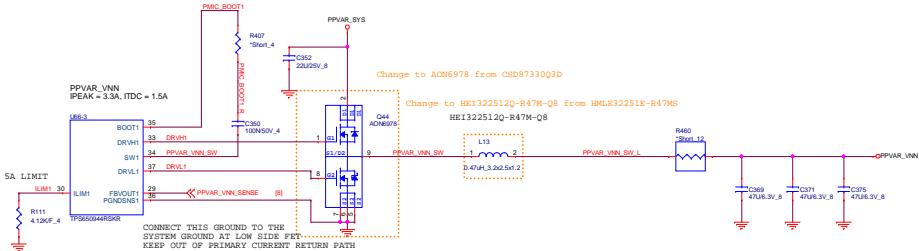
POWER - PMIC LOGIC



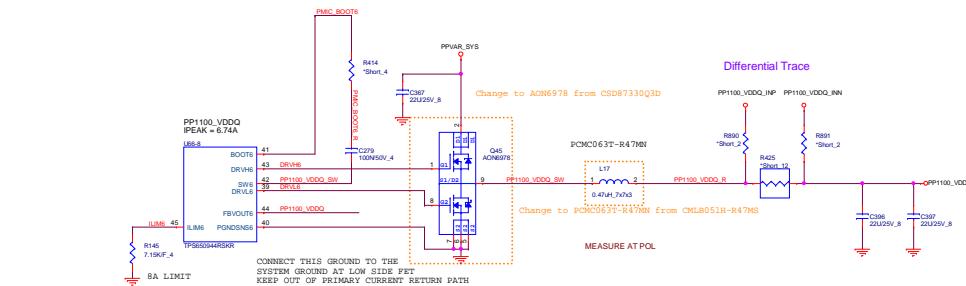
Quanta Computer Inc.

PROJECT : ZHY

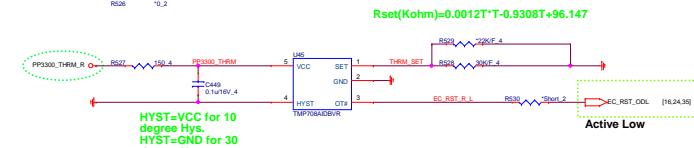
POWER - PMIC RAILS



PER TI CAN BE DISCONNECTED IF VTT IS DISABLED



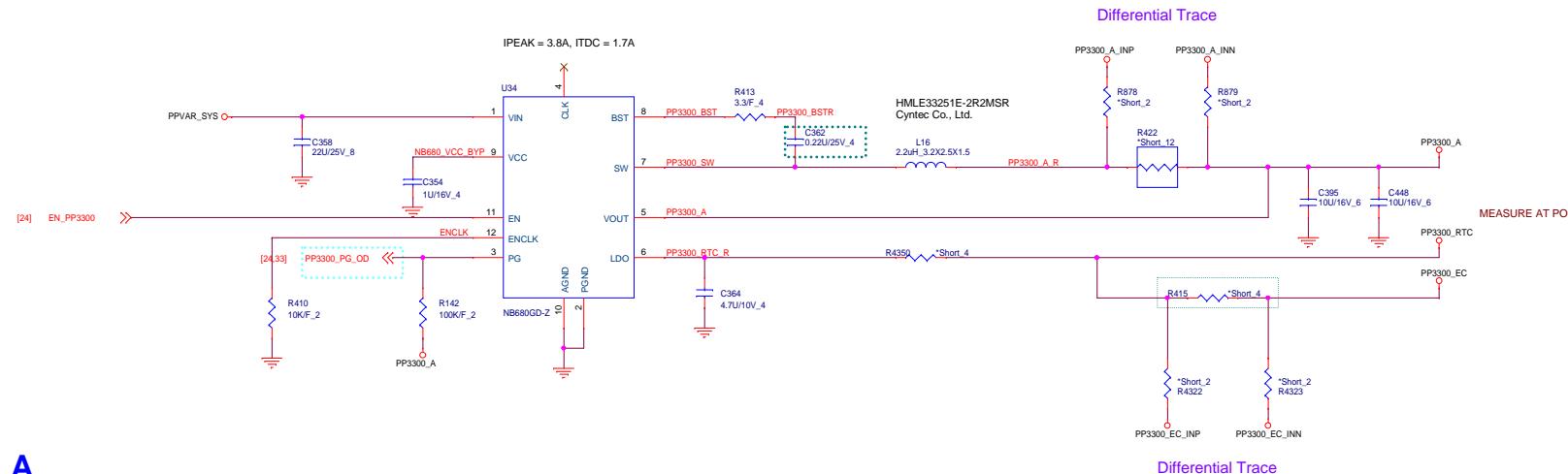
Thermal Protector



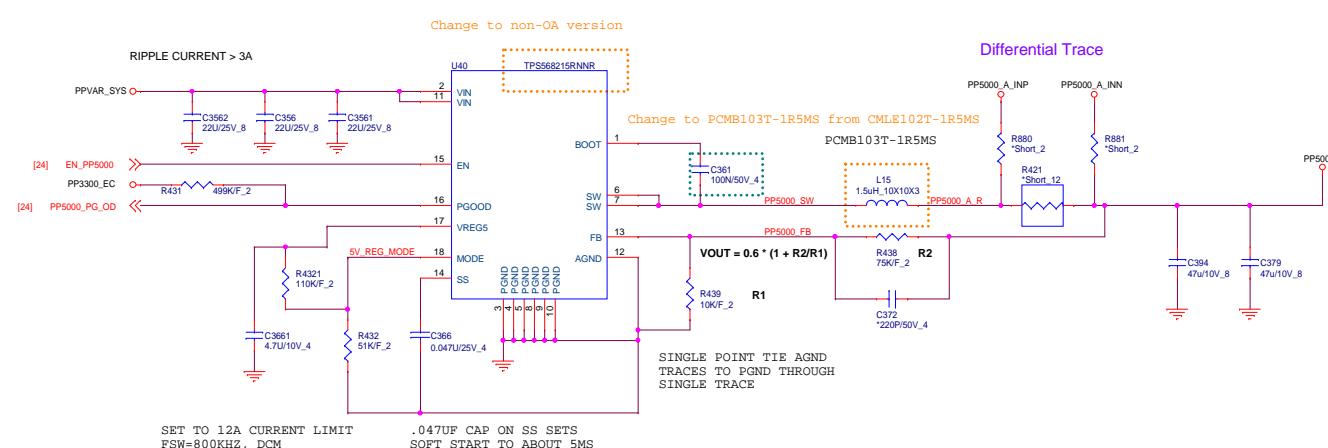
PMIC_VSYS

PPVAV_SYS — R4365 — Short_8 — PMIC_VSYS

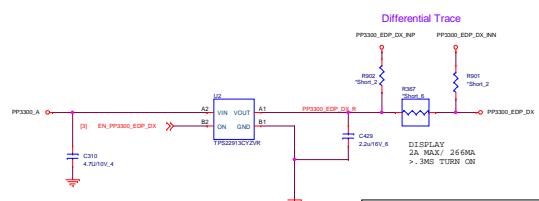
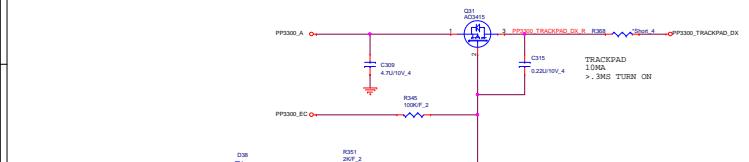
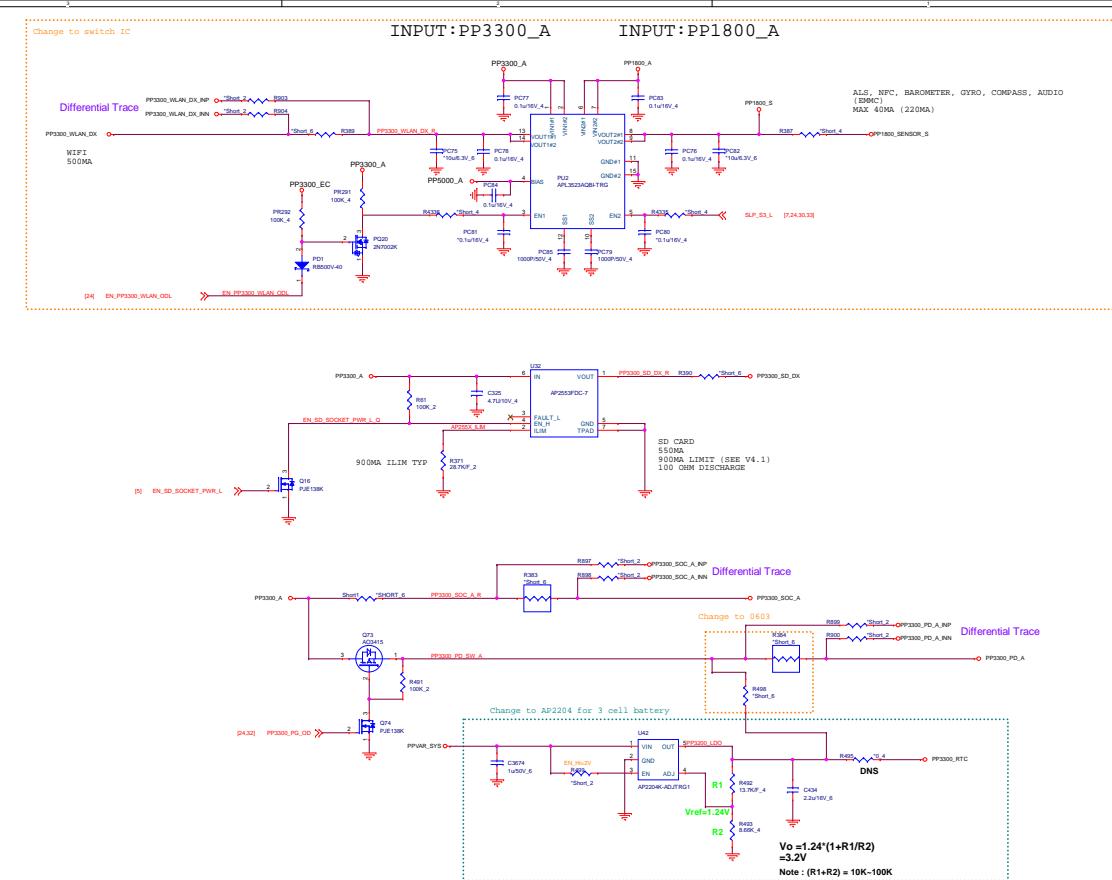
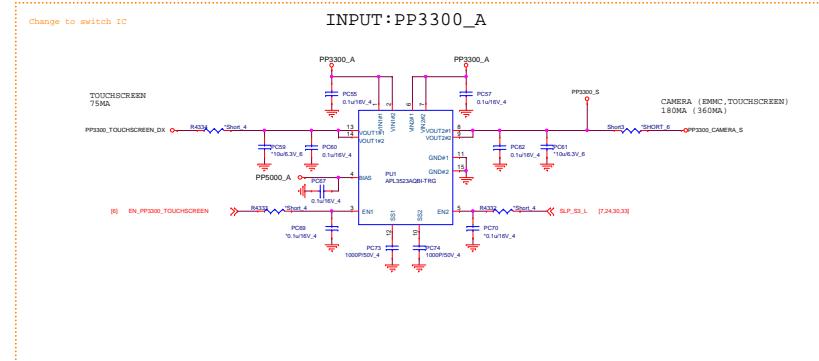
PP3300_A



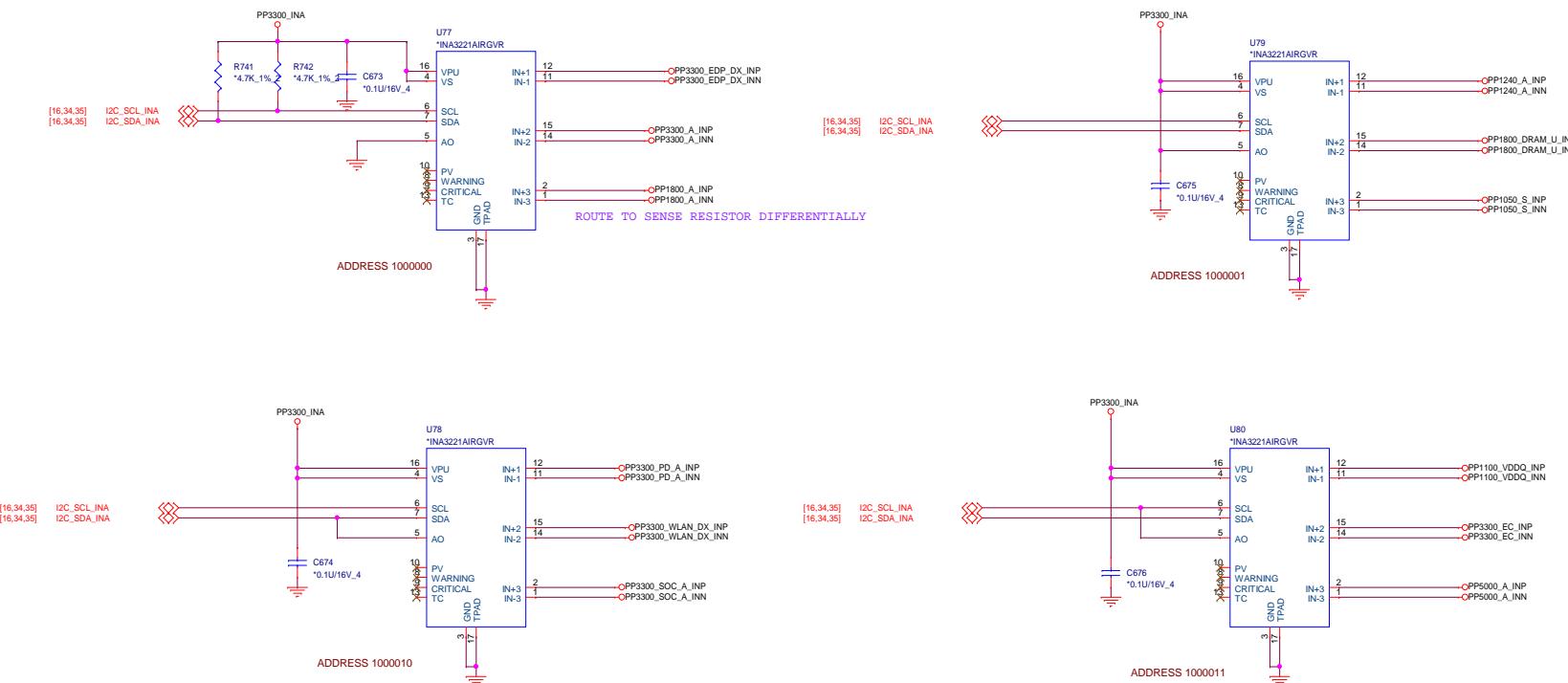
PP5000_A



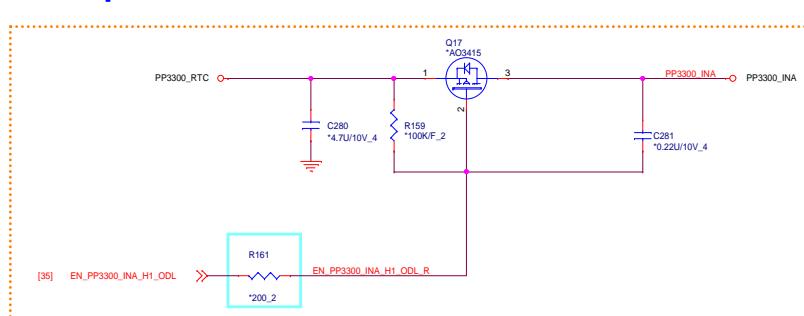
POWER - LOAD SWITCHES



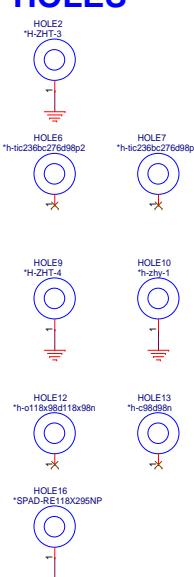
INAs



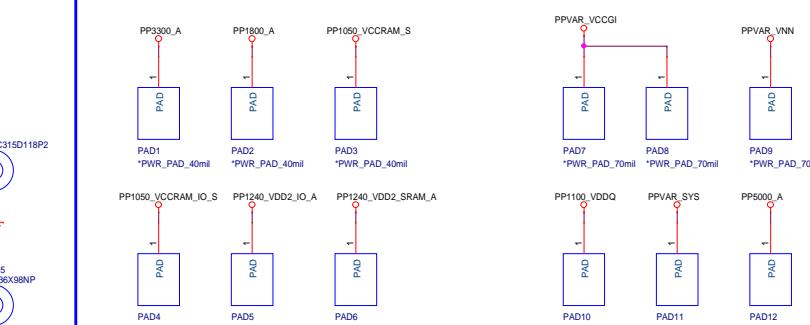
INAs power

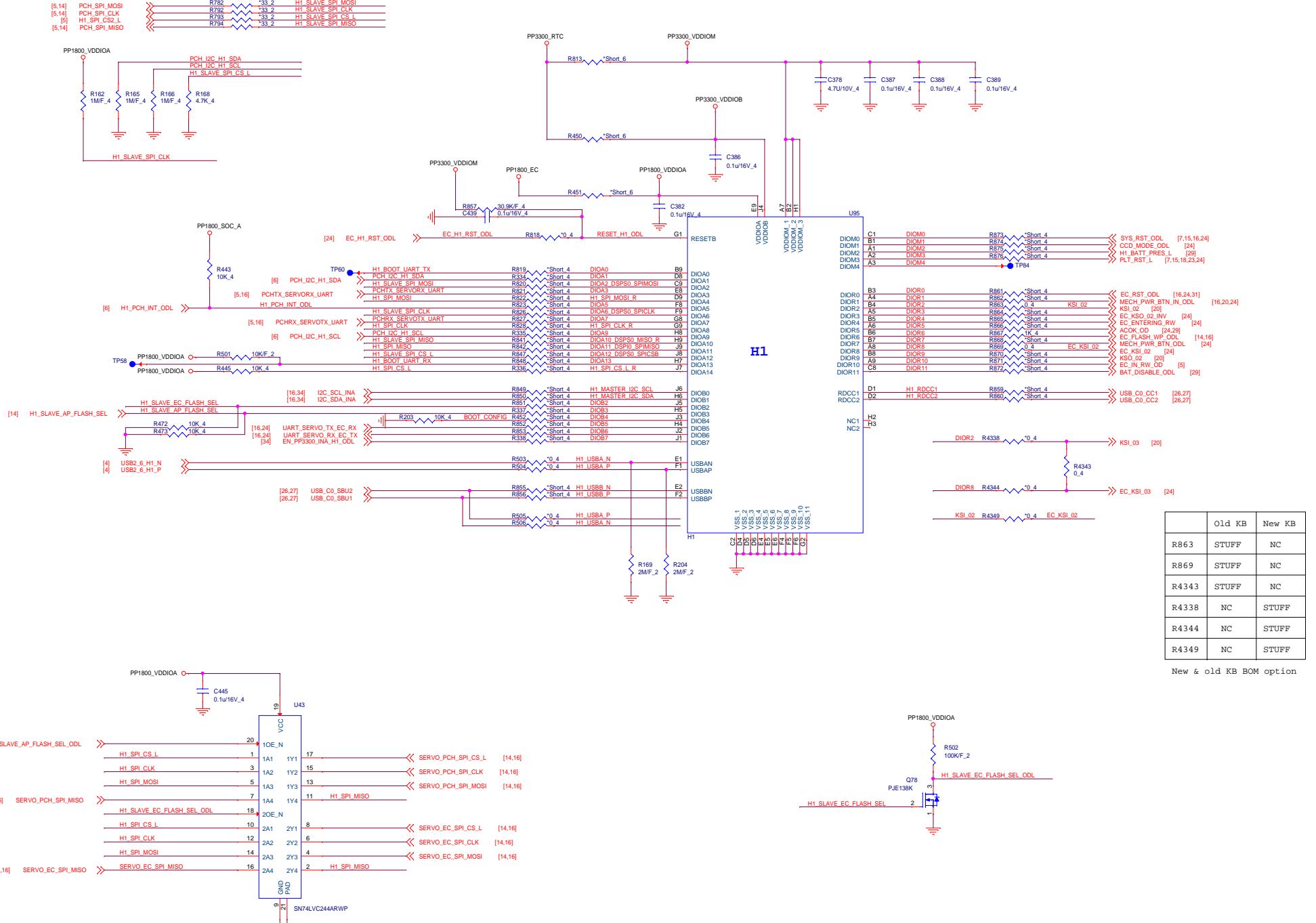


HOLES



POWER TEST PAD

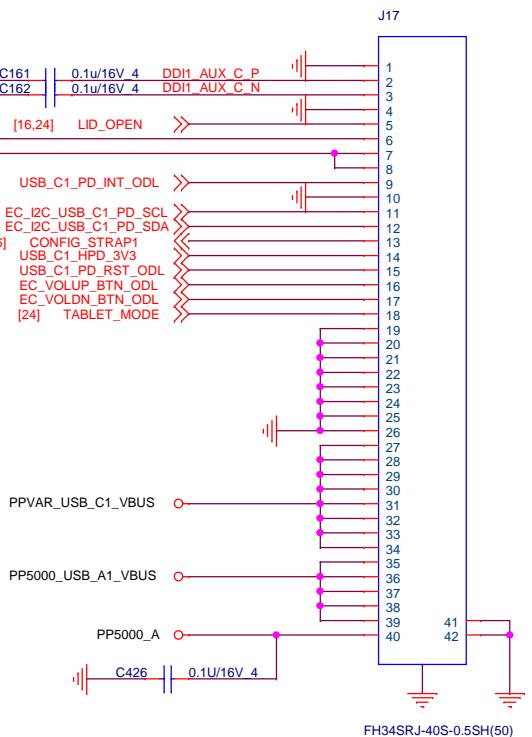




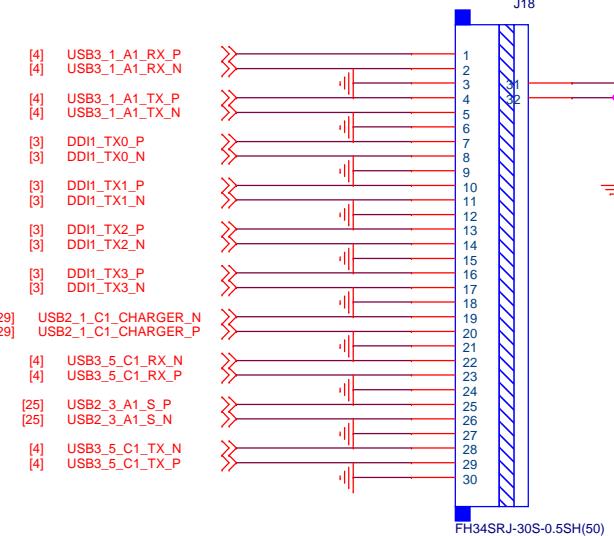
PP3300_A
R236
100K/F_4
DDI1_AUX_C_N
DDI1_AUX_C_P
R235
100K/F_4
PP3300_RTC
PP3300_PD_A
C424
0.1u/16V_4
C425
0.1u/16V_4

CONFIG_STRAP1		
Float	DB USBC	
H	DB USBC+LTE	
L	DB USBC+USBA	

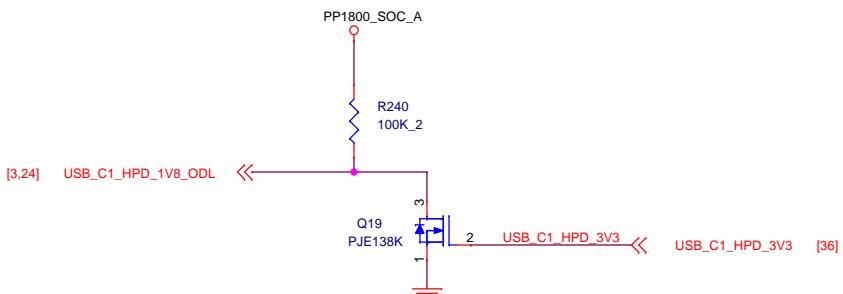
Low-speed connector



High-speed connector



FH34SRJ-40S-0.5SH(50)



Model	Version	CHANGE LIST
ZHY	3A	<p>2017/12/19</p> <p>page 6 Change Q91 to PMDXB600UNE, add R4368 for LTE EN_PP3300_DX_LTE signal glitch issue page 21 Change R497 to short pad page 35 Change R875,R450,R813 to short pad page 29 Change value to 10u/25V_8 for C296,C297,C410 page 30 Change R393,R402,R403 to short pad page 31 Change R460,R412,R416,R417,R425,R530,R4365 to short pad page 32 Change R422,R4350,R415,R421 to short pad page 33 Change R4334,R4333,R4332,R4336,R4335,R383,R381,R367,R499 to short pad Change R498,R384 to short pad page 34 DNS all INAs and INAs power</p> <p>2017/12/20</p> <p>page 29 Add C3675,C3676 (56uF/20V/7343) on PPVAR_SYS for acoustic noise. page 31 Remove PU15,R4364,PR191,PR189,PR188,R4363,PC197,PC195.</p> <p>2017/12/29</p> <p>page 29 Stuff C3676 for all project,stuff C3675 for Astronaut project only</p> <p>2018/1/3</p> <p>page 24 Change R37 to 12.4Kohm for PVT</p>