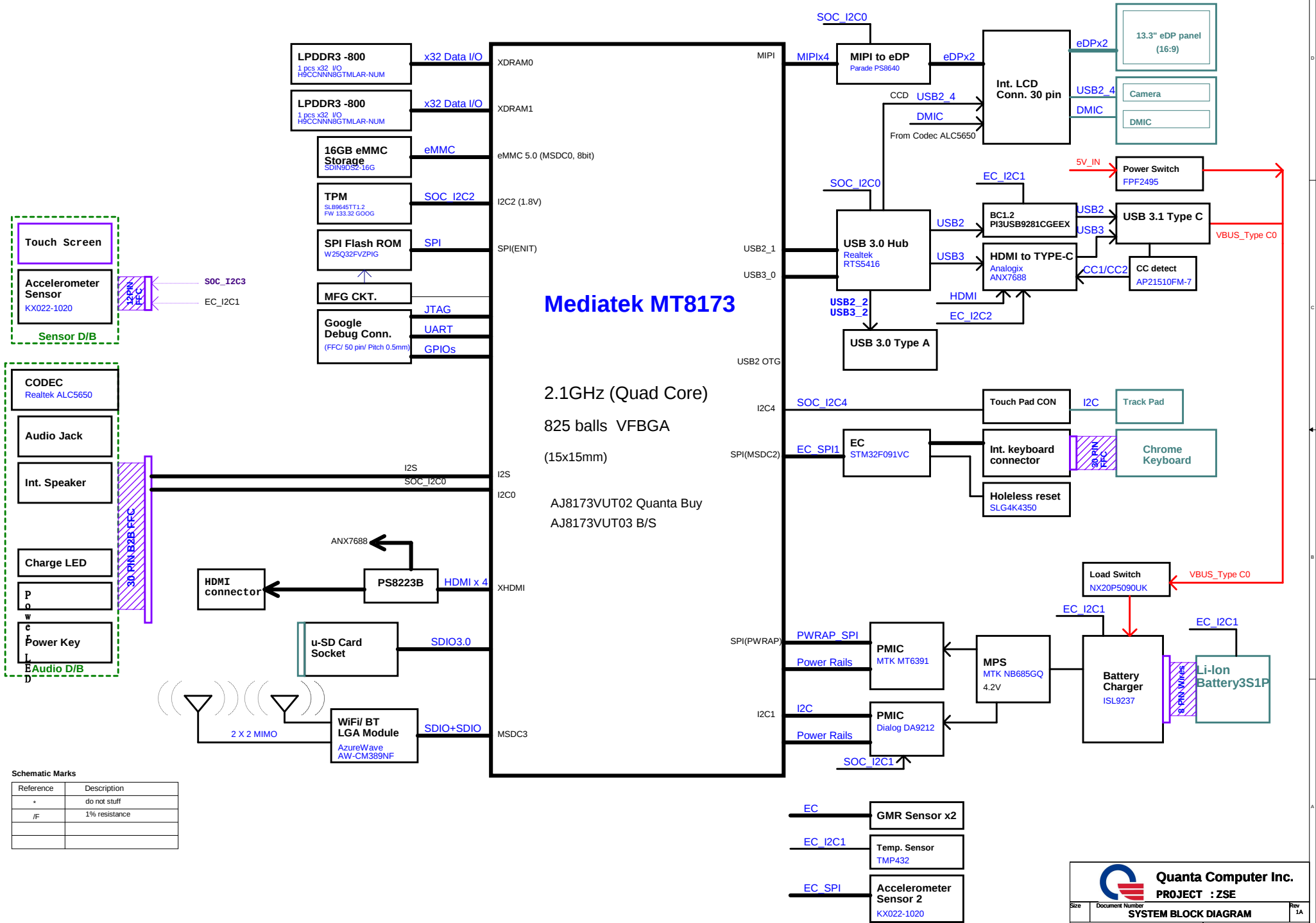


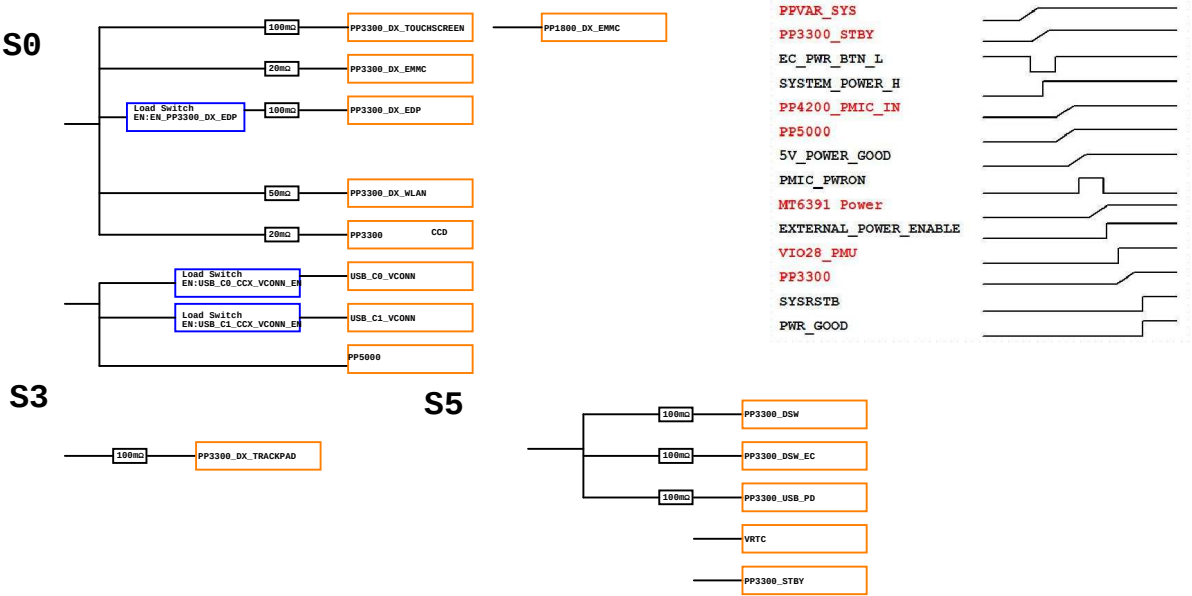
Block Diagram (MTK MT8173 SOC + LPDDR3)



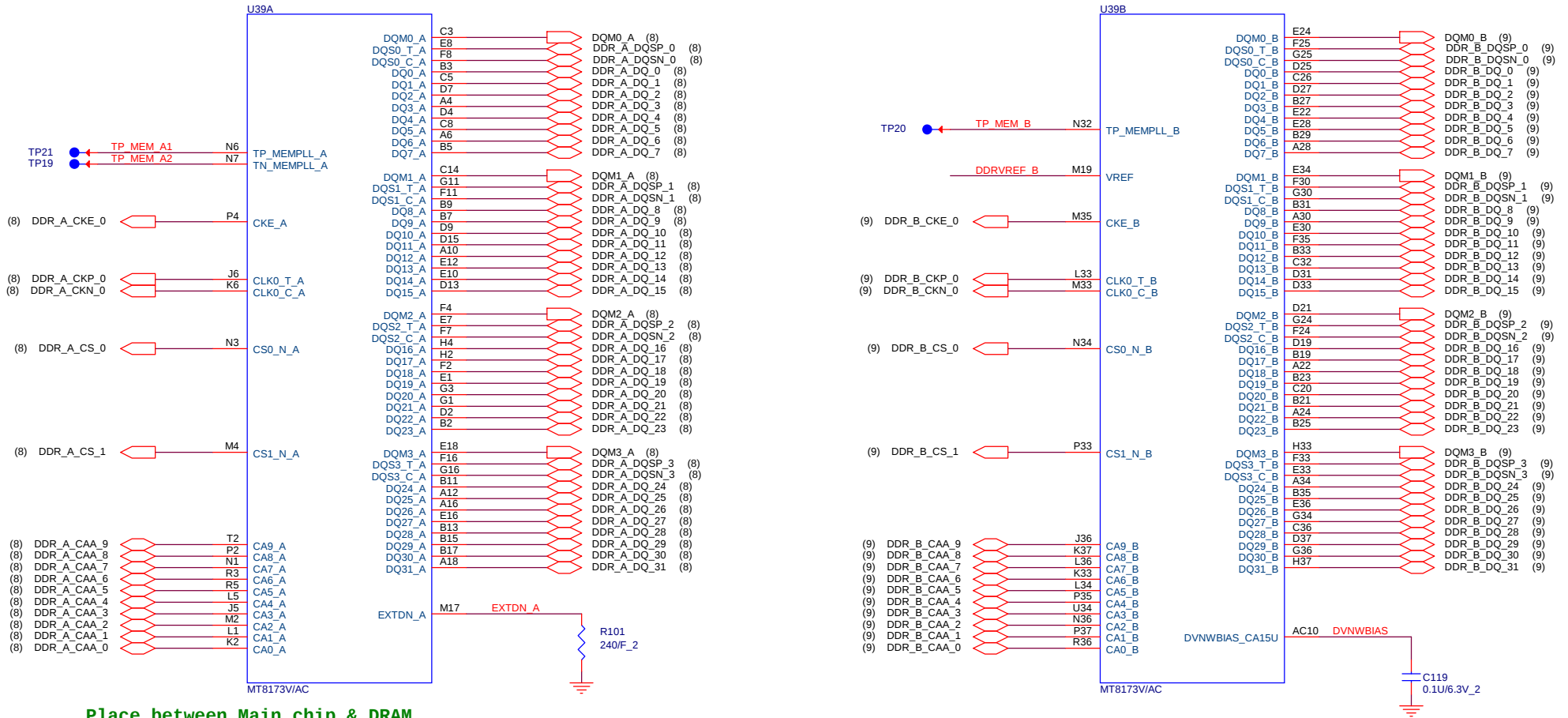
I2C MAP

HOST	BUS Name	Frequency	Pull up	Device	Part Number	7 bit	8 bit R	8 bit W	
SoC	SCL4(SDA4)	400KHz	PP3300	TrackPad	SA459C-1200				X
	SCL1(SDA1)	400KHz	VI018_PMU	PMIC	DA9212-AKU02				
	SCL2(SDA2)	400KHz	VI018_PMU	TPM	SLB9645TT1.2				X
	SCL3(SDA3)	400KHz	PP3300	Touchscreen	eKTH3915		0x10	0x20	V
	SCL0(SDA0)	400KHz	VGP1_PMU	Codec	ALC5650-CGT	0011010	0x34	0x35	V
		1MHz	PP3300	USB HUB	RTS5413-GRT	1101010	0xD2	0xD3	V
		400KHz	VGP2_PMU	eDP Bridge	PS8640QFN56G	pin17 L: 0x10-0x2F(default) H: 0x30-0x4F			/
EC	I2C1	1MHz	PP3300_STBY	Thermal	TMP432ADGSR	1001100			V
		400KHz	PP3300_STBY	USB BC 1.2	PI3USB9281CXWE	0100101			V
		400KHz	PP3300_STBY	Charger	PS8740BQFN40GTR	0001001	0X13H	0X12H	V
		400KHz	PP3300_STBY	Battery					
		400KHz	VI018_PMU	ACCEL (D/B)	PI3USB9281	0x1E			V
	I2C2	400KHz		HDMI to USB	C ANX7688				

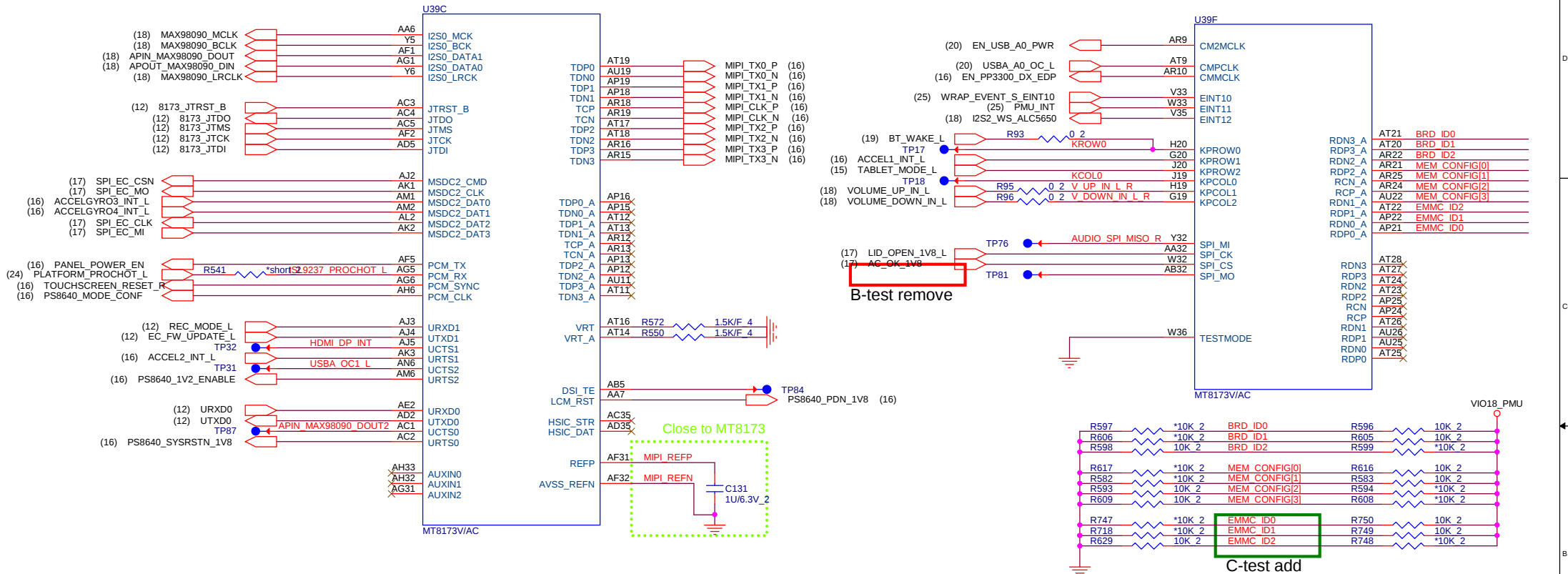
MT8173 POWER TREE



MT8173 MEMORY



MT8173 MIPI, AUDIO, GPIO



MEMORY STRAPPING

CONFIG	[3]	[2]	[1]	[0]	
HYNIX 2GB	0	0	0	0	
SAMSUNG 4G K4E6E304EE-EGCE	0	0	1	0	
HYNIX 4GB AKD5R0TW57, H9CCNNBLTBLAR-NUD	0	0	1	1	Proto EVT1-1 DVT-1 PVT
SAMSUNG 4G AKD5RZST504, K4E6E304EB-EGCF	0	1	0	0	EVT1-2 DVT-2

eMMC ID (U41)

CONFIG	[2]	[1]	[0]	
Hynix 16G H26M52208FPR, ARZHXSXR1000	0	0	0	EVT1-1
SAMSUNG 16G KLMA61JENB-B041, AR0ZDAR1004	0	1	0	DVT-1
HYNIX 32GB H26M64208EMR, ARZHXSXR1001	0	1	1	DVT-2 PVT
SAMSUNG 32G KLMBG2JENB-B041, AR0ZDAR1005	0	0	1	EVT1-2
HYNIX 64GB H26M78208CMR, AR0ZSER1000	1	0	0	DVT-3

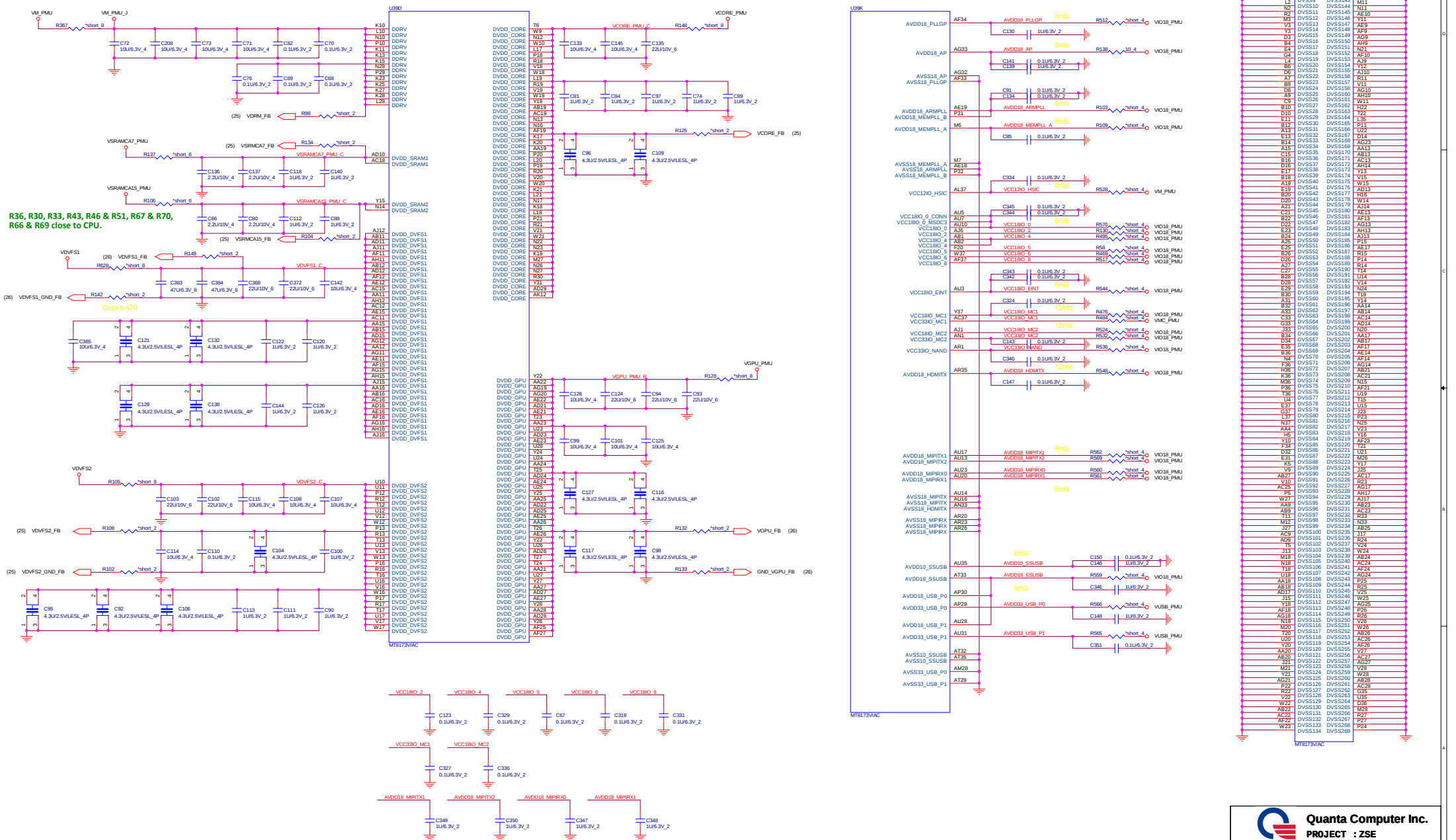
BOARD ID

CONFIG	[2]	[1]	[0]	
Rev. 0	0	0	0	Proto
Rev. 1	0	0	1	EVT
Rev. 2	0	1	0	DVT
Rev. 3	0	1	1	PVT

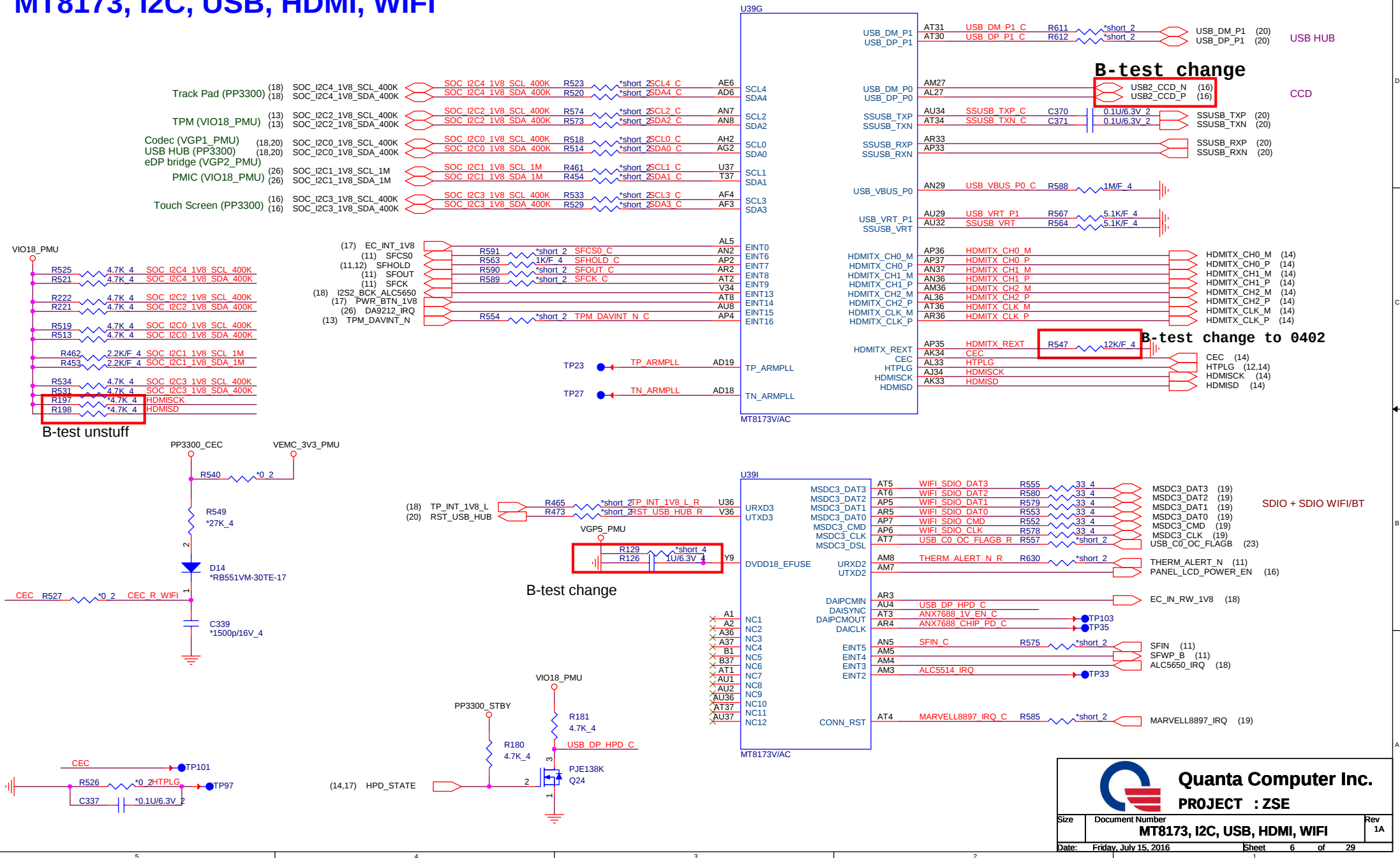
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Size	Document Number	Rev 1A
MT8173 MIPI, AUDIO, GPIO		
Date: Friday, July 15, 2016	Sheet 4 of 29	

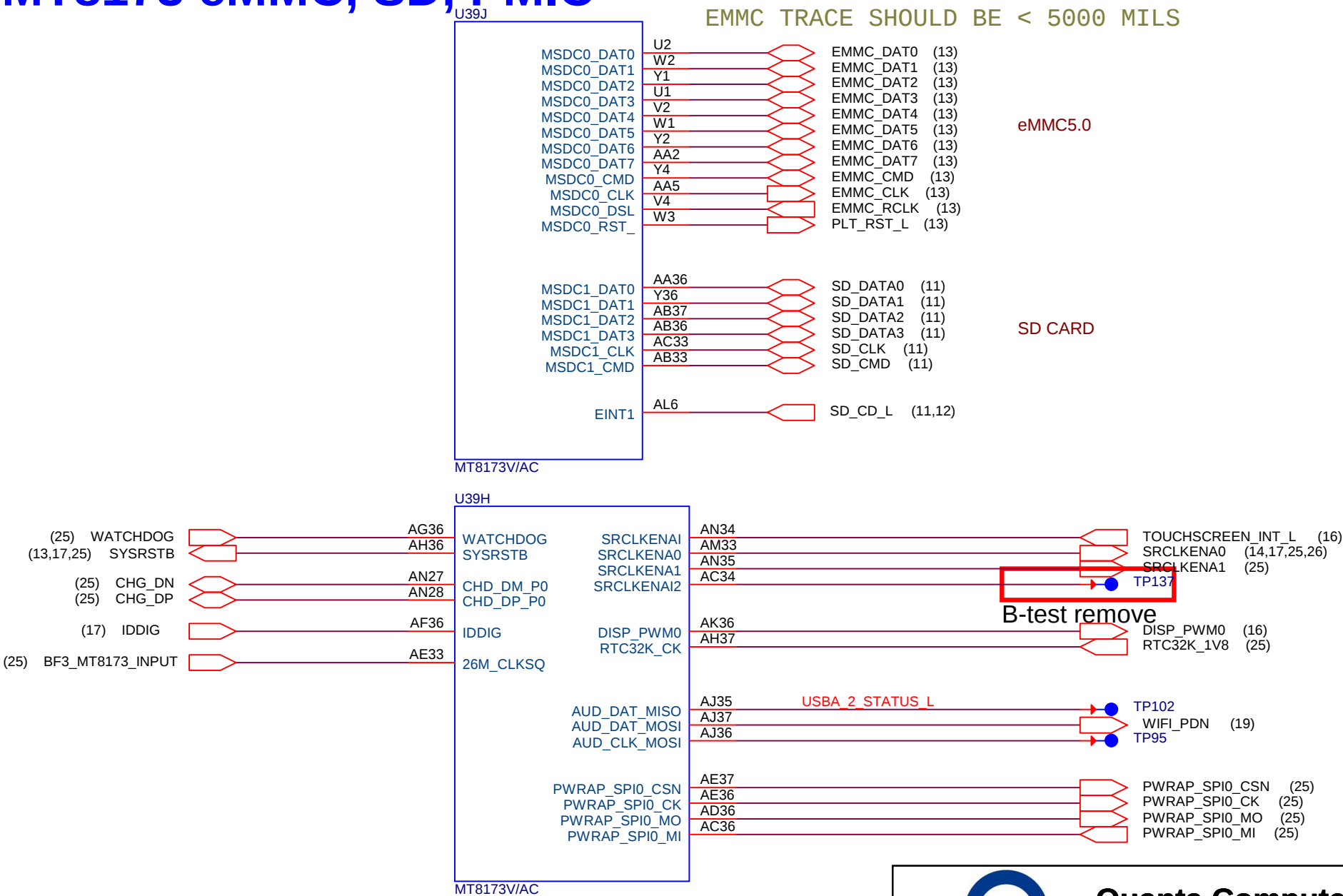
MT8173 POWER, GND



Only USB port 0 can support OTG.

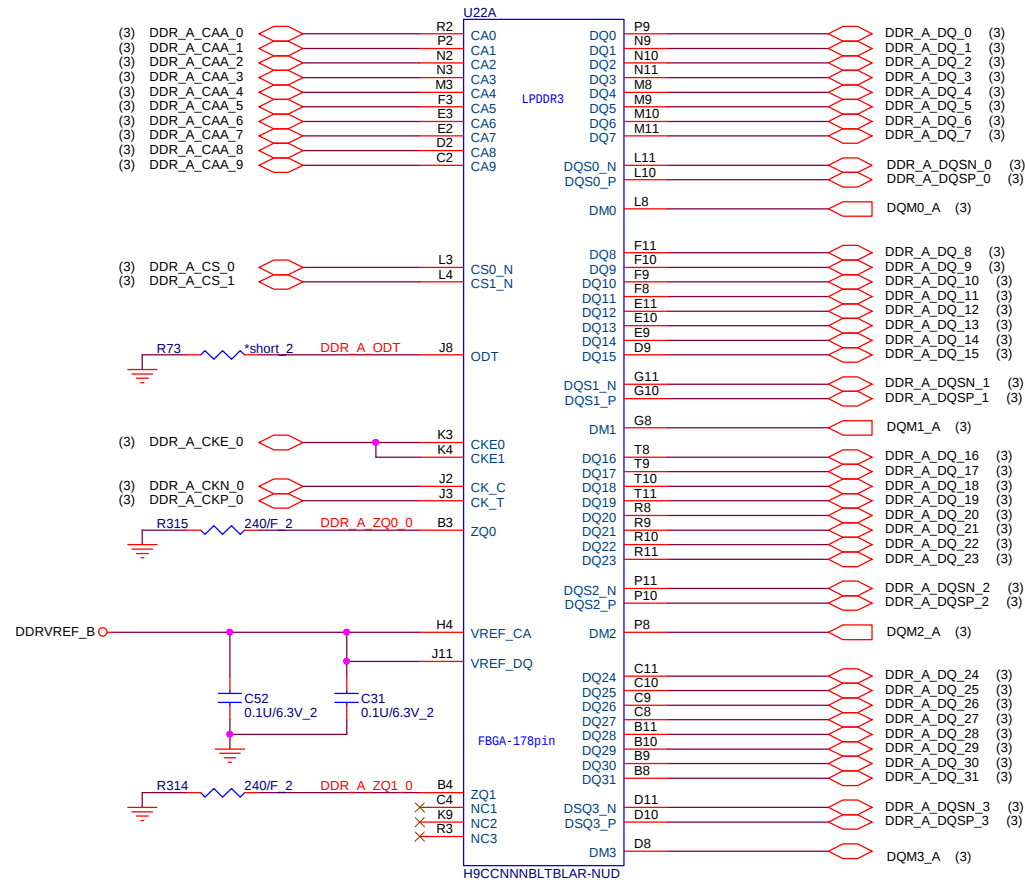


MT8173 eMMC, SD, PMIC



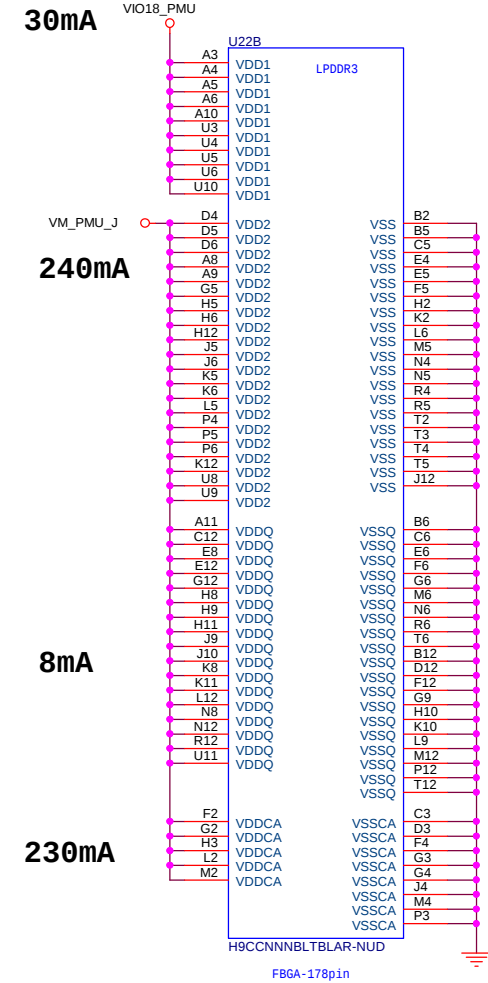
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MEMORY CH_A



RAM (U21,U22)

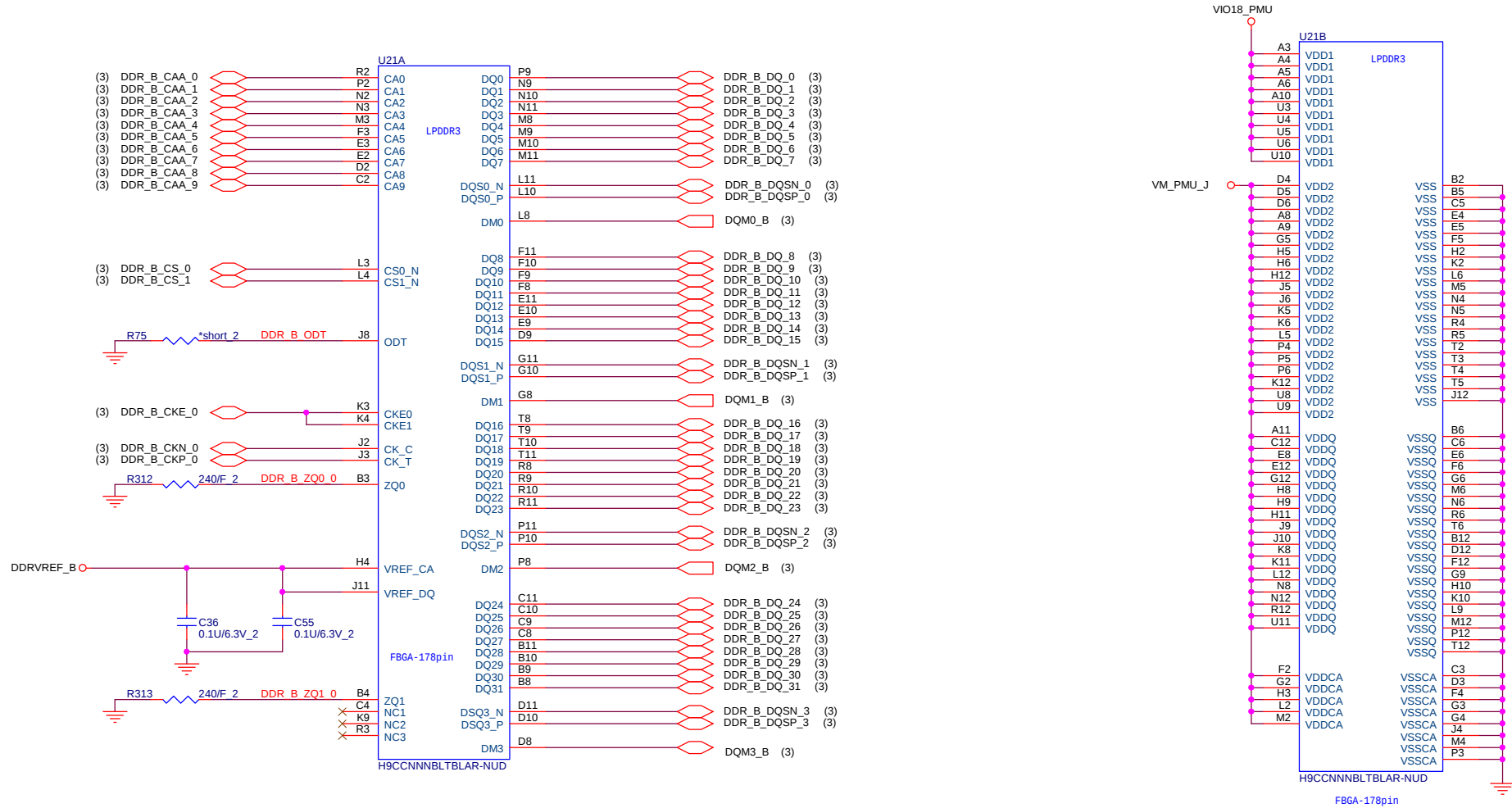
Hynix 16Gb H9CCNNBLTBLAR-NUD , AKD5RW0TW57
SAMSUNG 16Gb K4E6E304EB-EGCF , AKD5RZST504



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PROJECT : ZSE

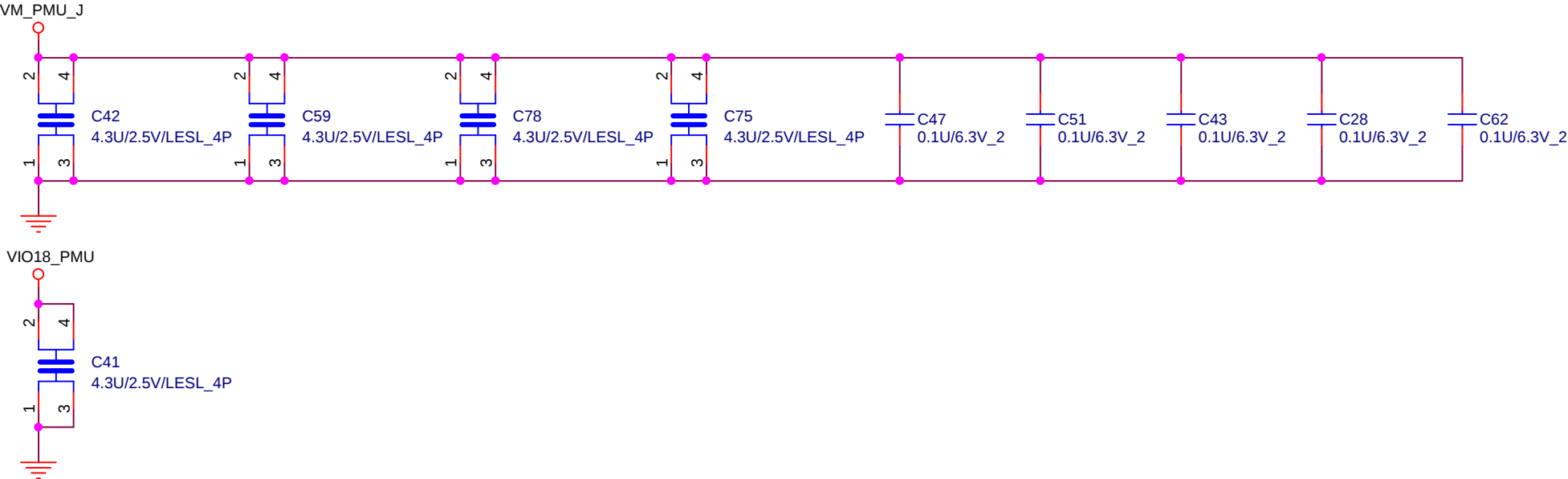
Size	Document Number	Rev 1A
Date: Friday, July 15, 2016	MEMORY CH A	

MEMORY CH_B

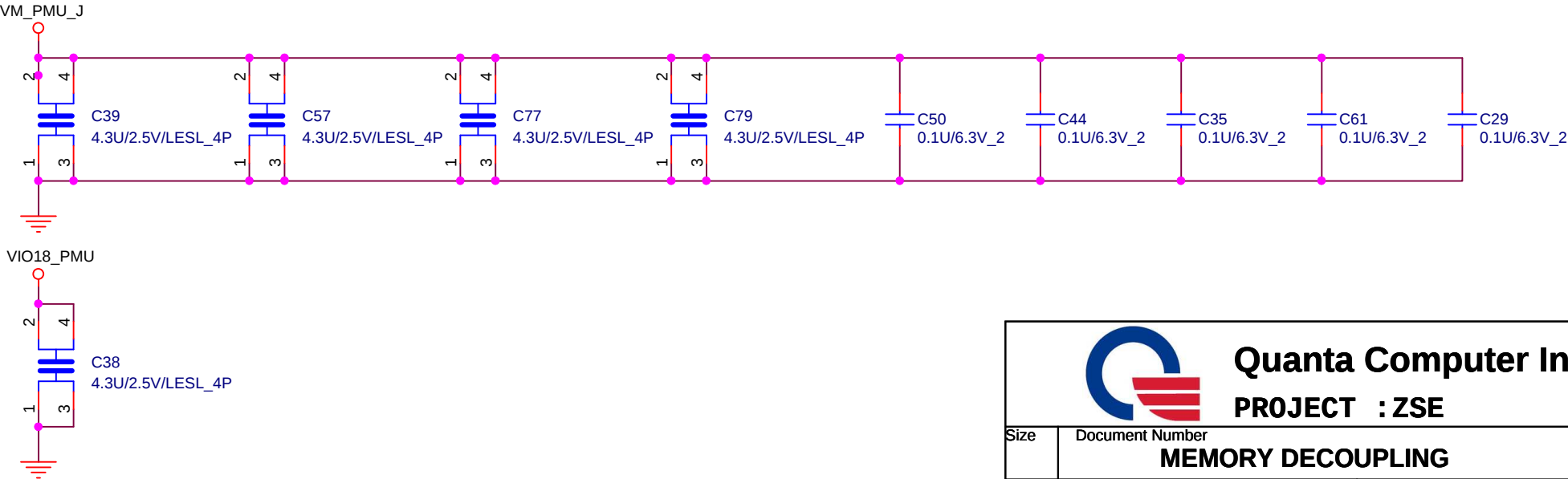


MEMORY DECOUPLING

CHANNEL A



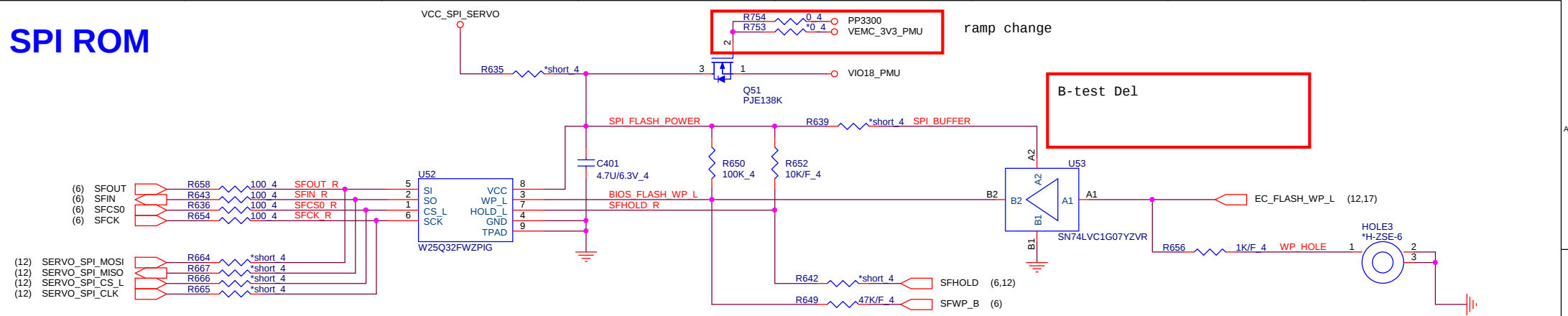
CHANNEL B



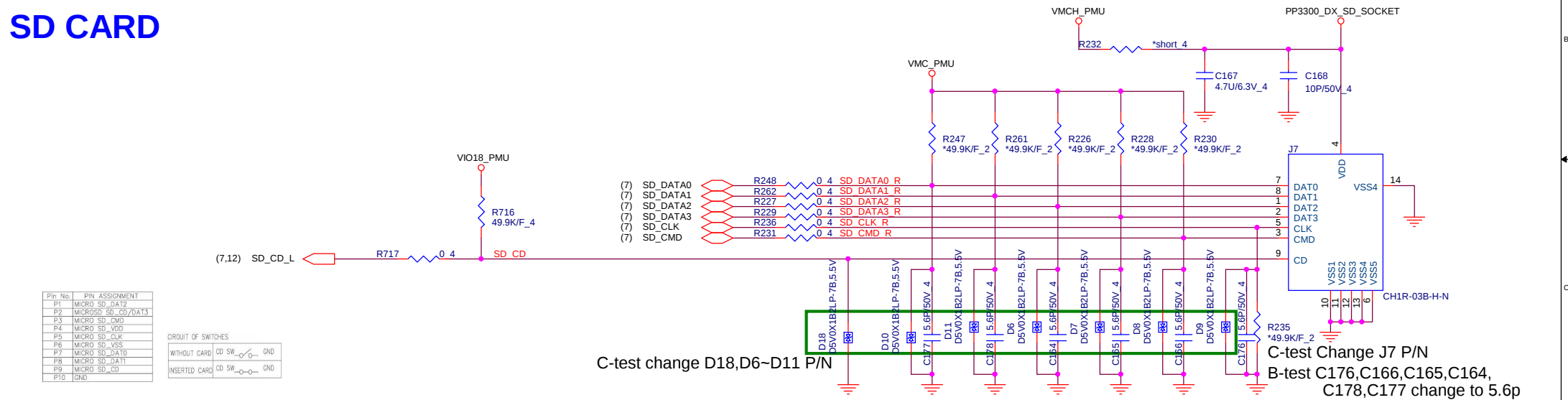
Quanta Computer Inc.
PROJECT : ZSE

Size	Document Number	Rev
	MEMORY DECOUPLING	1A
Date:	Friday, July 15, 2016	Sheet 10 of 29

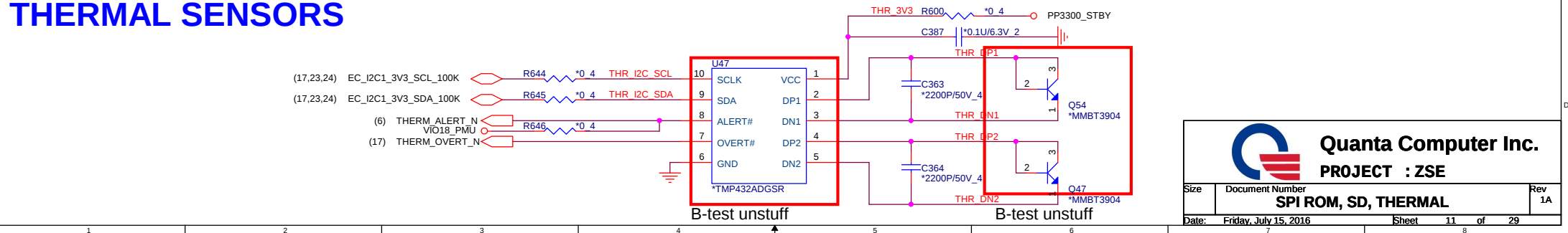
SPI ROM



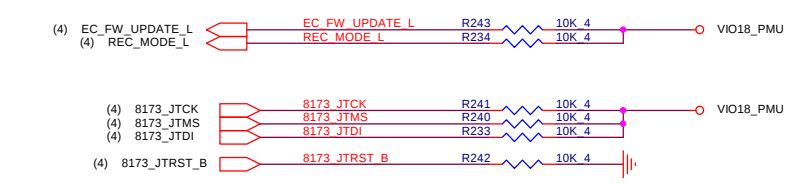
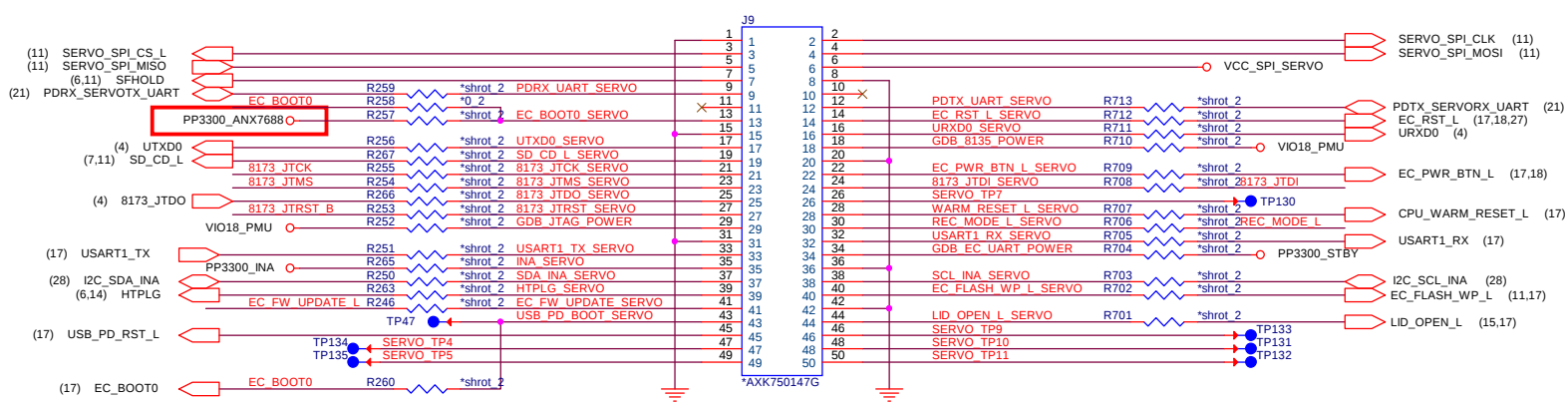
SD CARD



THERMAL SENSORS



SERVO



GROUP	Pin	Direction	Description	x86 boards	ARM boards
GND	1	S <-> D	Shared ground plane	GND	GND
SPI2	2	S --> D	SPI2 clock (referenced to SPI2_VREF)	SPI_SCK BIOS EEPROM	SPI_SCK BIOS EEPROM
SPI2	3	S --> D	SPI2 chip select (referenced to SPI2_VREF)	SPI_CS# BIOS EEPROM	SPI_CS# BIOS EEPROM
SPI2	4	S --> D	SPI2 data input (referenced to SPI2_VREF)	SPI_DI BIOS EEPROM	SPI_DI BIOS EEPROM
SPI2	5	S <-> D	SPI2 data output (referenced to SPI2_VREF)	SPI_DO BIOS EEPROM	SPI_DO BIOS EEPROM
SPI2	6	S <-> D	SPI2 voltage reference (1.8-3.3V)	SPI BIOS EEPROM voltage rail	SPI BIOS EEPROM voltage rail
SPI2	7	S --> D 00	SPI2 Hold signal. Used to float outputs on DUT EEPROM during emulation)	SPI2_HOLD# (referenced to SPI2_VREF)	SPI2_HOLD# (referenced to SPI2_VREF)
GND	8	S <-> D	Shared ground plane	GND	GND
SPI1	9	S --> D	SPI1 clock (referenced to SPI1_VREF)	SPI_SCK EC EEPROM	SPI_SCK EC EEPROM
SPI1	10	S --> D	SPI1 chip select (referenced to SPI1_VREF)	SPI_CS# EC EEPROM	SPI_CS# EC EEPROM
SPI1	11	S --> D	SPI1 data input (referenced to SPI1_VREF)	SPI_DI EC EEPROM	SPI_DI EC EEPROM
SPI1	12	S <-> D	SPI1 data output (referenced to SPI1_VREF)	SPI_DO EC EEPROM	SPI_DO EC EEPROM
SPI1	13	S <-> D	SPI1 voltage reference (1.8-3.3V)	SPI EC EEPROM voltage rail	SPI EC EEPROM voltage rail
SPI0	14	S --> D 00	EC Reset	EC RESET#/COLD_RESET#	EC_RESET#/COLD_RESET#
GND	15	S <-> D	Shared ground plane	GND	GND
UART2	16	S --> D	UART2 Rx Data (referenced to UART2_VREF)	PCH_UART_RXD (or system uart from EC/SuperI/O)	ARM UP UART_RXD
UART2	17	S <-> D	UART2 Tx Data (referenced to UART2_VREF)	PCH_UART_TXD (or system uart from EC/SuperI/O)	ARM UP UART_TXD
UART2	18	S <-> D	UART2 Voltage reference (1.8-3.3V)	PCH_UART voltage rail	ARM UP UART voltage rail
SP10	19	S --> D 00	SD Card detect - pulled low to simulate removal of an SD card	SD_DETECT	SD_DETECT
GND	20	S <-> D	Shared ground plane	GND	GND
JTAG	21	S --> D	JTAG clock (referenced to JTAG_VREF)	EC_JTAG_TCK	ARM UP JTAG_TCK
SP10	22	S --> D 00	Power button access (Low-pressed)	PWR_BTN#	PWR_BTN#
JTAG	23	S --> D	JTAG mode select (referenced to JTAG_VREF)	EC_JTAG_TMS	ARM UP JTAG_TMS
JTAG	24	S --> D	JTAG Data input (referenced to JTAG_VREF)	EC_JTAG_TDI	ARM UP JTAG_TDI
JTAG	25	S <-> D	JTAG Data output (referenced to JTAG_VREF)	EC_JTAG_TDO	ARM UP JTAG_TDO
JTAG	26	S <-> D	JTAG Return clock (referenced to JTAG_VREF)	EC_JTAG_RTCK	ARM UP JTAG_RTCK
JTAG	27	S --> D	JTAG Reset (referenced to JTAG_VREF)	EC_JTAG_TRST#	ARM UP JTAG_TRST#
JTAG	28	S --> D 00	CPU/Peripheral reset	to be used as a system wide reset while JTAG'ing	to be used as a system wide reset while JTAG'ing
JTAG	29	S <-> D	JTAG Voltage reference (1.8-3.3V)	EC_JTAG voltage rail	ARM UP JTAG voltage rail
SP10	30	S --> D 00	Recovery Mode	REC_MODE_L	REC_MODE_L
GND	31	S <-> D	Shared ground plane	GND	GND
UART1	32	S --> D	UART1 Rx Data (referenced to UART1_VREF)	EC_UART_RXD	EC_UART_RXD
UART1	33	S <-> D	UART1 Tx Data (referenced to UART1_VREF)	EC_UART_TXD	EC_UART_TXD
UART1	34	S <-> D	UART1 Voltage reference (1.8-3.3V)	EC_UART voltage rail	EC_UART voltage rail
I2C	35	S --> D	Power supplied by debug board for power measurement devices on DUT	Connect to INA's on DUT	Connect to INA's on DUT
GND	36	S <-> D	Shared ground plane	GND	GND
I2C	37	S <-> D 00	I2C data lin to communicate with power measurement devices	Connect to INA's on DUT	Connect to INA's on DUT
I2C	38	S --> D 00	I2C clock lin to communicate with power measurement devices	Connect to INA's on DUT	Connect to INA's on DUT
SP0	39	S --> D 00	HOT Plug Detect - pulled low to simulate external display disconnect	HPD	HPD
SP10	40	S --> D	enable WP on BIOS SPI bus (referenced to SPI2_VREF)	FW_WP (don't hard tie to VCC or GND) Referenced to SPI2_VREF	FW_WP (don't hard tie to VCC or GND) Referenced to SPI2_VREF
SP10	41	S --> D 00	Thermal trip simulation/Firmware update (ARM)	PROC_HOT_L/FW_UPDATE_L	PROC_HOT_L/FW_UPDATE_L
GND	42	S <-> D	Shared ground plane	GND	GND
SP10	43**	S --> D 00	Developer Mode	**See note below DEV_MODE	**See note below DEV_MODE
SP10	44**	S --> D 00	Lid Open	LID_OPEN	LID_OPEN
SP10	45**	S --> D 00	PCH disable/CPU NMI (ARM)	PCH_DISABLE_L/CPU_NMI	PCH_DISABLE_L/CPU_NMI
SP10	46**	S --> D 00	Keyboard matrix, column1	KBD_COL1	KBD_COL1
SP10	47**	S --> D 00	Keyboard matrix, column2	KBD_COL2	KBD_COL2
SP10	48**	S <-> D 00	Keyboard matrix, row1	KBD_ROW1	KBD_ROW1
SP10	49**	S <-> D 00	Keyboard matrix, row2	KBD_ROW2	KBD_ROW2
SP10	50**	S <-> D 00	Keyboard matrix, row3	KBD_ROW3	KBD_ROW3

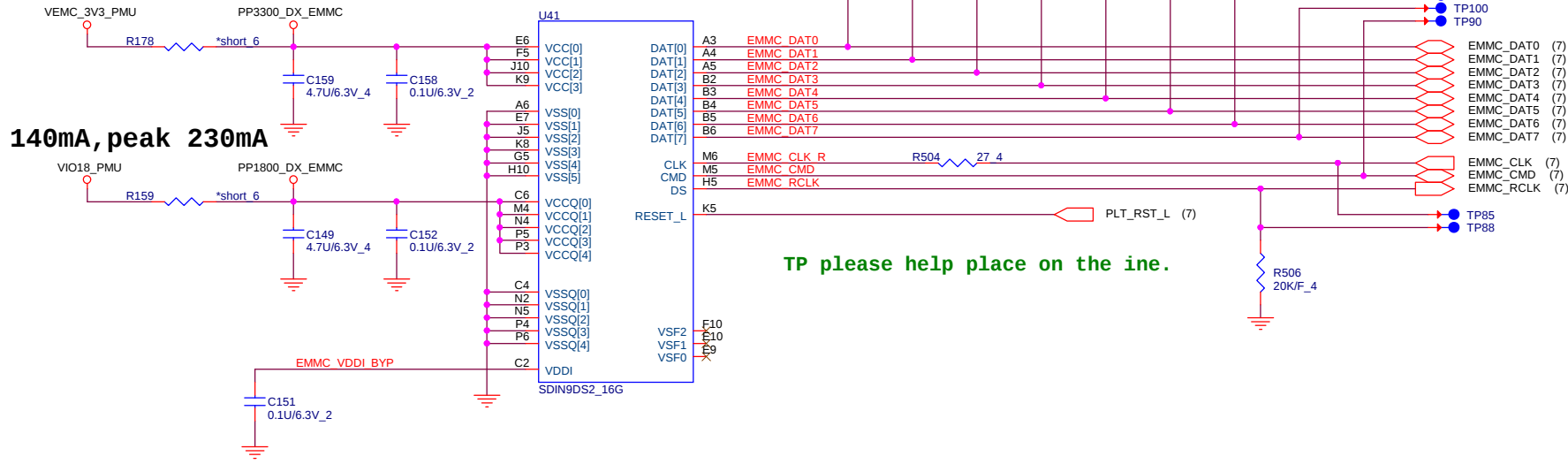


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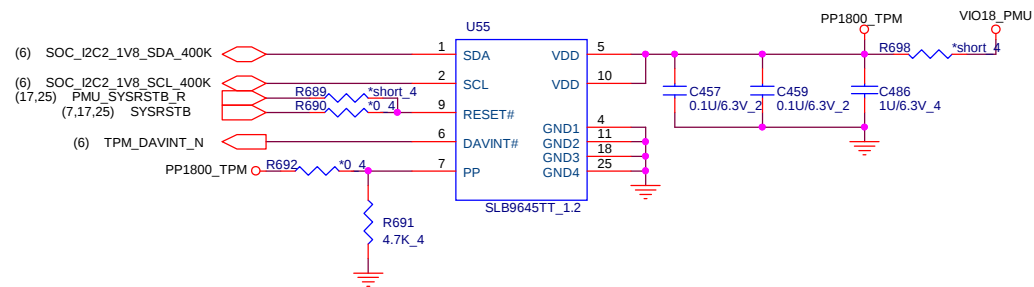
Size	Document Number	Rev
	SERVO	1A
Date:	Friday, July 15, 2016	Sheet 12 of 29

EMMC

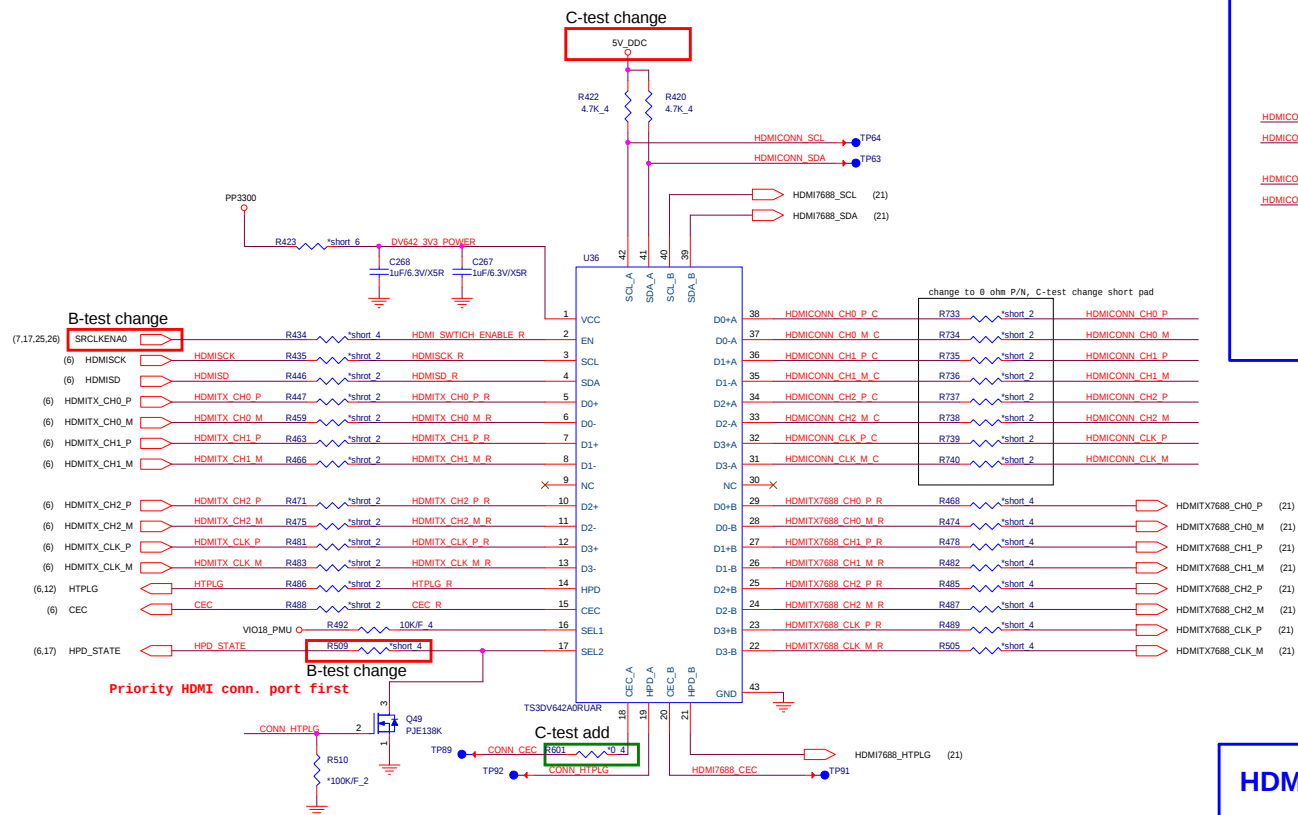
180mA, peak 300mA



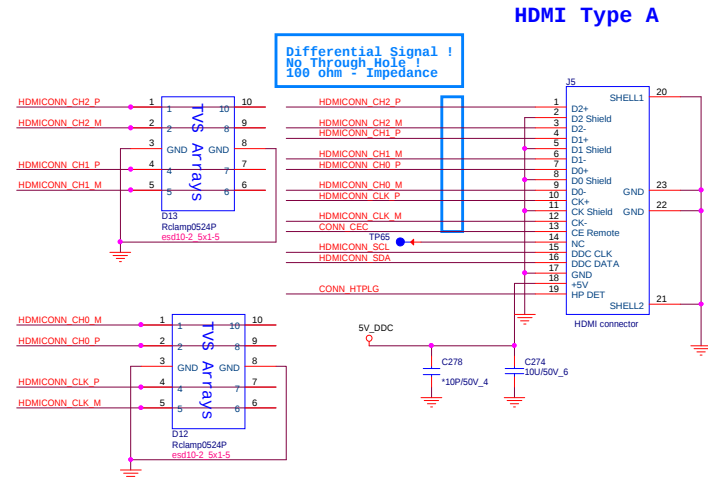
TPM



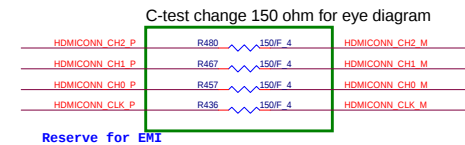
HDMI SWITCH



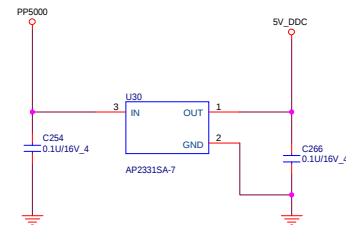
HDMI CONNECTOR



HDMI EMI

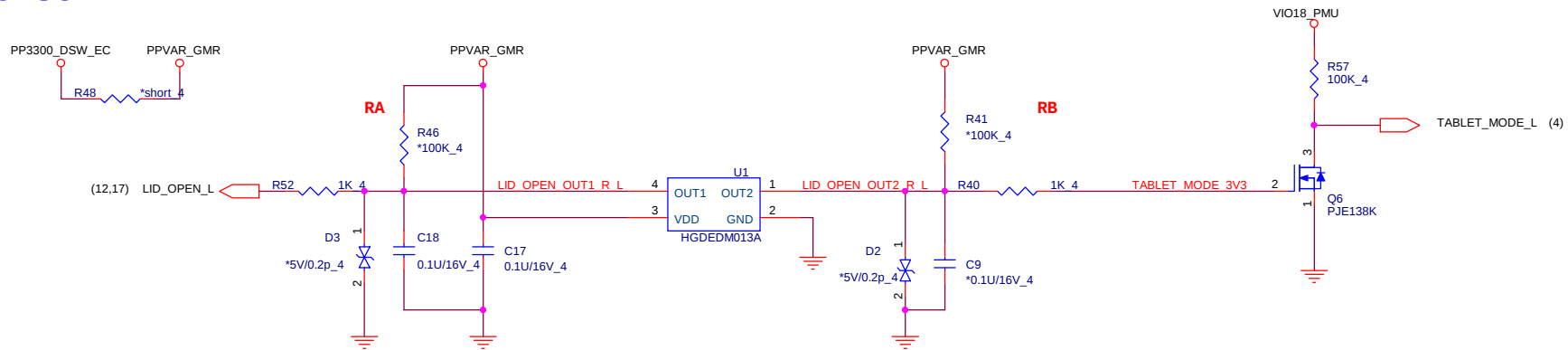


HDMI CURRENT LIMIT

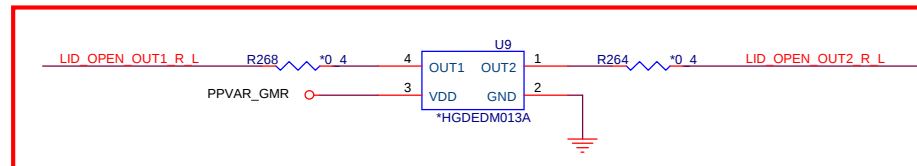


Remove

GMR Sensor



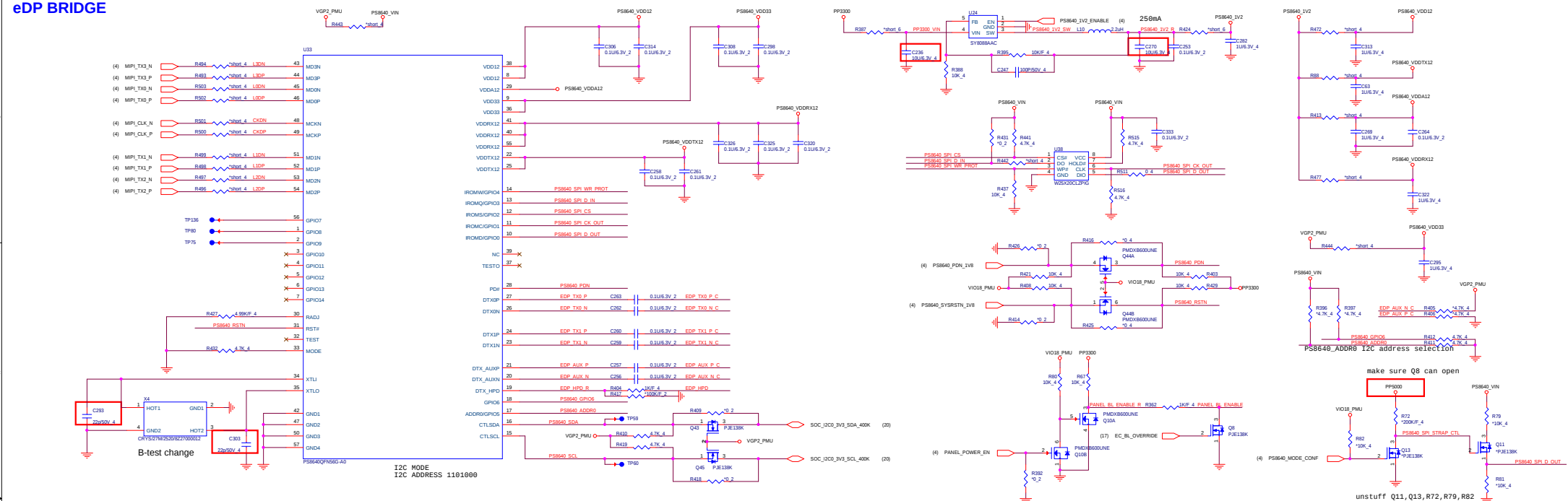
B-test unstuff



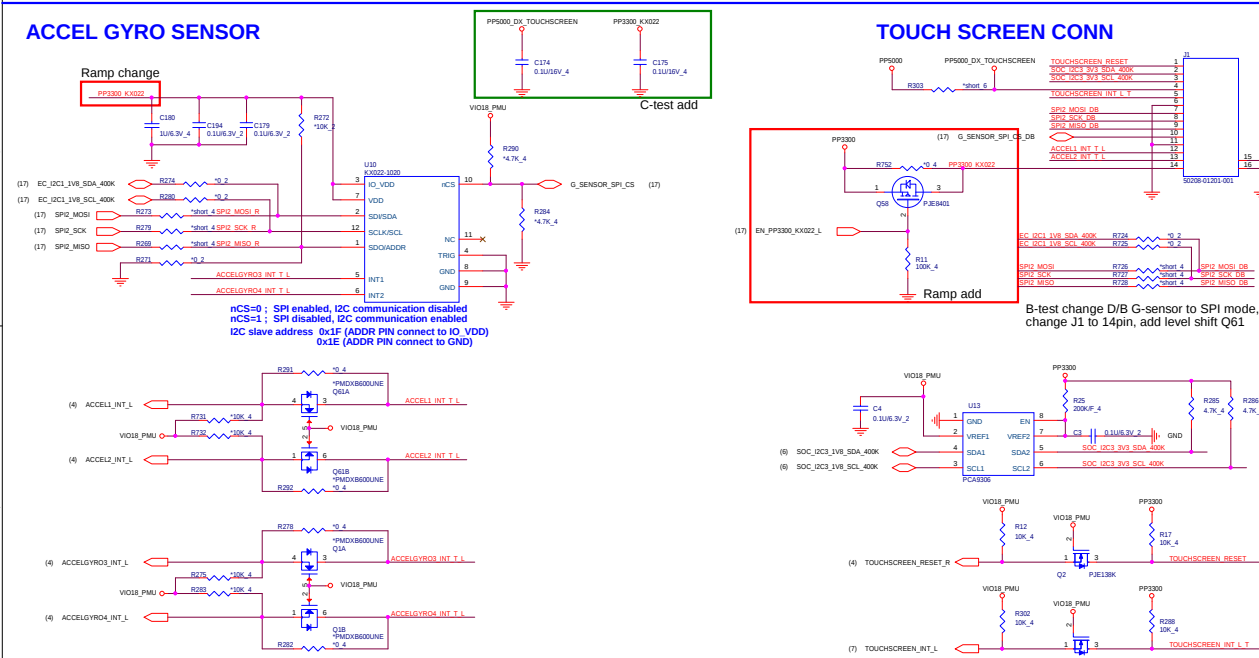
Quanta Computer Inc.
PROJECT : ZSE

Size	Document Number	Rev
	AUDIO 5514 & GMR	1A
Date:	Friday, July 15, 2016	Sheet 15 of 29

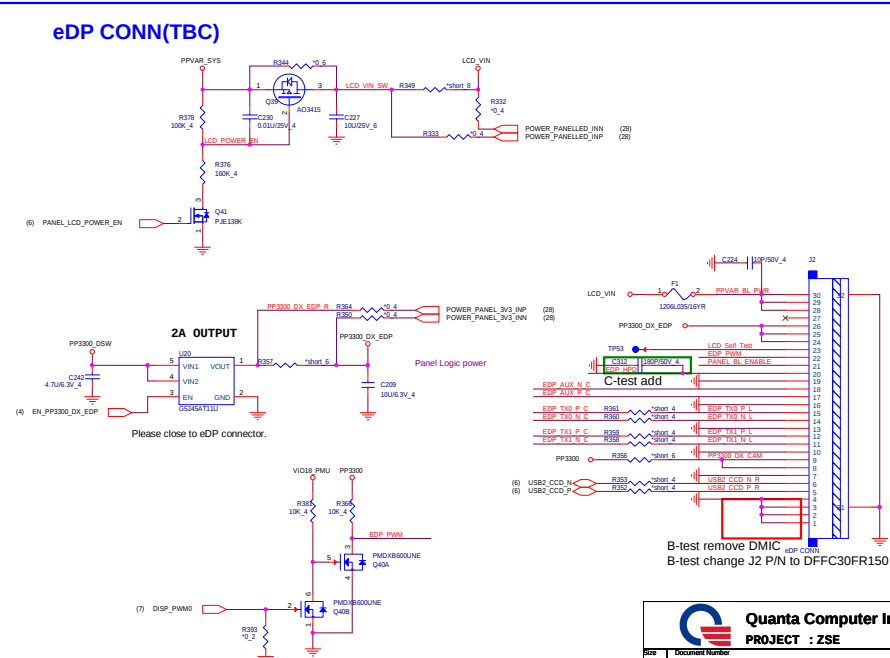
eDP BRIDGE



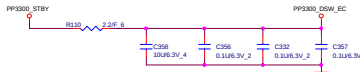
ACCEL GYRO SENSOR



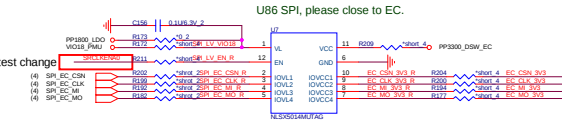
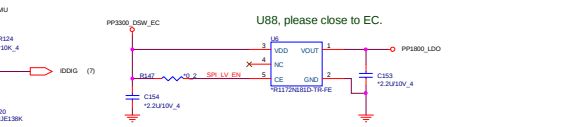
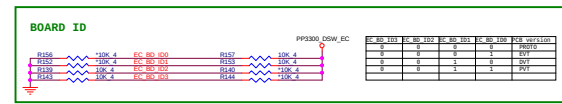
eDP CONN(TBC)



EC (STM32F091VC)

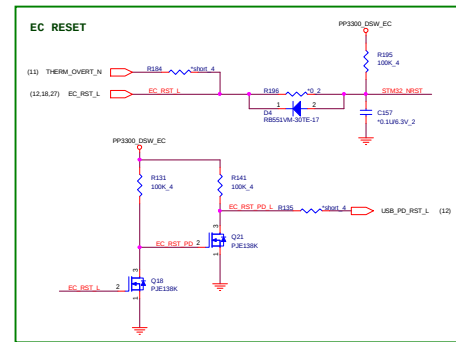
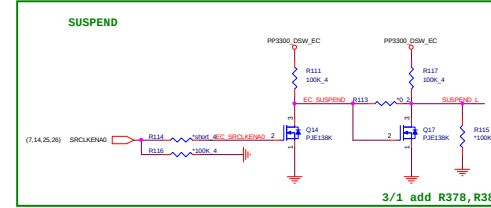
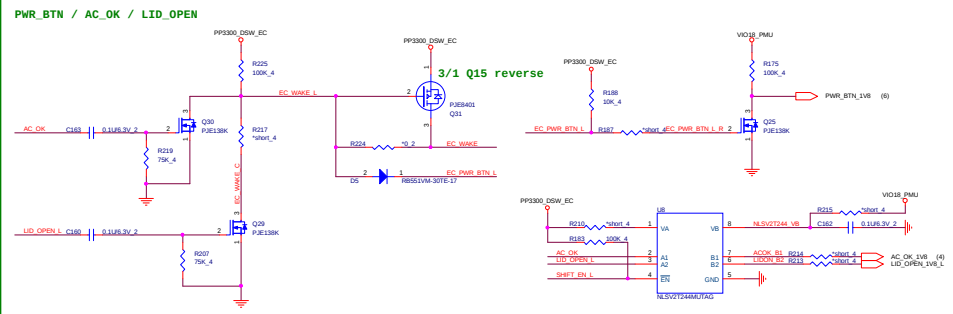
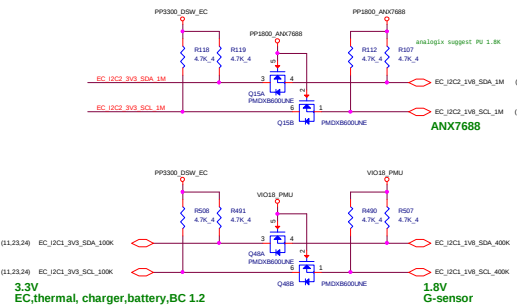


BC1.2 Wake



BC1.2 Control by EC_I2C0_SCL

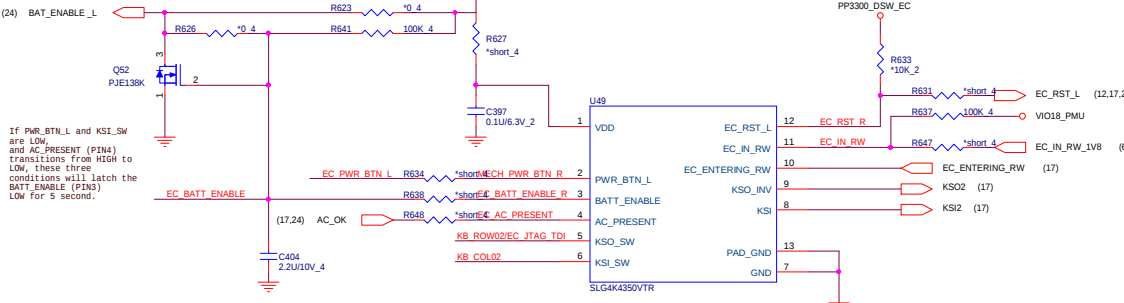
IN		OUT	
S	A	Y0	Y1
L	L	L	Z
L	H	H	Z
H	L	Z	L
H	H	Z	H



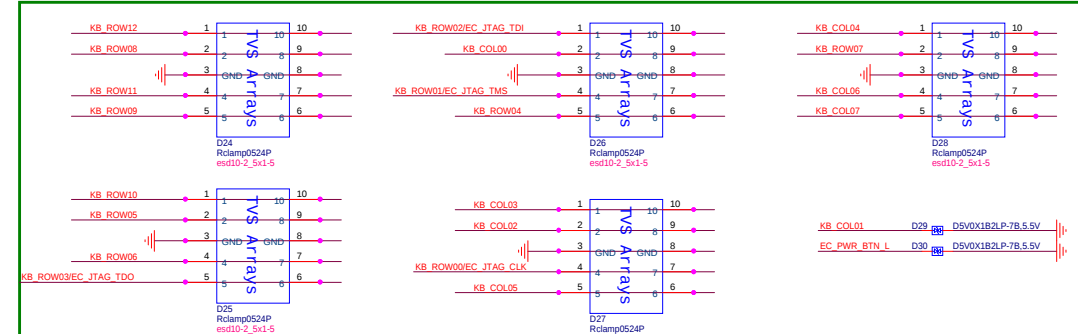
Remove

Reverse for BMI160 I2C interface, DUAL N-MOS

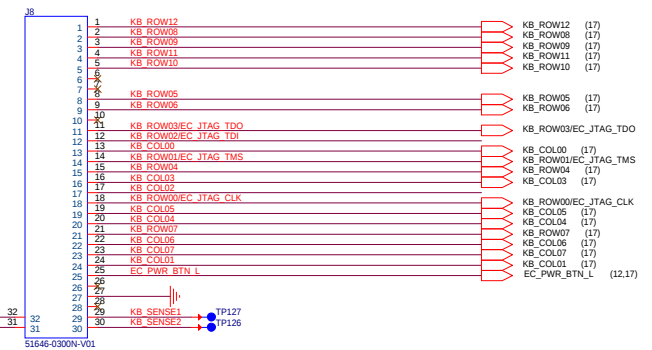
RESET IC



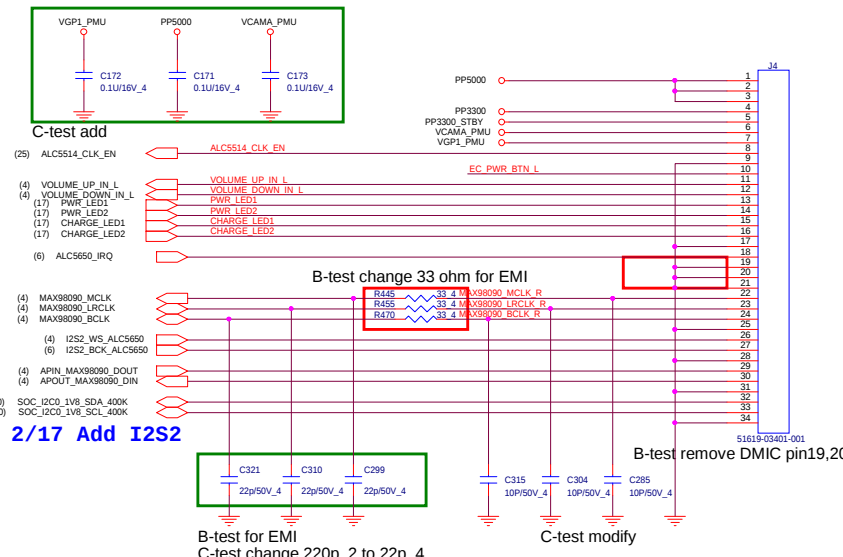
KeyBoard



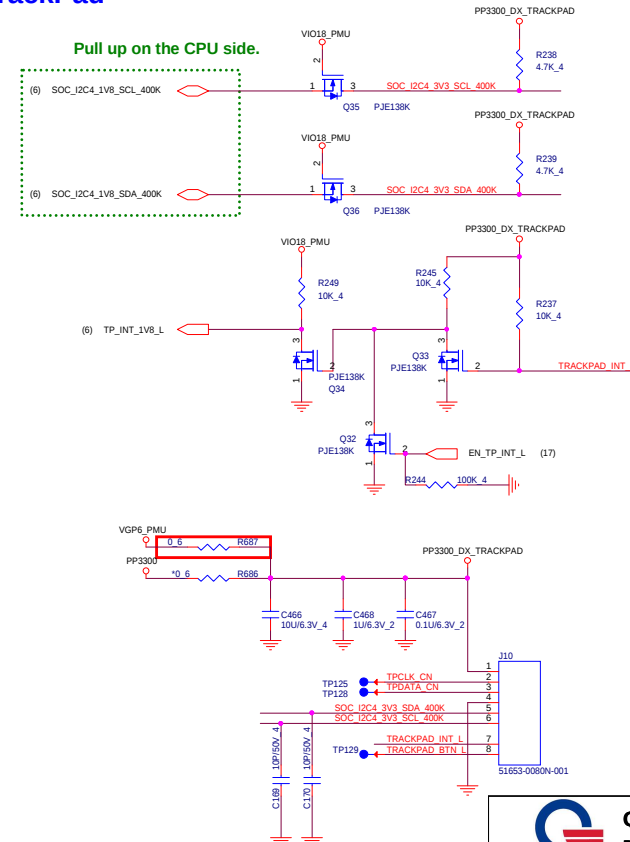
C-test replace 22p to ESD materi



Audio Board Connector

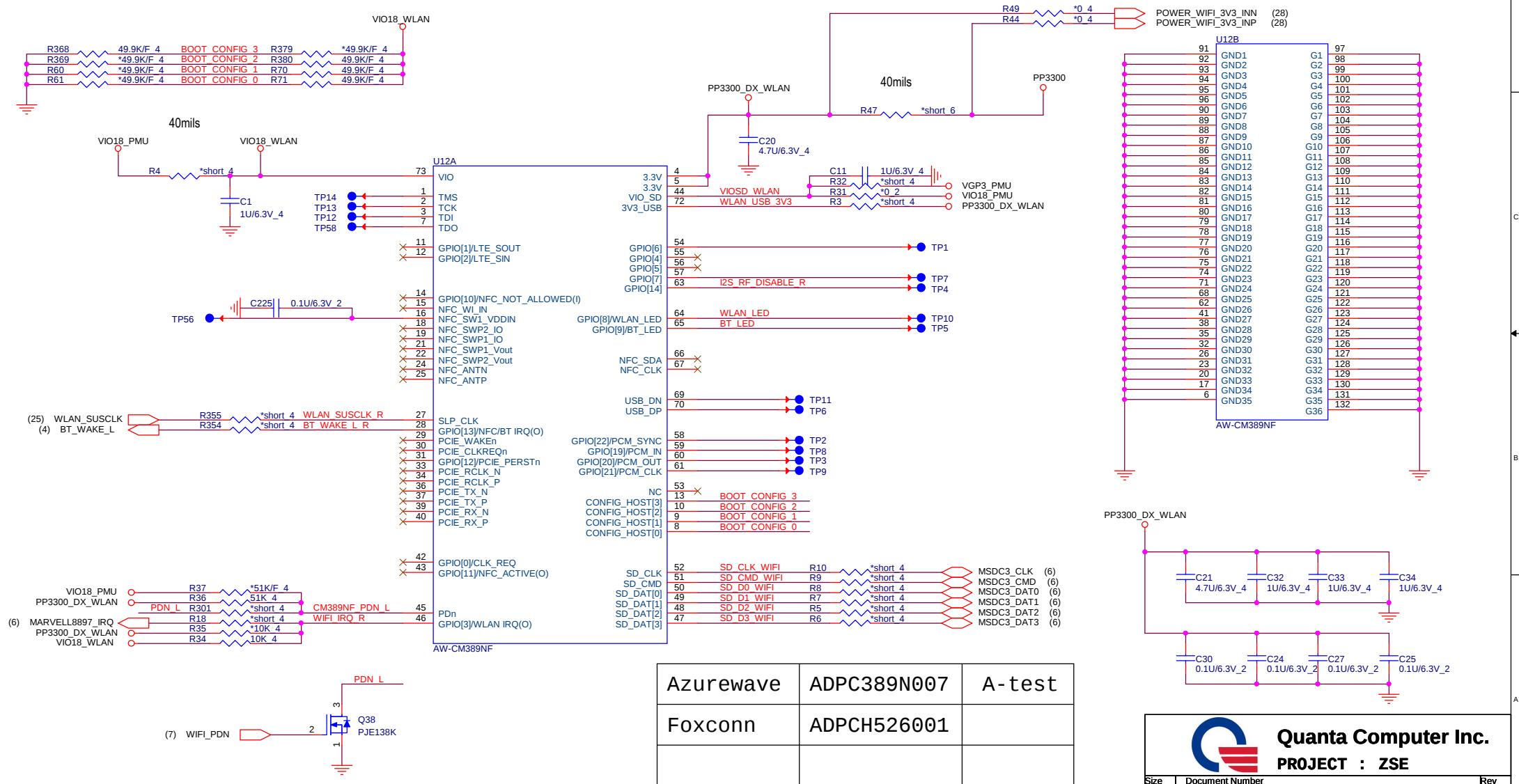


TrackPad



WLAN

Scenario	WLAN	BT/NFC	BT_AMPS	Firmware Download I/F	Firmware Download Mode	Configuration
1	SDIO	SDIO	--	SDIO	Serial	CON[3:0]=b'0001
2	SDIO	SDIO	SDIO	SDIO	Serial	CON[3:0]=b'0111
3	PCIE	UART	--	PCIE+UART	Parallel	CON[3:0]=b'1111
4	PCIE	UART	--	PCIE+UART	Serial	CON[3:0]=b'1100
5	PCIE	USB	USB	PCIE	Serial	CON[3:0]=b'1110

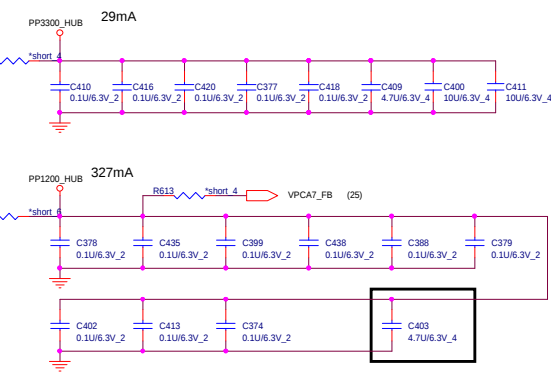
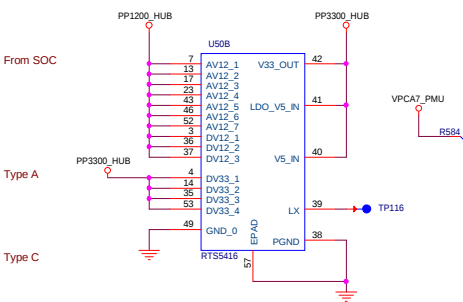
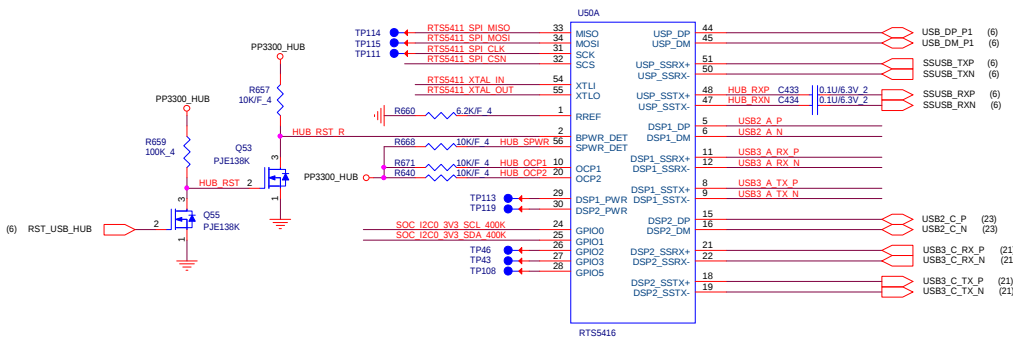


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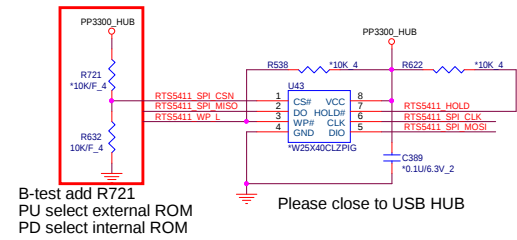
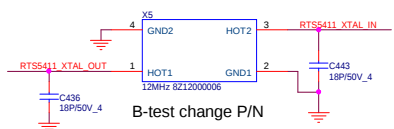
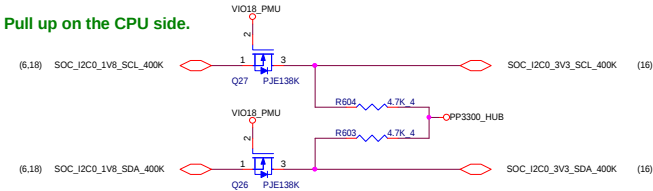
Size	Document Number	Rev
	WLAN	1A

Date	Friday, July 15, 2016	Sheet	19 of 29
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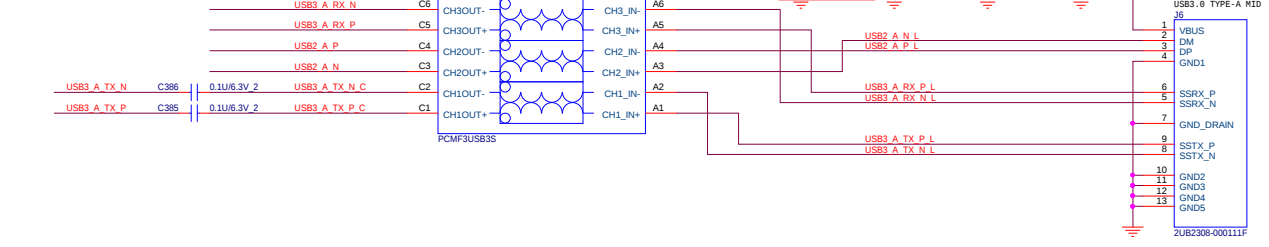
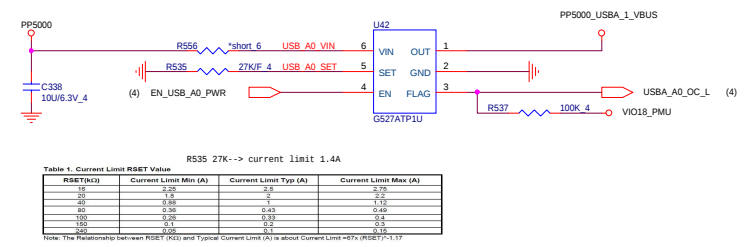
USB HUB



Pull up on the CPU side.



USB3.0 Type A



USB2.0 OTG

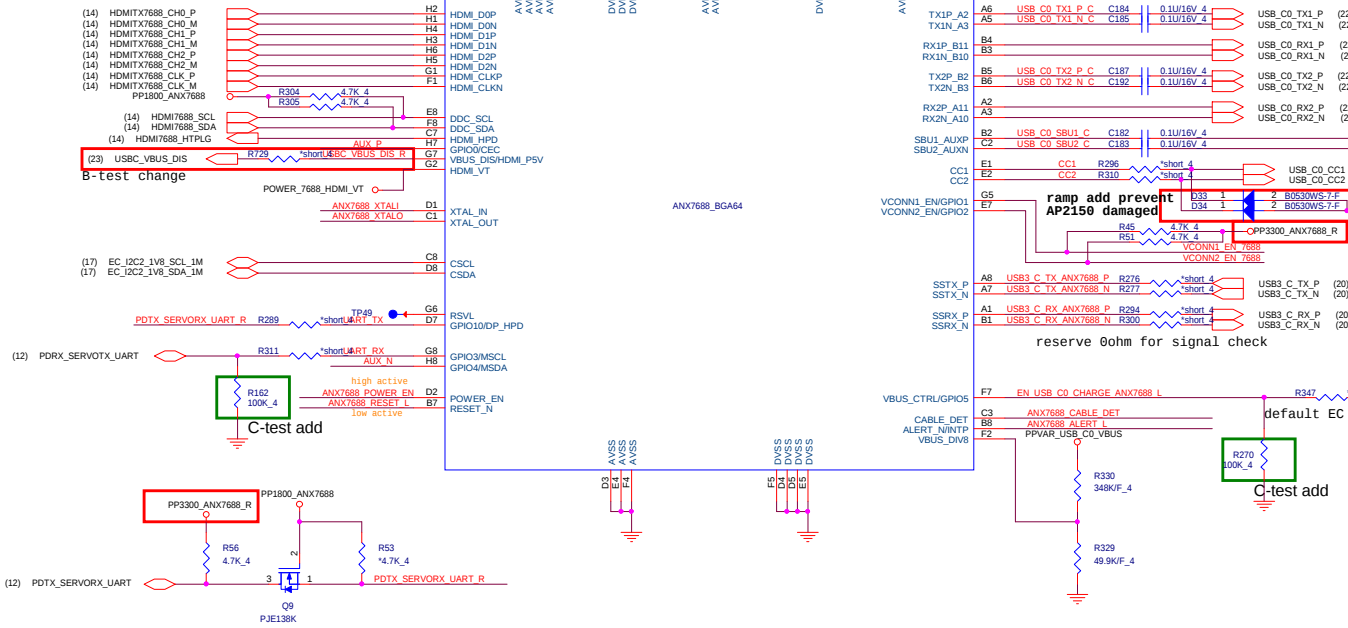
Remove

HDMI TO DP BRIDGE

120mA ,AGX suggest reserve bead

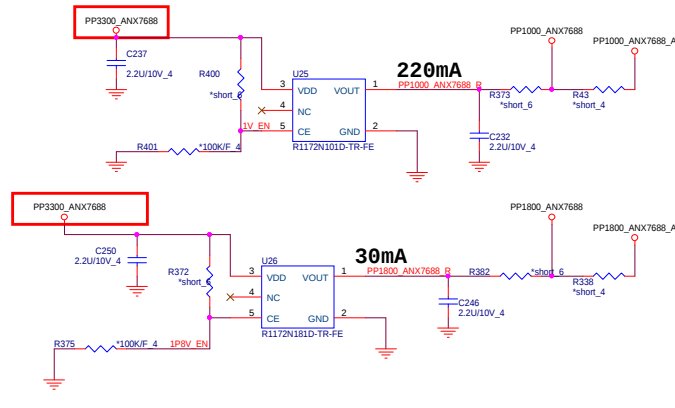
100mA 20mA ,AGX suggest reserve bead

10mA 80mA

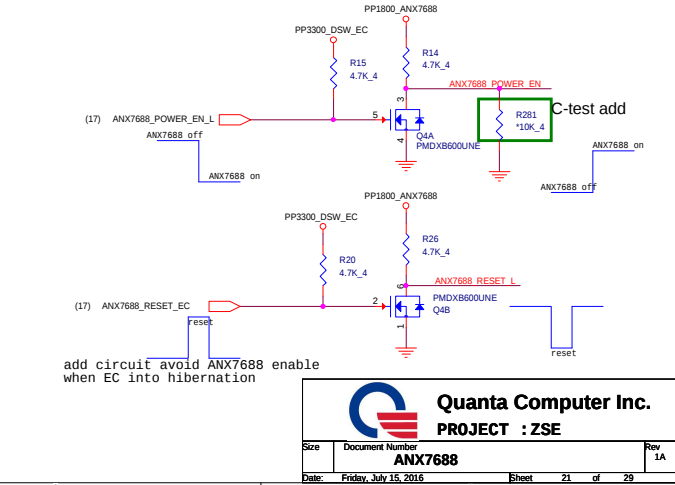
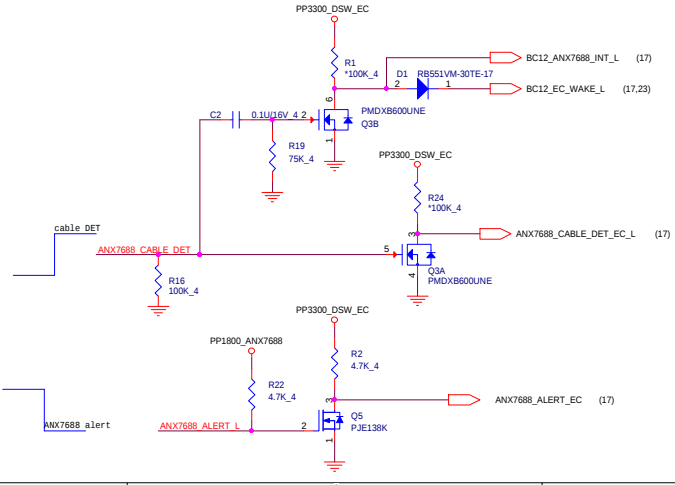
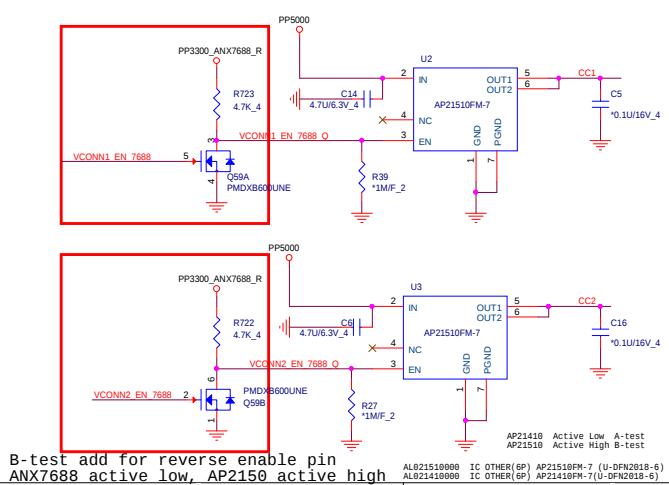
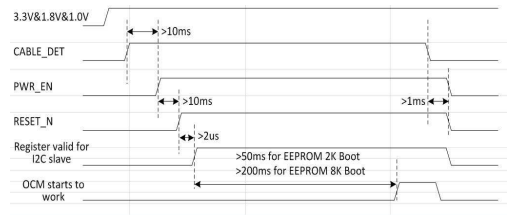
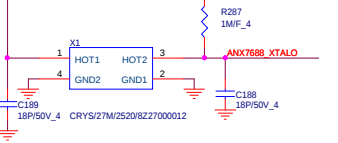


ANX7688 power

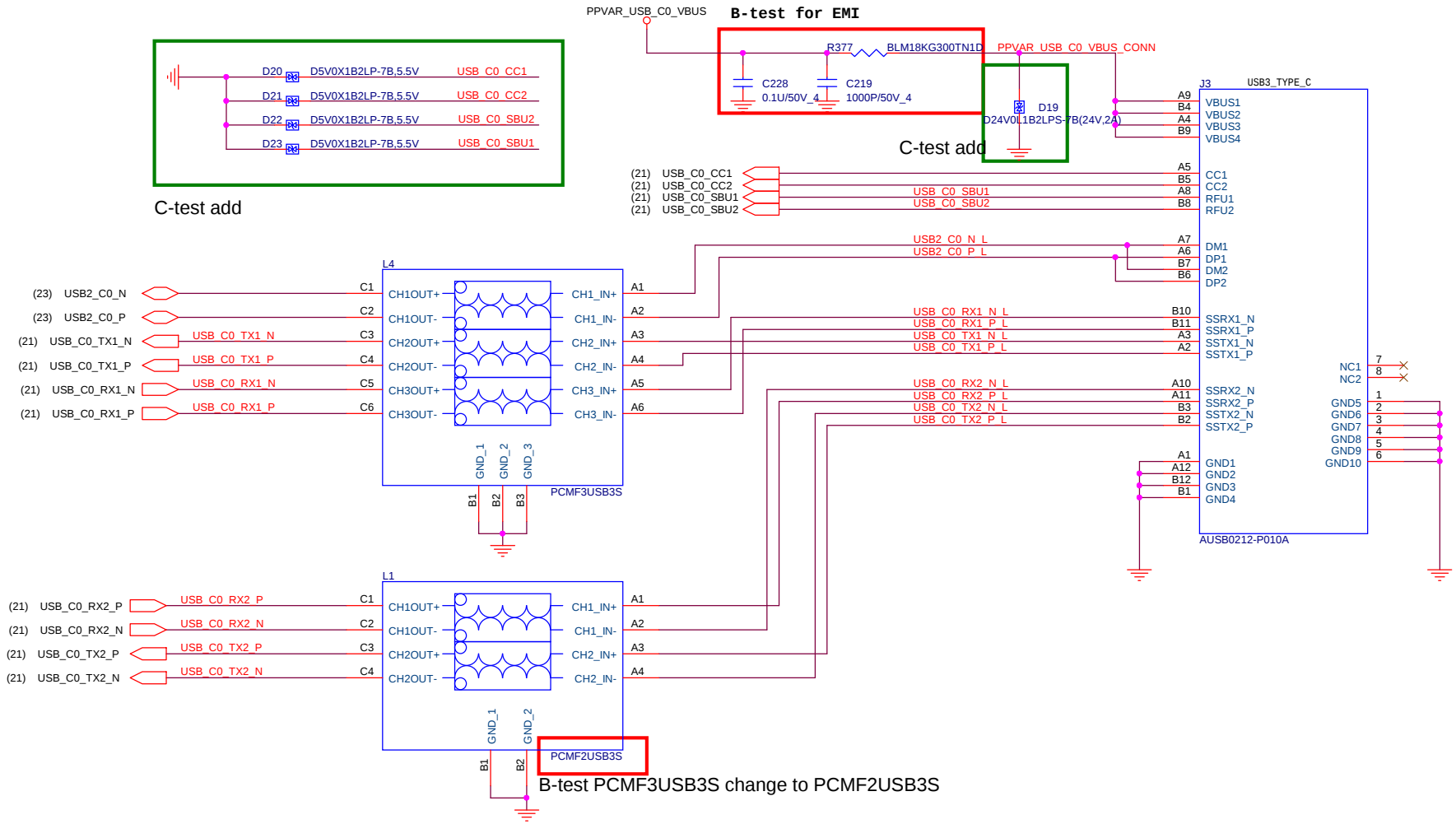
B-test change PP3300_STBY to PP3300_ANX7688



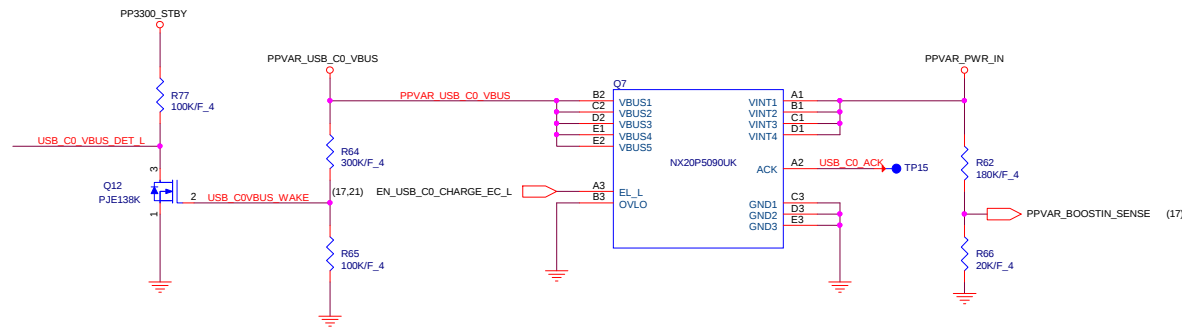
27M X'tal



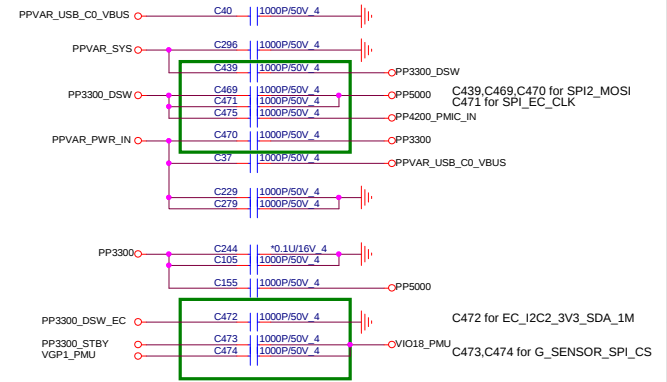
USB C CONNECTOR



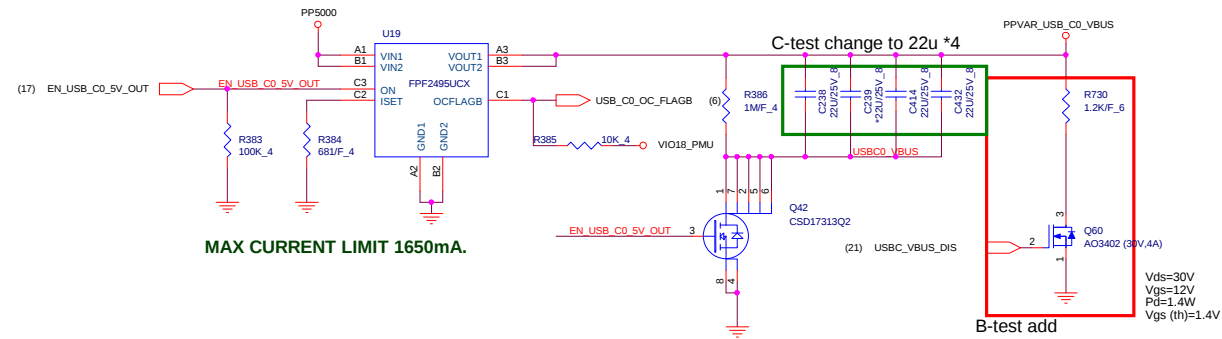
USB C POWER INPUT



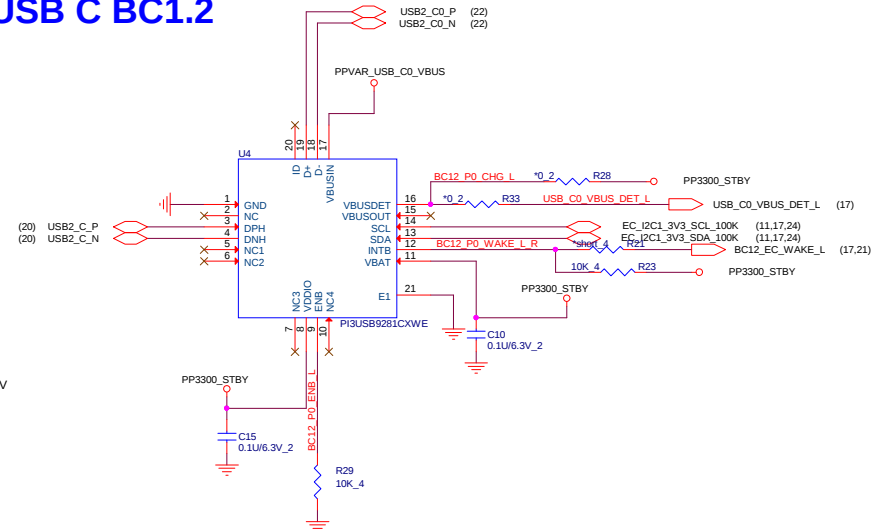
Stitching CAP



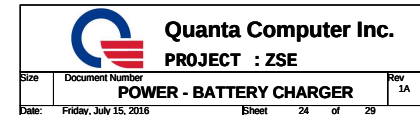
USB C POWER SWITCH



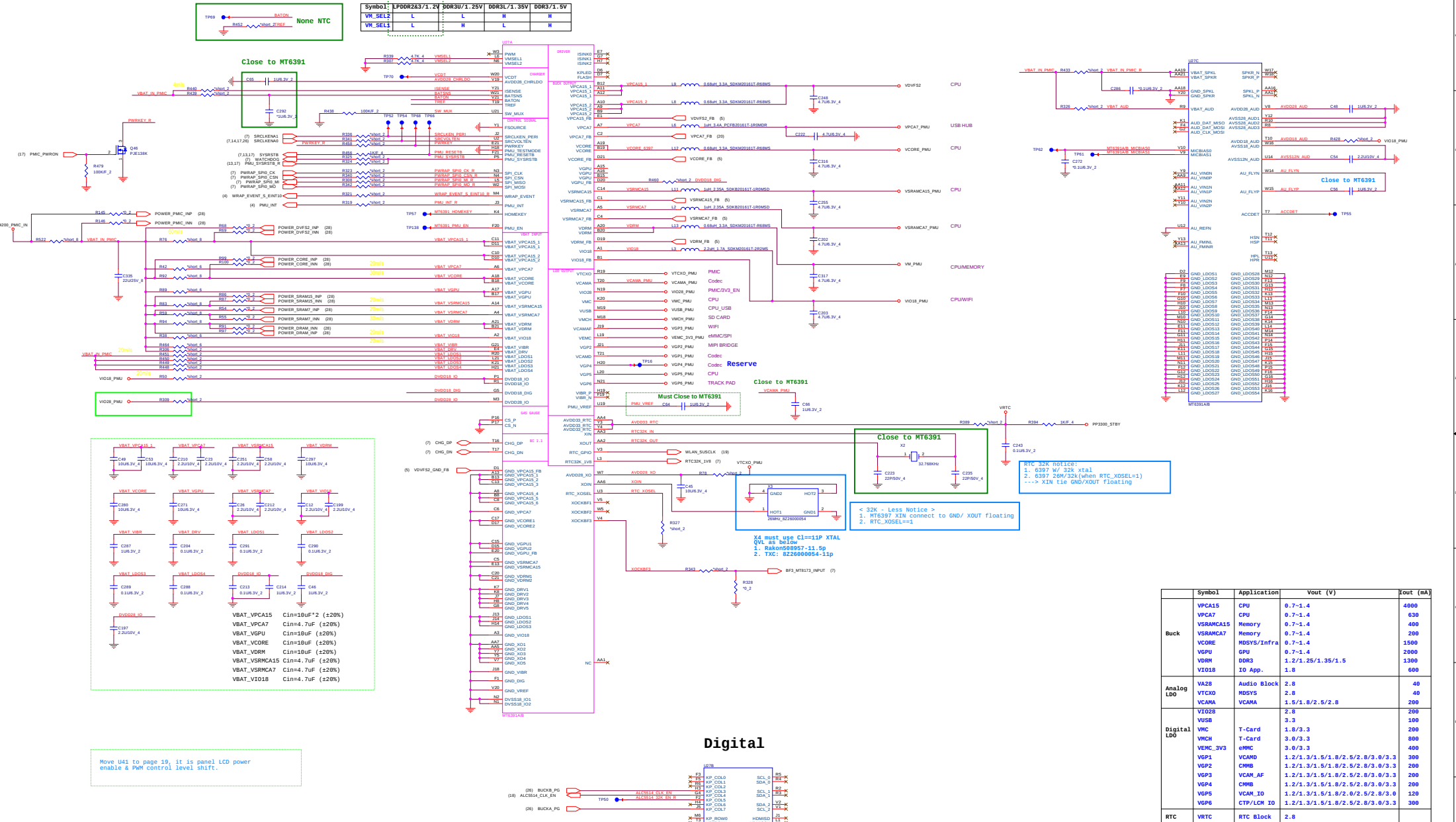
USB C BC1.2



INTERSIL BUCK-BOOST CHARGER

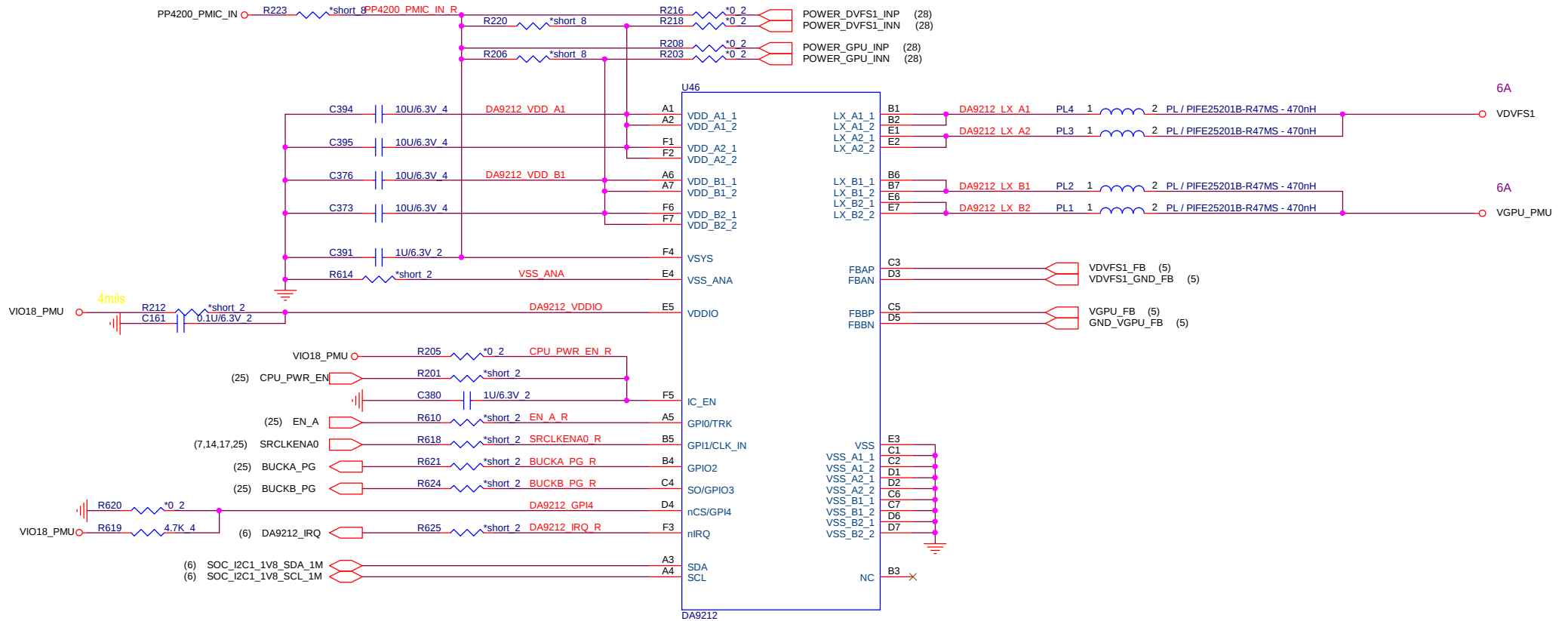


POWER - PMIC MT6391

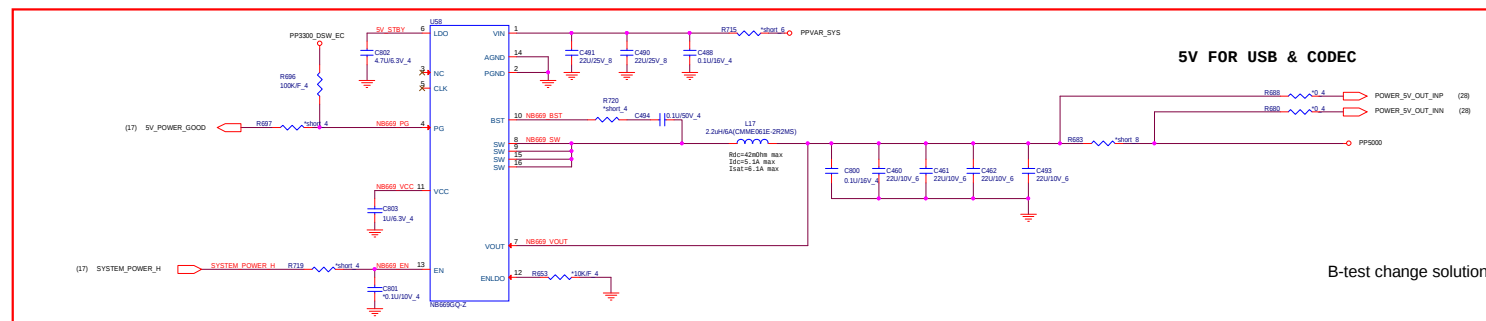
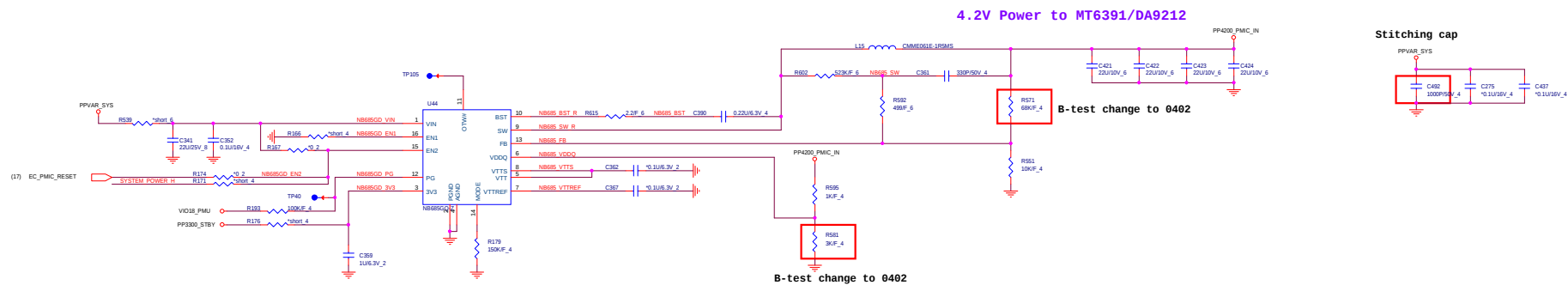


	Symbol	Application	Vout (V)	Iout (mA)
Buck	VP1A5	CPU	0.7-1.4	4000
	VP1CA7	CPU	0.7-1.4	630
	VSR1CA15	Memory	0.7-1.4	400
	VSR1CA147	Memory	0.7-1.4	200
	W10RE	MSDS/VSInfra	0.7-1.4	1500
	YD1U	GPU	0.7-1.4	2000
	VD18	DDR3	1.2/1.25/1.35/1.5	1300
	VT018	IO App.	1.8	600
Analog LDO	VA28	Audio block	2.8	40
	VTCK0	MSDS	2.8	40
	VCAMA	VCAMA	1.5/1.8/2.5/2.8	200
Digital LDO	VT028		2.8	200
	VSU8		3.3	100
	V1C	T-Card	1.8/3.3	200
	V1C8	T-Card	3.0/3.3	800
	W1E1C_3V3	EMC	3.0/3.3	400
	VP11	VP11	1.2/1.3/1.5/1.8/2.5/2.8/3.3/3.3	300
	VP21	CM1B	1.2/1.3/1.5/1.8/2.5/2.8/3.3/3.3	200
	VP31	VCAM_AF	1.2/1.3/1.5/1.8/2.5/2.8/3.3/3.3	200
	VP41	CM1B	1.2/1.3/1.5/1.8/2.5/2.8/3.3/3.3	200
	VP51	VCAM_IO	1.2/1.3/1.5/1.8/2.5/2.8/3.3/3.3	120
VP61	CTP/LCM_IO	1.2/1.3/1.5/1.8/2.5/2.8/3.3/3.3	300	
RTC	VRTC	RTC Block	2.8	

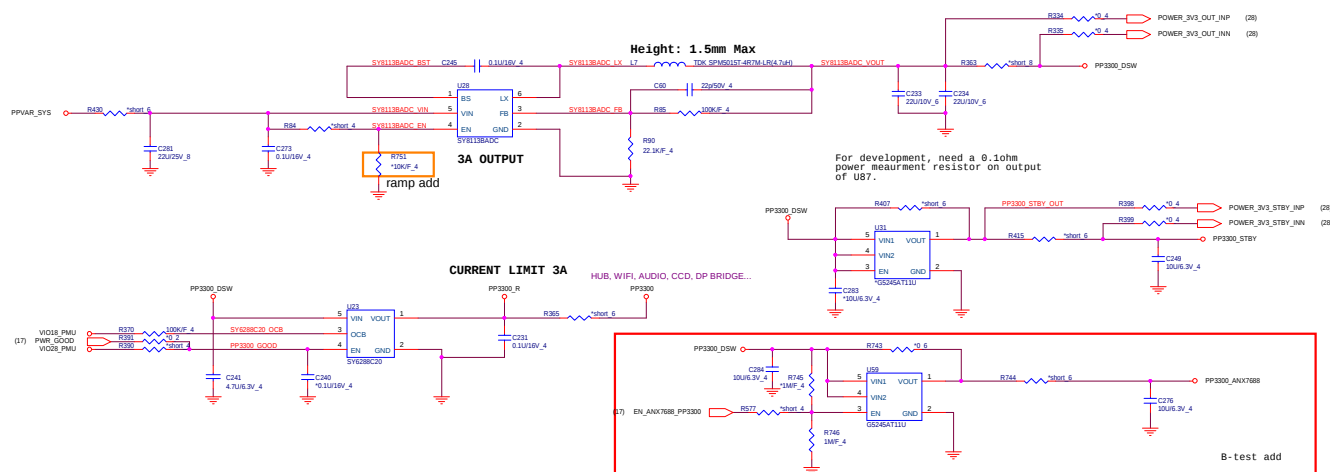
Size	Document Number	Rev
	POWER - PMIC DA9212	1A
Date:	Friday, July 15, 2016	Sheet 26 of 29



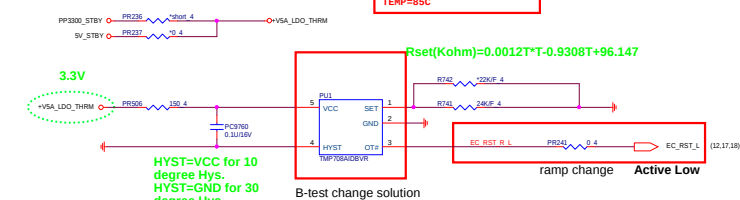
POWER - MPS NB685GQ, 3V/5V

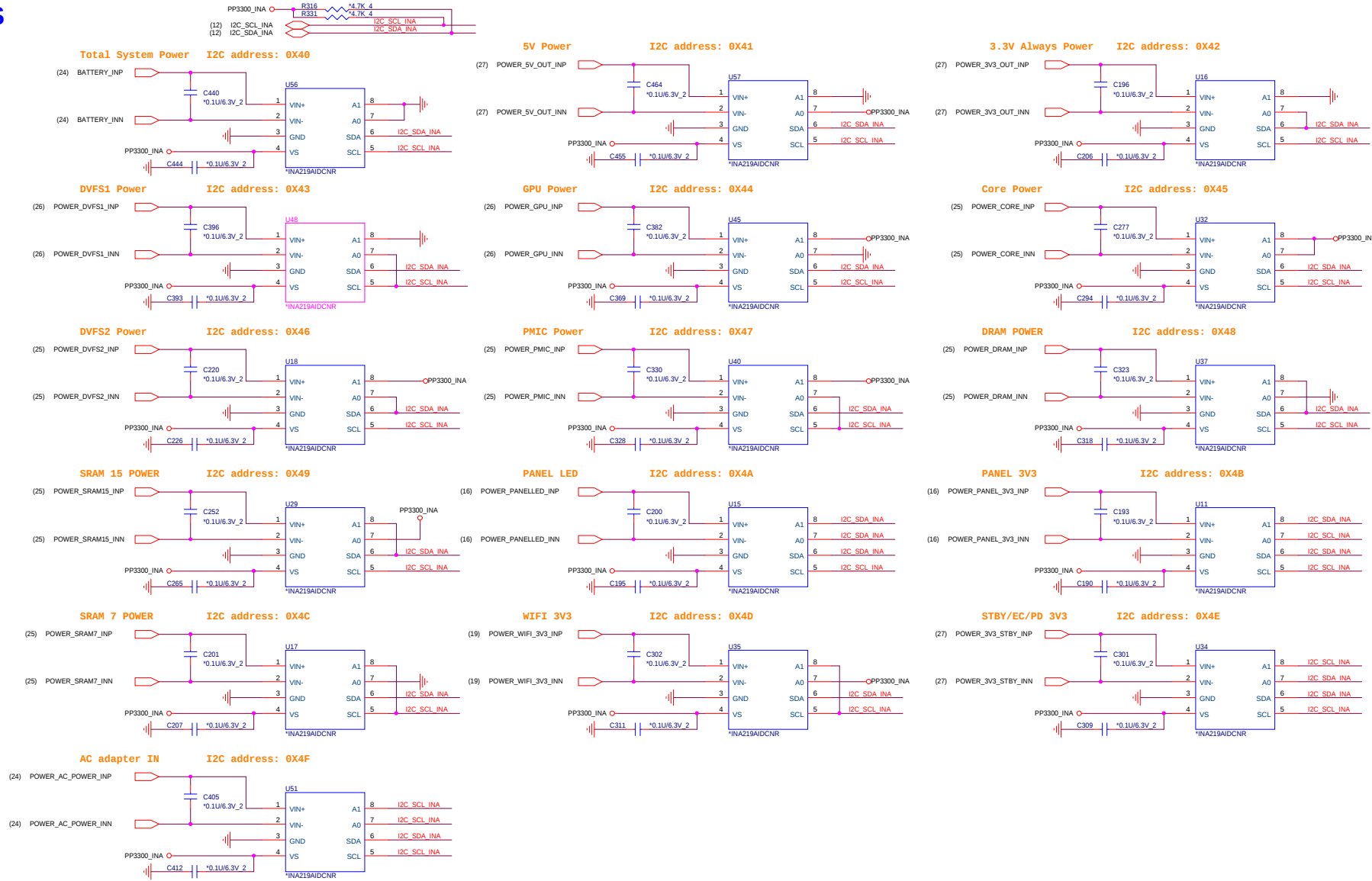


3.3V ALWAYS & PP3300_STBY

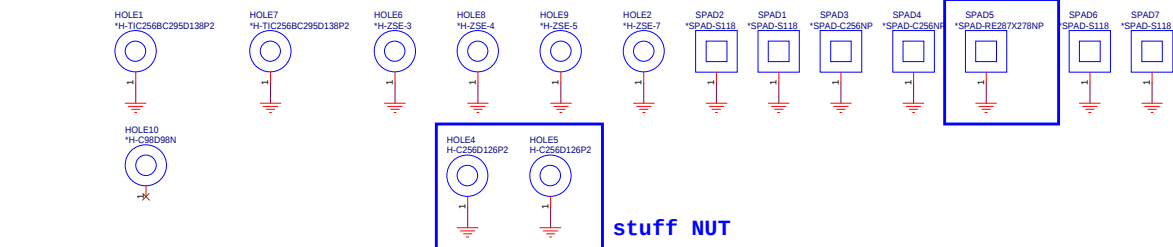


Thermal Protector



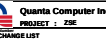


MOUNTING HOLES



MODEL	REV	CHANGE LIST	From	To
ZSE MB	1A		1	3A
B	3/9	page 27 :change R571,R581 to 0402 package	2	3A
		page 6 :change R547 to 0402 package	3	3A
		page 21 :add Q59 for reverse VCONN_EN	4	3A
		page 24 :change J11.3 net name to BAT_DISABLE_L	5	3A
		page 20 :add R721 for USB HUB ROM select	6	3A
		page 18,16 :remove DMIC data,CLK signal.	7	3A
		page 24 : SWAP battery CONN J11	8	3A
			9	3A
			10	3A
			11	3A
3/21		page 23 : change R386 to 1.2K	12	3A
		page 22 : USB_CO_CCI,USB_CO_CC2 no pass through L1,change L1 PIN	13	3A
		page 15 : unstuff R268,R264,U9 avoid two GMR work together	14	3A
			15	3A
			16	3A
			17	3A
			18	3A
			19	3A
			20	3A
			21	3A
3/23		page 14 : U36 EN pin change connect to SRCLKENAO, U39 AB32 pin reserved as TP	22	3A
		page 17 : U39 AC34 pin reserved as TP, change R211 pin connect to SRCLKENAO	23	3A
		page 23 : C238,C239 change to 0805 package	24	3A
		add R730,Q50 Vbus discharge circuit control signal is from ANX7688 (U14.G7)	25	3A
		page 16 : change D/B G-sensor to SPI mode,change J1 to 14pin, add level shift Q61	26	3A
		page all : change 0 ohm 0201 to 0402	27	3A
		R15,R16,R7,R8,R9,R10,R18,R32,R44,R49,R301,R354,R355	28	3A
		R21,R84,R90,R386,R399,R680,R688,R697,R713,R720,R281	29	3A
		R295,R398,R314,R123,R135,R154,R159,R160,R169,R172,R177	30	3A
		R182,R184,R185,R187,R192,R199,R202,R204,R211,R213,R214	31	3A
3/28		R215,R217,R186,R171,R176,R227,R228,R231,R238,R248,R262	32	3A
		R717,R928,R723,R279,R332,R333,R360,R364,R542,R546,R672	33	3A
		R635,R639,R642,R664,R665,R666,R667	34	3A
			35	3A
			36	3A
			37	3A
			38	3A
			39	3A
			40	3A
			41	3A
3/29		page 6 : R129 0 ohm stuff, R126 change to 1uF cap for Efuse application	42	3A
		change PIN reduce material item R110,R506,R716,C180,C3,C4,C20,C21,C403,C420	43	3A
		page 14 : change symbol C278,C279,C284,C280,C300,C305,C307,C312 to R733-R740	44	3A
		page 6 : R197,R198 unstuffed, hdmi mux switch is bypass circuit	45	3A
		page 11 : unstuff thermal sensor U47,Q47,Q54	46	3A
			47	3A
			48	3A
			49	3A
			50	3A
			51	3A
3/29		page 16 : change eDP J2 CONN PIN	52	3A
		page 6 : remove R577,R162	53	3A
		page 27 : change thermal protect solution ,add PU1	54	3A
		page 21,25 : change X5 crystal PIN, fine tune C	55	3A
		page all : change 0 ohm 0201 to short pad	56	3A
		R127,R177,R182,R192,R194,R199,R200,R202,R204,R246,R250,R251	57	3A
		R252,R253,R254,R255,R256,R257,R259,R260,R263,R265,R266,R267	58	3A
		R435,R446,R447,R459,R463,R466,R471,R475,R481,R483,R486,R489	59	3A
		R701,R702,R703,R704,R705,R706,R707,R708,R709,R710,R711,R712,R713	60	3A
		page all : change resistor 0201 to 0402	61	3A
3/30		R12,R139,R140,R143,R144,R152,R153,R156,R157,R17,R193,R23,R237	62	3A
		R245,R249,R275,R283,R288,R29,R302,R334,R335,R366,R381,R383,R383	63	3A
		R384,R385,R403,R408,R471,R453,R434,R468,R474,R478,R482,R485,R487	64	3A
		R489,R505,R506,R537,R558,R576,R62,R64,R65,R651,R655,R656,R66,R661	65	3A
		R662,R663,R669,R67,R670,R674,R677,R678,R681,R682,R684,R685,R696	66	3A
		R714,R729,R731,R732,R77,R80	67	3A
		page 24 : change Z2U PIN to CH6224M8A00 for cost down	68	3A
		C427,C428,C429,C430,C448,C452,C453,C454,C489	69	3A
		page 24 : change R673,R568 PIN for cost down	70	3A
		page 27 : add U59 load switch for U25,U26 Vin	71	3A
3/29		page 21 : remove R348,R374,R402	72	3A
		Rename net name from ANX7688_IV_EN to EN ANX7688_PP3300	73	3A
		R56 pull high power rename to PP3300_ANX7688_R	74	3A
			75	3A
			76	3A
		page 27 : add R754 turn on output while EC isn't in control.	77	3A
		page 14 : del U30,stuff R480,R467,R457,R436 for EMI	78	3A
		page 11 : Remove R653 to avoid potential risk of AP damage from PP3300.	79	3A
		stuff C164,C165,C166,C176,C177,C178 5.6p for EMI	80	3A
		page 12 : R257 power name rename to PP3300_ANX7688	81	3A
3/30		page 18 : R445,R455,R470 change to 330,C321,C310,C299 change to 200PF for EMI	82	3A
		page 22 : R377 stuff BLM188C300TN1, C129 stuff 1000p,C228 0.1u for EMI	83	3A
		page 23 : C40,C37,C105,C155,C492 change to 1000p for EMI	84	3A
		add C229,C279,C296,C305 Sitching cap	85	3A
			86	3A
		page 20 : change X5 crystal PIN,C293,C303 to 22p	87	3A
		page 27 : change PP5000 solution to NB669	88	3A
		page 16 : unstuff Q11,Q13, these circuit is just a backup update R640 for solution	89	3A
		page 20 : unstuff U43, R721,R622,R538,C389,stuff R682 to use internal ROM	90	3A
		page 6 : Change USB CCD signal to MT8173 USB Port0	91	3A
4/1			92	3A
			93	3A
			94	3A
			95	3A
			96	3A
			97	3A
			98	3A
			99	3A
			100	3A
			101	3A
4/6		page 27 : add 1M R746 ,R653	102	3A
		page 16 : change C270,C236 to 0402	103	3A
		page 6 : change R462,R453 to 2.2K	104	3A
			105	3A
			106	3A
			107	3A
			108	3A
			109	3A
			110	3A
			111	3A
C		page 14 : R420,R422 pull high to 5V, DDC	112	3A
		page 20 : change USB HUB U50 to RTS5416	113	3A
		page 11 : change J7 SD CONN PIN LTS AU580212-P010A	114	3A
		page 17 : add ESD protect C300,R586,C312,R587,C307	115	3A
		5/6 page 23 : change C238,C239 to 22u, add C414,C432 22u.	116	3A
		page 4 : add EMAC W [2-9]	117	3A
		5/13 page 22 : Add ESD protect D19-D23 on type C USB.	118	3A
		5/16 page 21 : Add R281,R270,R162,R297,R295 avoid floating status of those pins	119	3A
		5/18 page 22 : change .33 type C footprint	120	3A
		page 14,17,21 : Modify nets name from	121	3A
5/19		EC_12C2_1V8_SDA_400K to EC_12C2_1V8_SDA_1M	122	3A
		EC_12C2_1V8_SCL_400K to EC_12C2_1V8_SCL_1M	123	3A
		EC_12C2_3V0_SDA_400K to EC_12C2_3V0_SDA_1M	124	3A
		EC_12C2_3V0_SCL_400K to EC_12C2_3V0_SCL_1M	125	3A
		USB_DP_HPD to HPD_STATE	126	3A
			127	3A
		page 24 : stuff C452, C427, C448, C429, C454, C453, C456 for voltage stable	128	3A
			129	3A
			130	3A
			131	3A
5/20		page 24 : change Q57 PIN for Vgs rating issue	132	3A
		page 24 : add C487,C483,C487,C485,C486 22u, 8	133	3A
		change C381, C382, C366, C398 to 22u, 8 for buzzed noise issue	134	3A
		page 21 : change U14 PIN to ANX7688BH-AC	135	3A
			136	3A
			137	3A
			138	3A
			139	3A
			140	3A
			141	3A
5/23		page 21 : change D18,D6-D11 PIN BC5V0X1B201	142	3A
		page 18 : replace 22p to ESD material D24-D30	143	3A
		change C321,C310,C259 to 22n 0402	144	3A
			145	3A
			146	3A
			147	3A
			148	3A
			149	3A
			150	3A
			151	3A
5/24		page 16,18 : add decoupling cap C171,C174 for D/B conn power	152	3A
		page 23 : add C409,C469-C475 Sitching CAP	153	3A
		page 14 : change R480,R467,R457,R436 150 ohm for HDMI eye diagram.	154	3A
			155	3A
			156	3A
			157	3A
			158	3A
			159	3A
			160	3A
			161	3A
ramp		page 27 : add PR242 reserve reset PP3300_DSW path.	162	3A
		page 24 : change Q50 PIN to BAMB7350003 meet U54 driving current.	163	3A
		page 3 : change CPU U39 to B/S PIN	164	3A
		page 25 : change PMIC U27 to B/S PIN	165	3A
			166	3A
		page 21 : change C5,C16 to 0.1u,4, add D33,D34 prevent AP21510 damaged	167	3A
		page 16 : add Q58 control G-sensor power, control from EC J11 pin.	168	3A
		page 27 : change PU1 OT# pin to EC_RST_L,remove PR242	169	3A
		page 24 : change U54 pin 16 to PPVAR_PWR_IN_R	170	3A
		change R676 to 178K for EMI issue	171	3A
7/12		page 11 : change Q51 gate pin to PP3300	172	3A
		page 16 : un-stuff Q1,Q61,R731,R732,R275,R283, prevent leakage from AP during EC turn off the G sensor's power.	173	3A
			174	3A
			175	3A
			176	3A
			177	3A
			178	3A
			179	3A
			180	3A
			181	3A
7/13		page 21 : change U14 ANX7688 PIN	182	3A
			183	3A
			184	3A
			185	3A
			186	3A
			187	3A
			188	3A
			189	3A
			190	3A
			191	3A
7/15		page 18 : change r686 to 0 ohm, stuff R687	192	3A
			193	3A
			194	3A
			195	3A
			196	3A
			197	3A
			198	3A
			199	3A
			200	3A
			201	3A

Part Number :	Project Model :	DOC: 204
APPROVED BY :	DRAWING BY :	DATE :
		CHANGE LIST



Quanta Computer Inc.
PROJECT : ZSE
CHANGE LIST