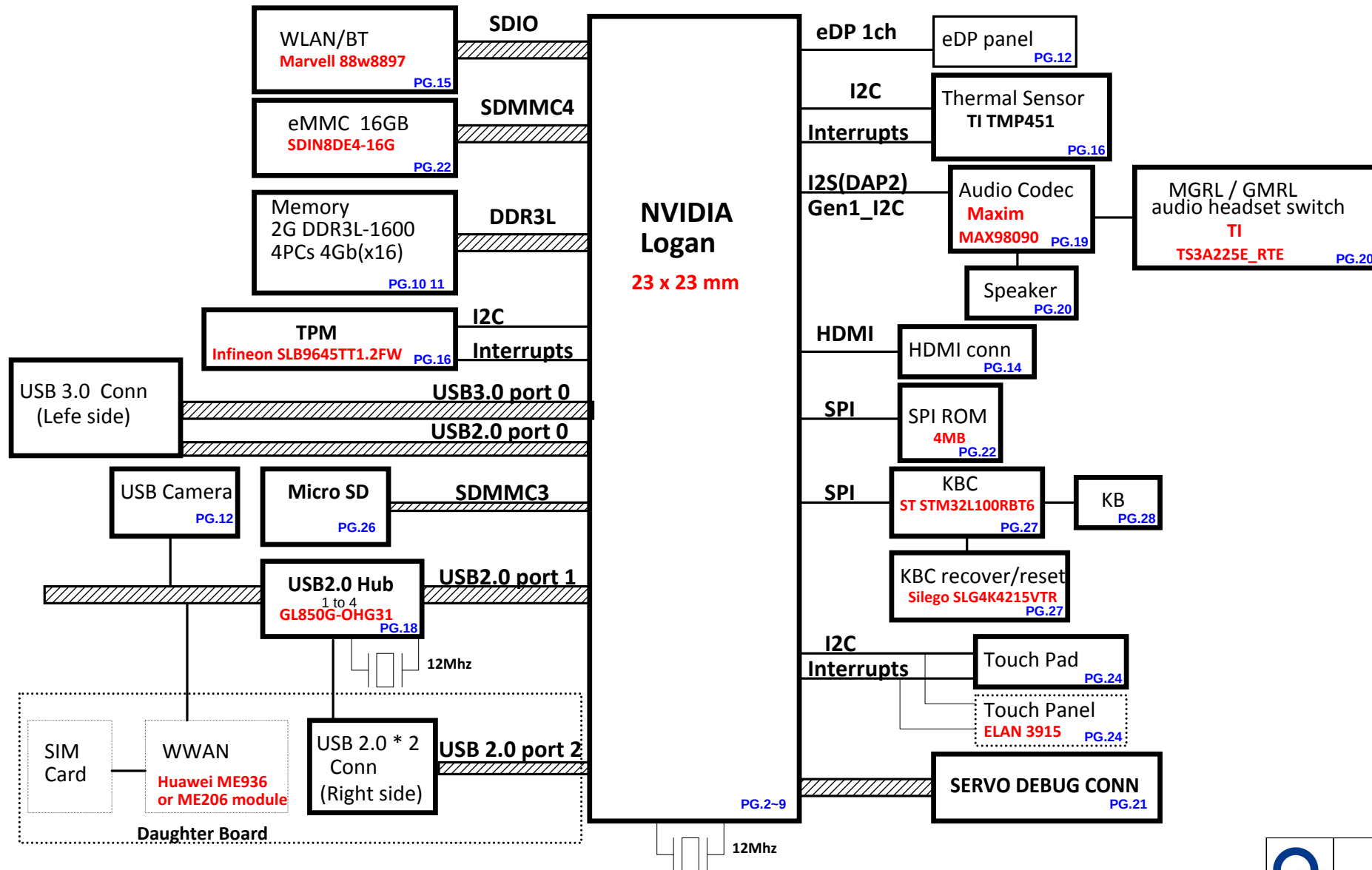


Baileys Tegra5 Logan Platform Block Diagram

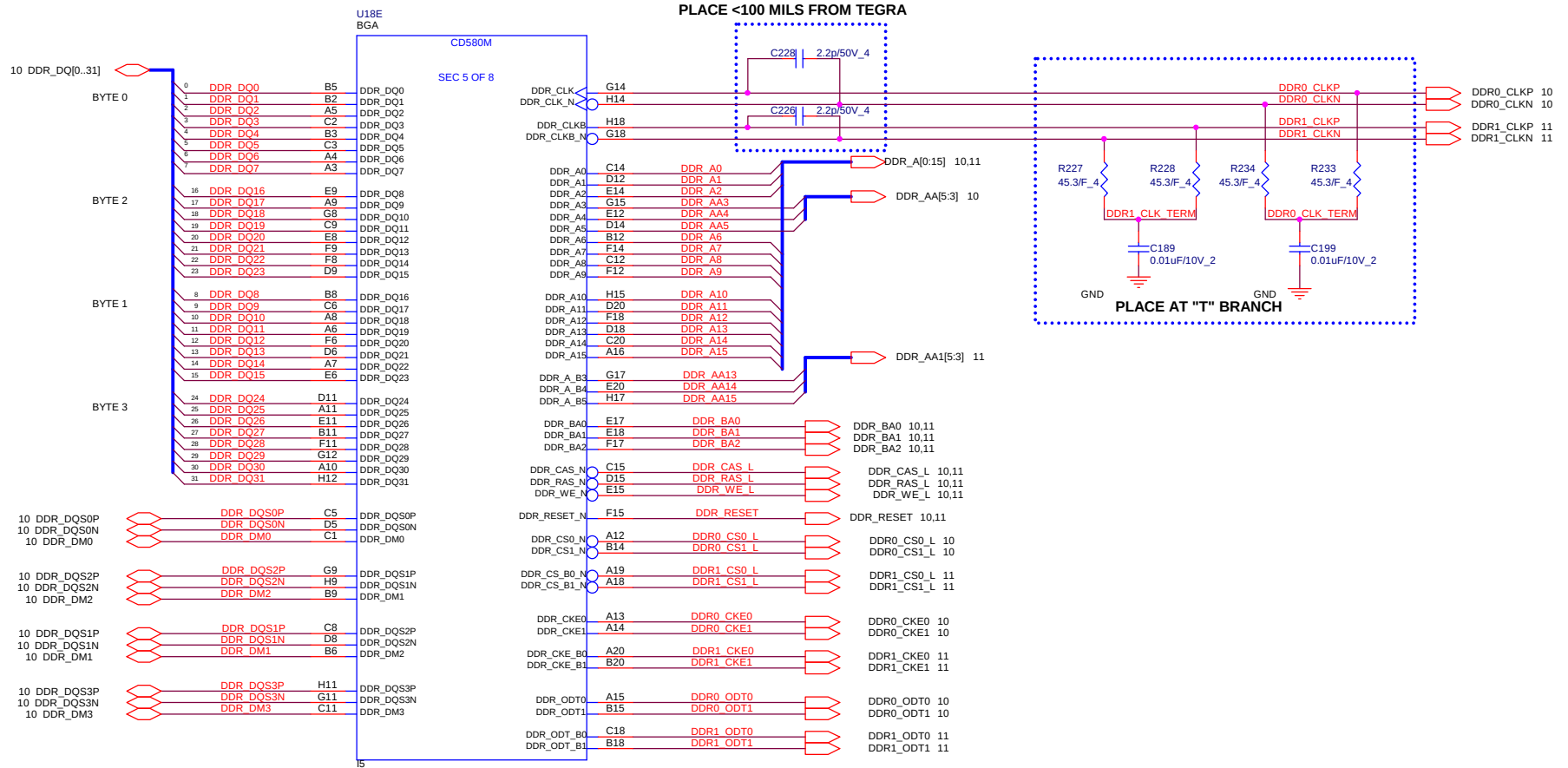
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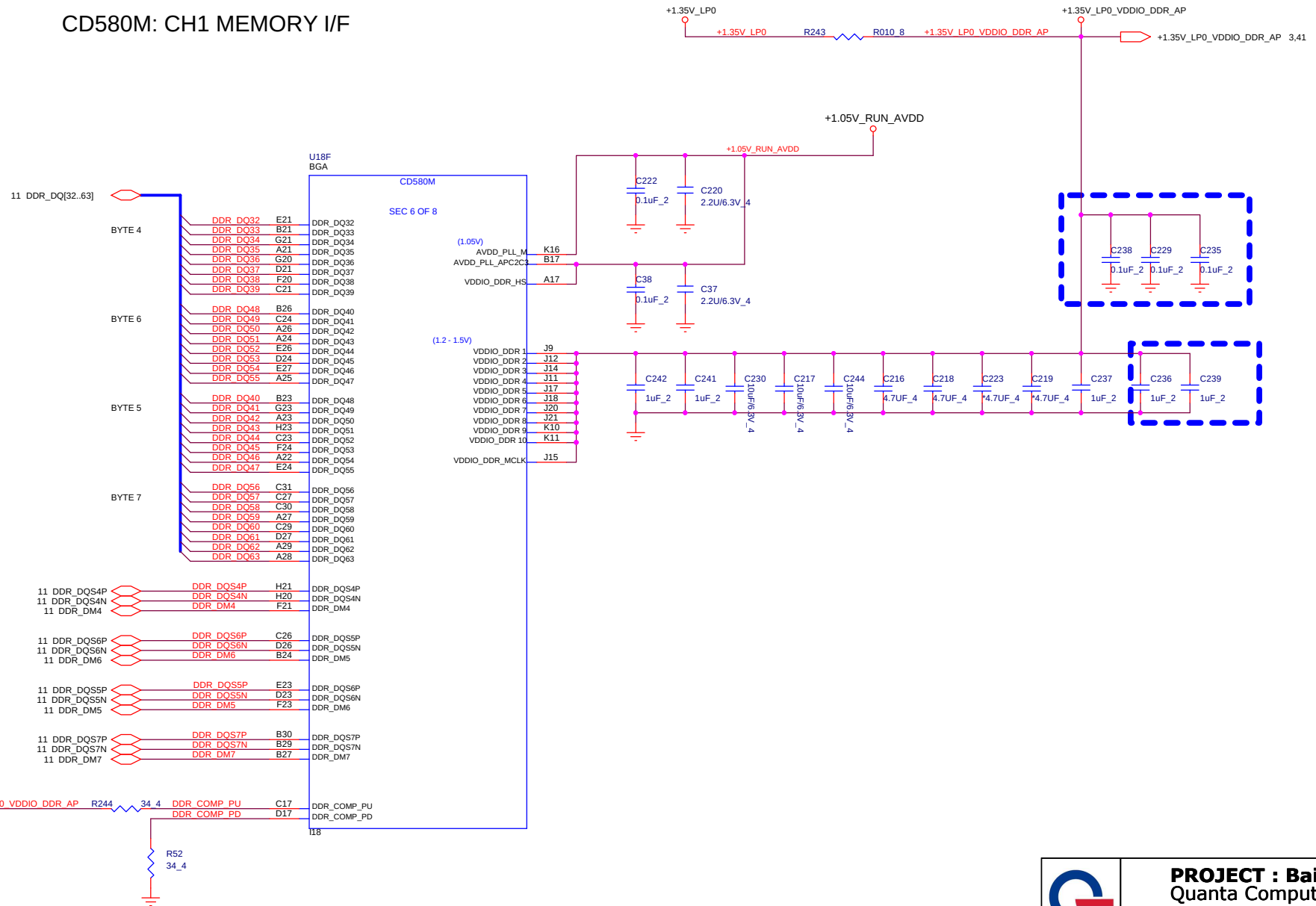
www.schematic-x.blogspot.com

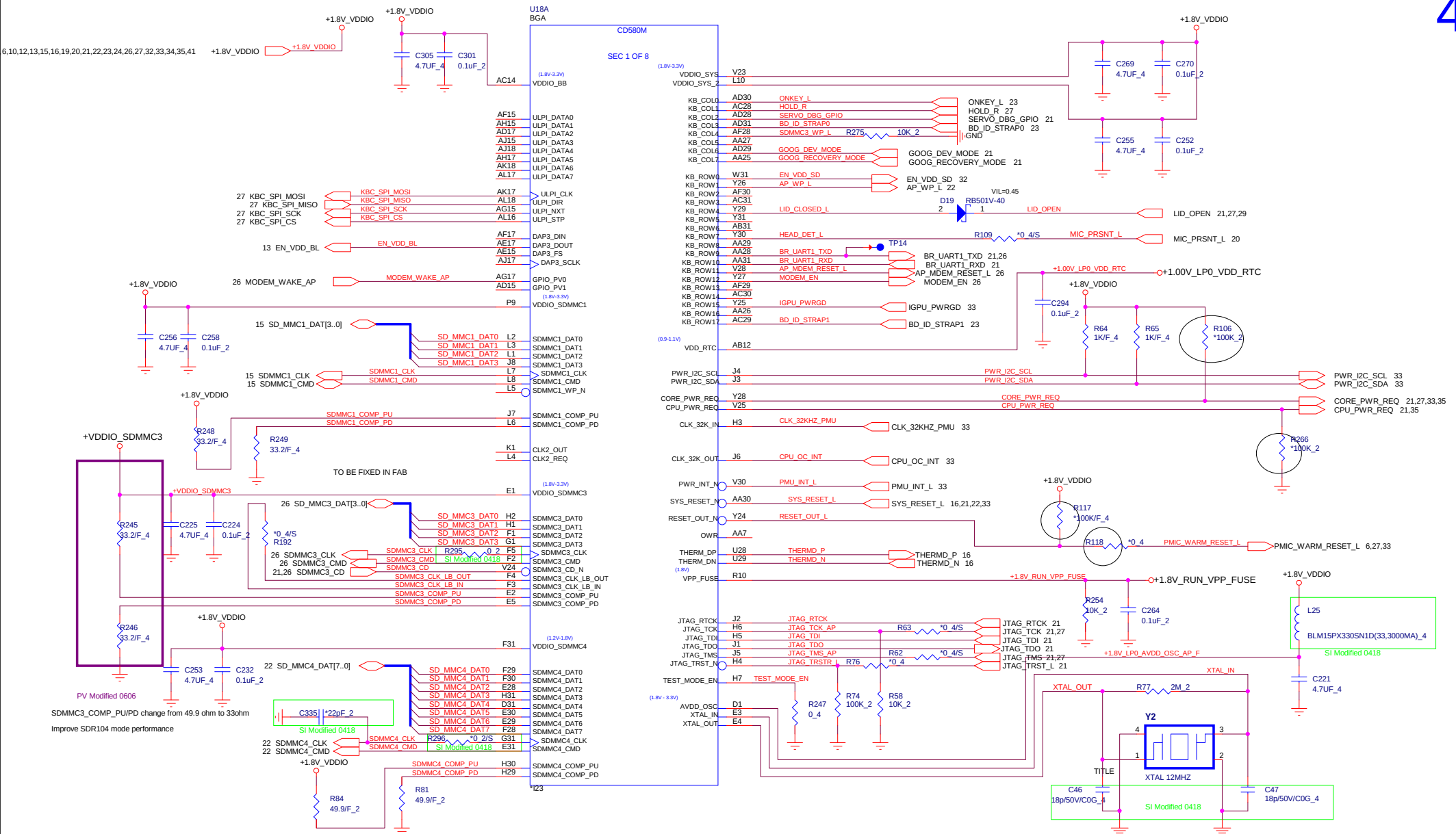
	PROJECT : Baileys Quanta Computer Inc.		
	Size Custom	Document Number Block Diagram	Rev 1A
	Date: Tuesday, June 10, 2014	Sheet	1 of 41

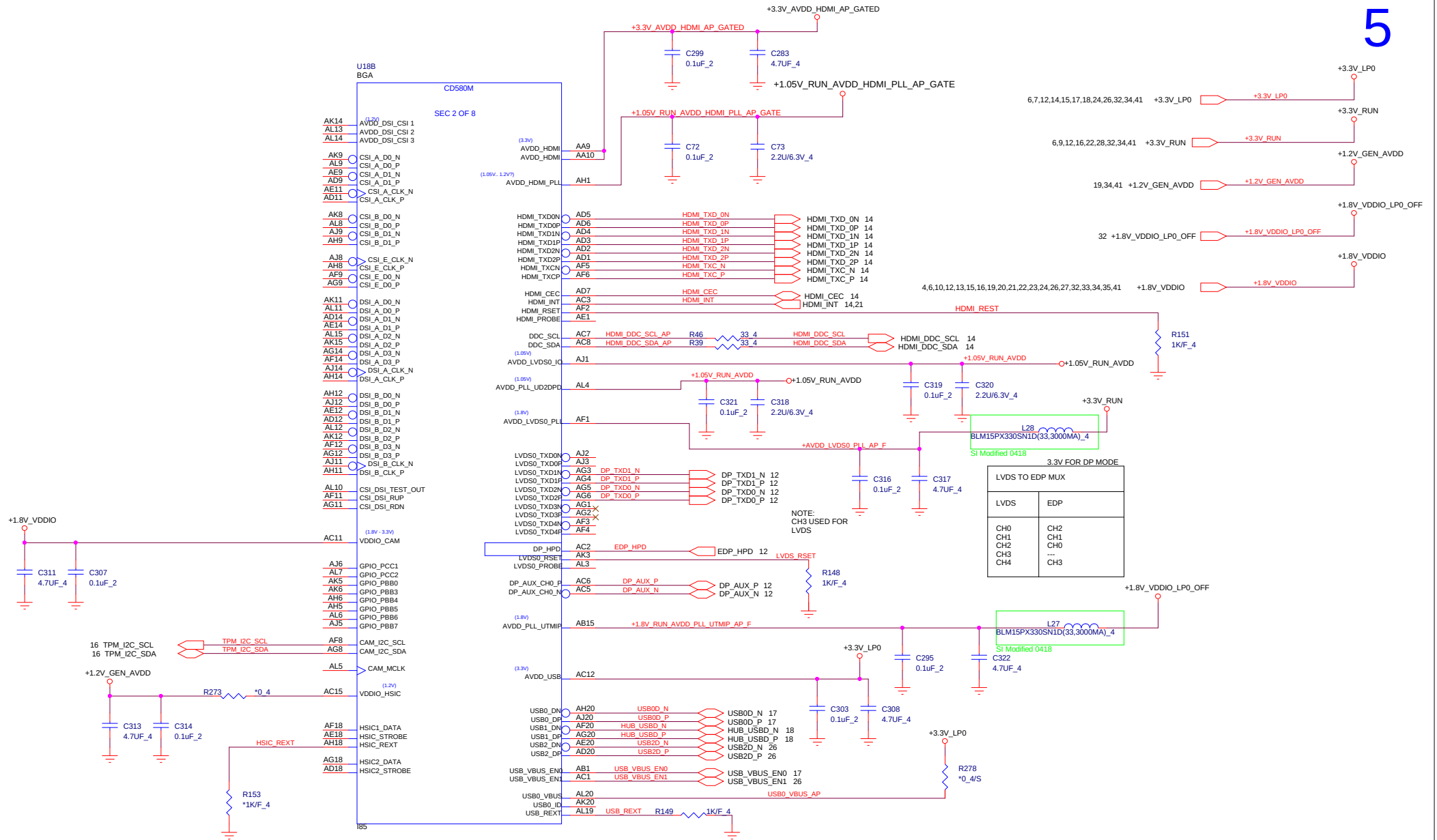
CD580M: CH0 MEMORY I/F



CD580M: CH1 MEMORY I/F





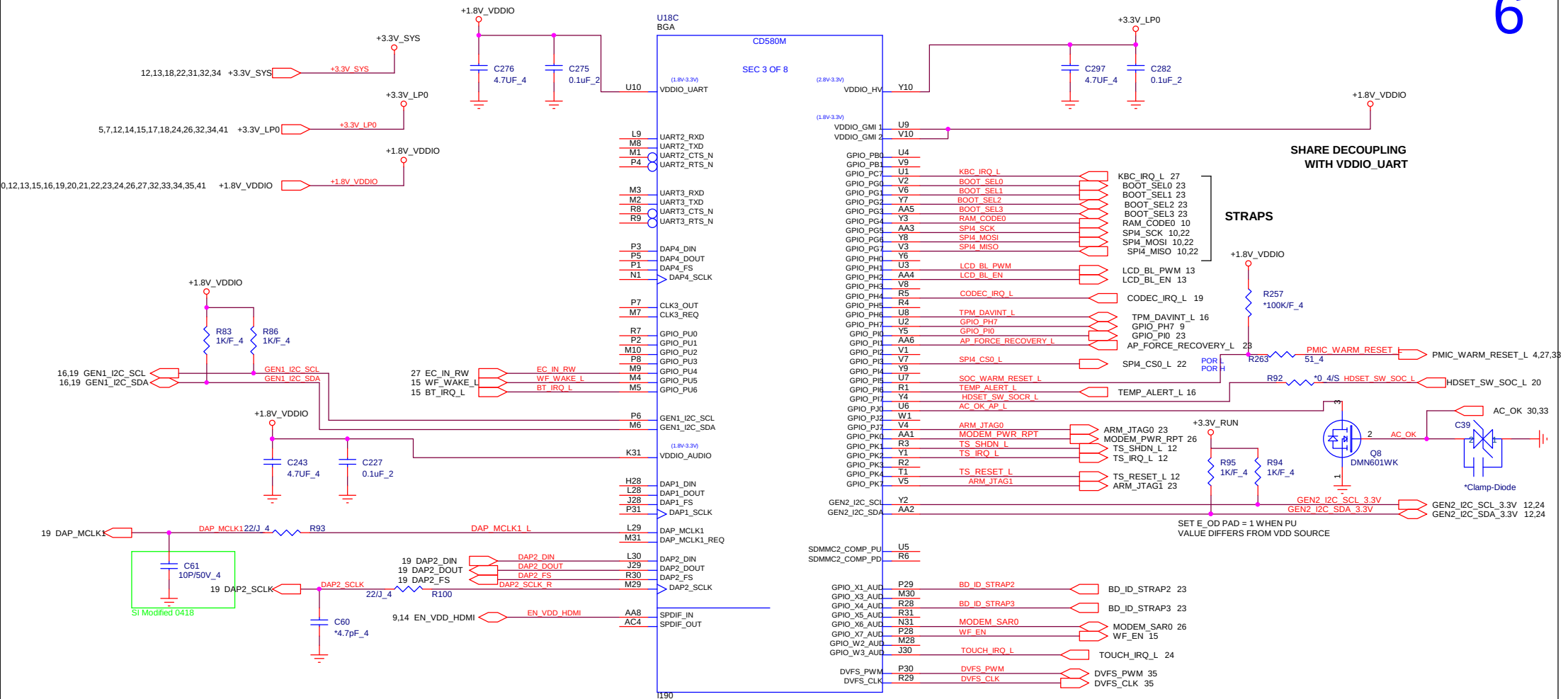


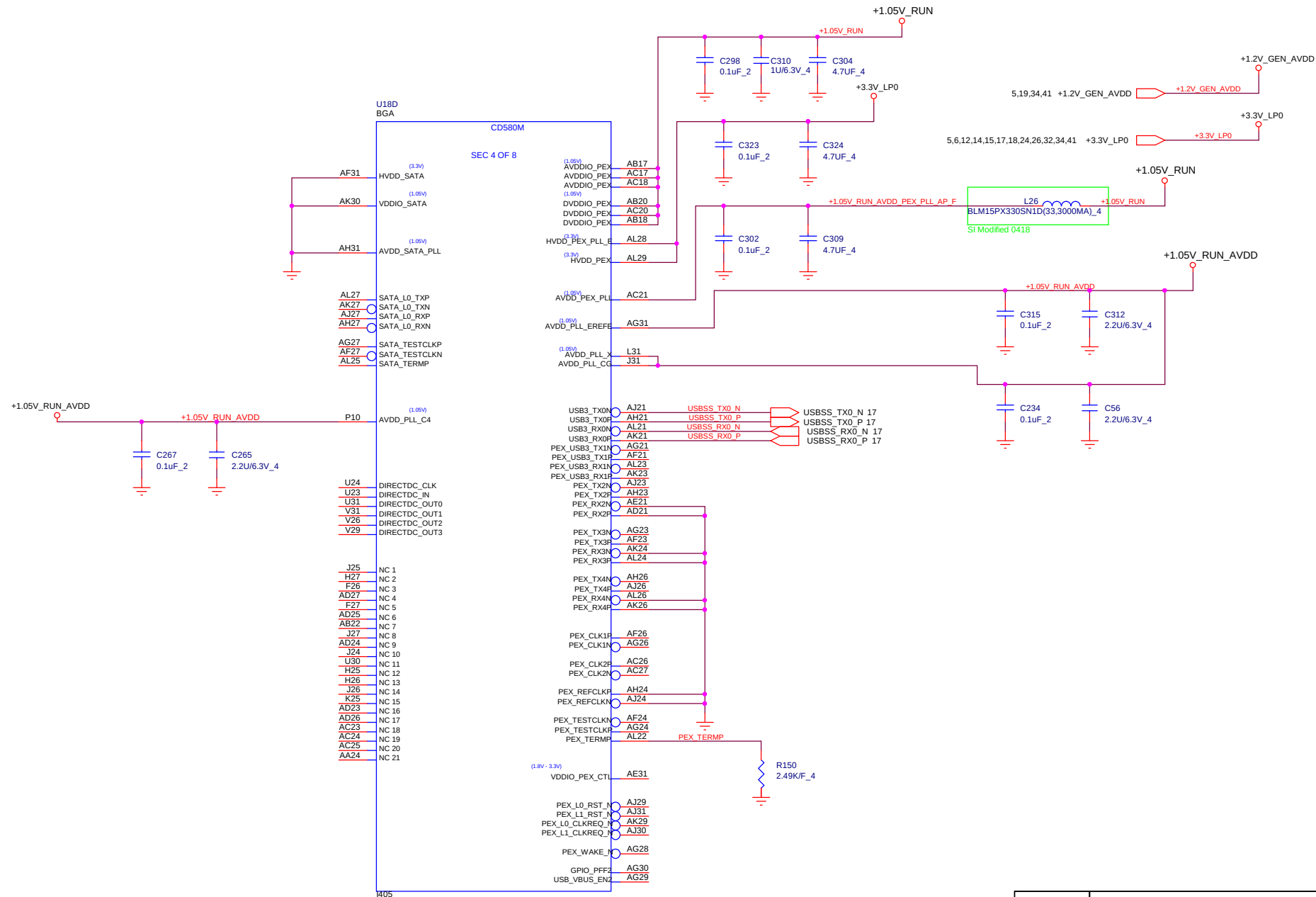
CAD NOTE:
PLACE NEAR TEGRA

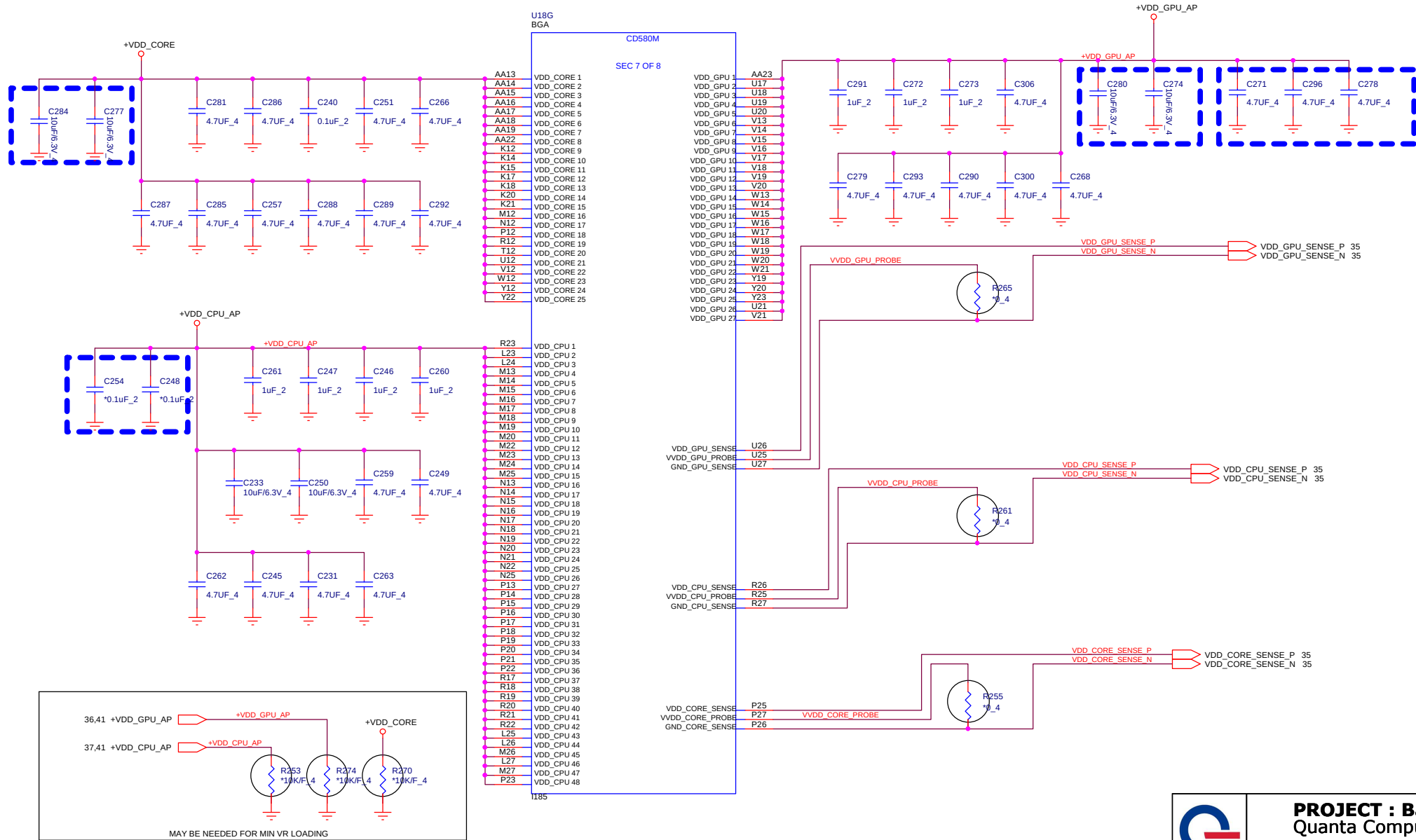


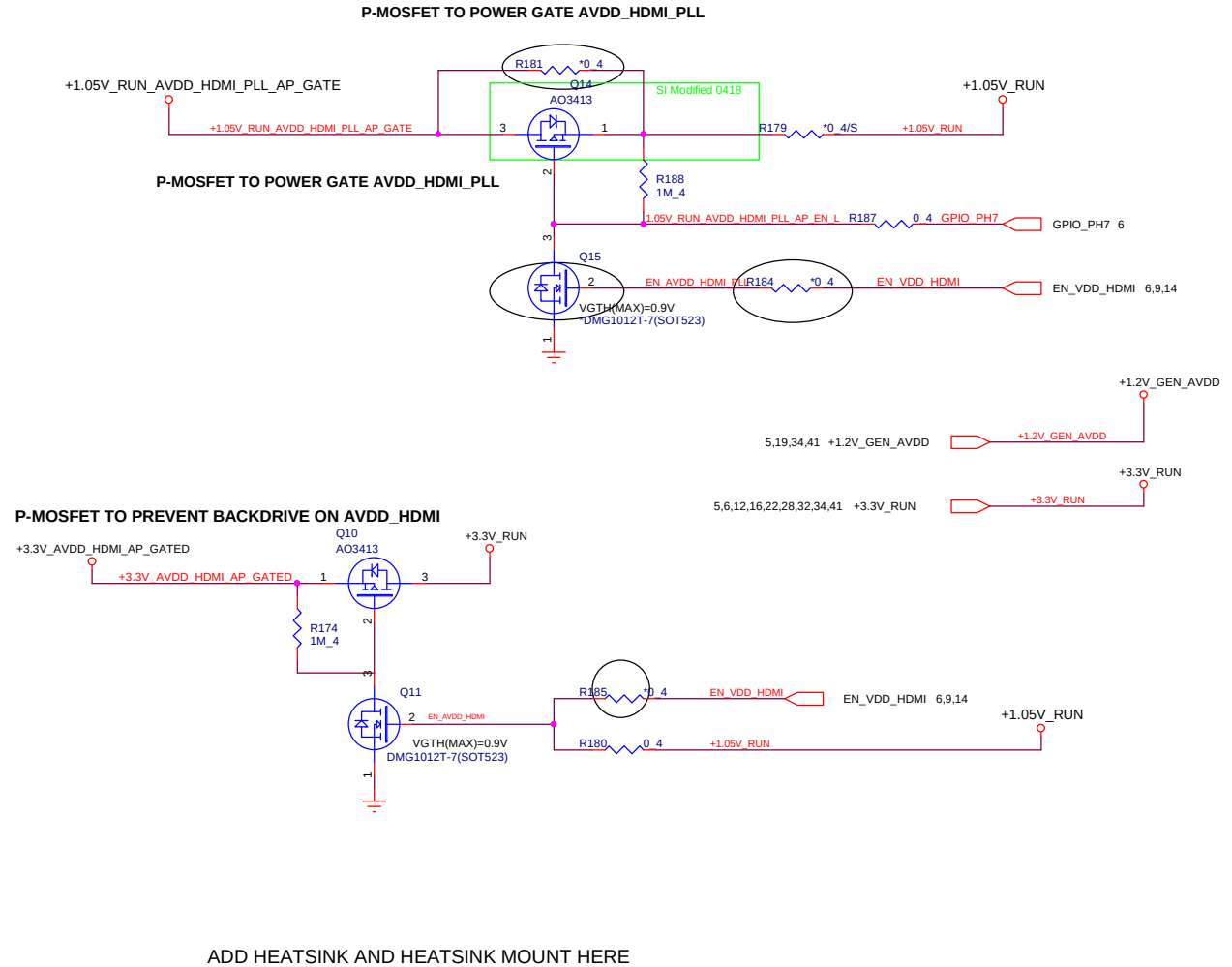
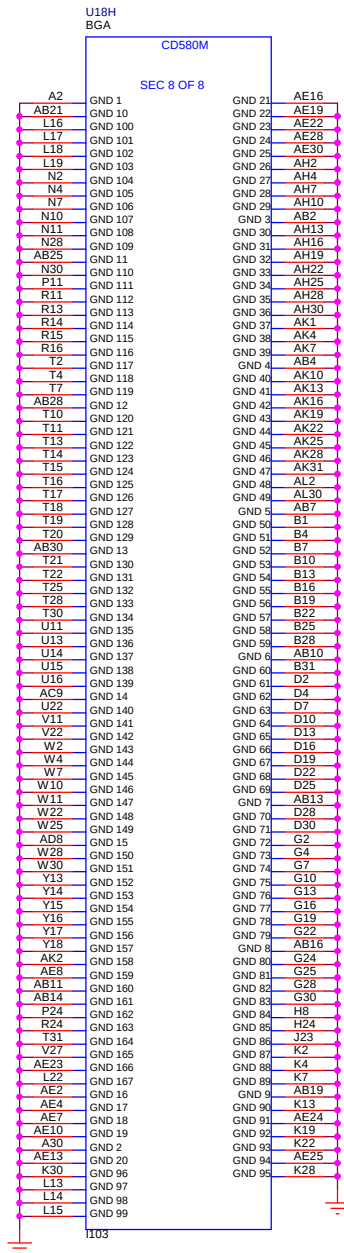
PROJECT : Baileys
Quanta Computer Inc.

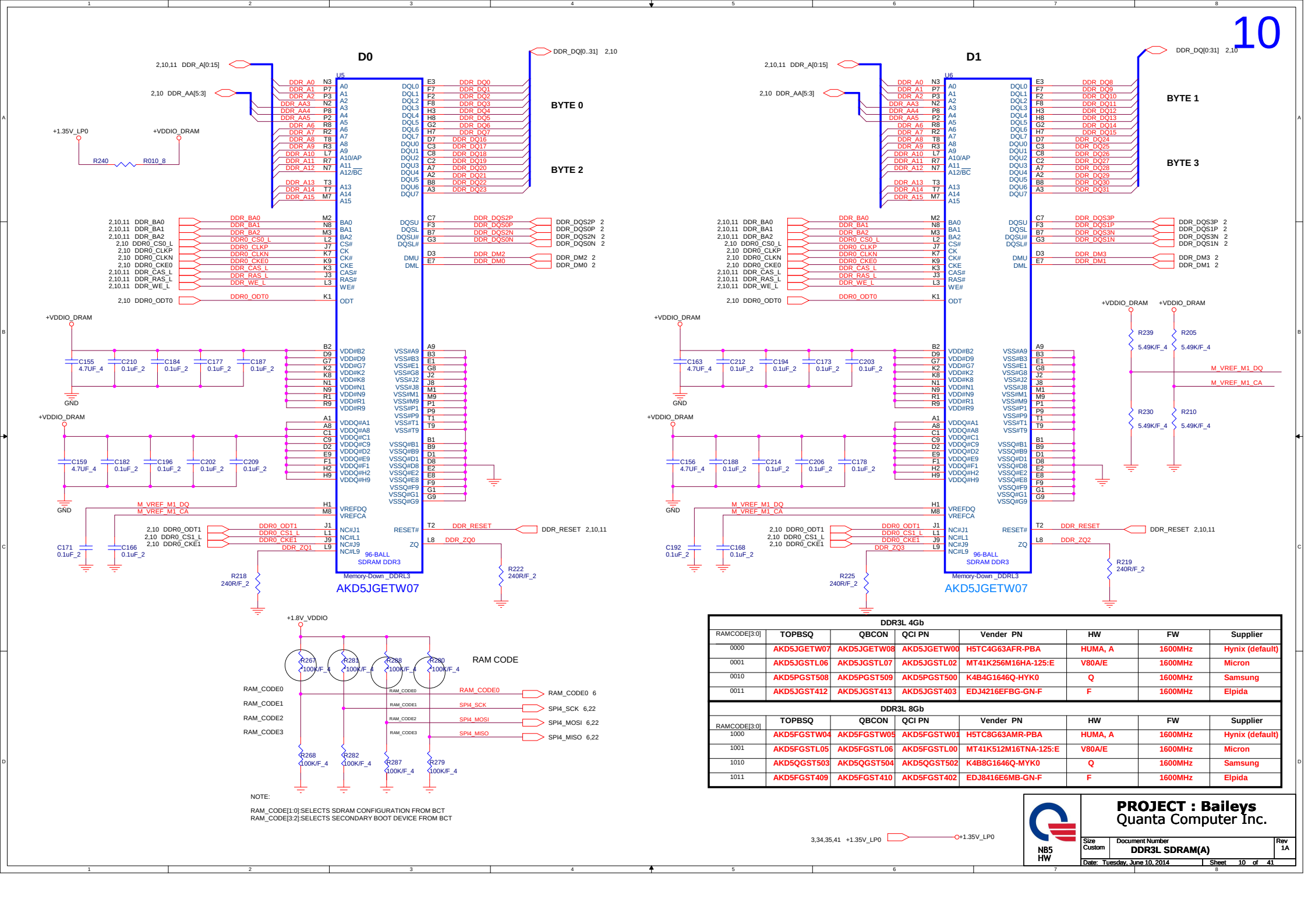
Size	Document Number	Rev
Custom	CS1_DSI_HDMI_USB	1A
Date: Tuesday, June 10, 2014	Sheet 5 of 41	











DDR3L 4Gb							
RAMCODE[3:0]	TOPBSQ	QBCON	QCI PN	Vender PN	HW	FW	Supplier
0000	AKD5JGETW07	AKD5JGETW08	AKD5JGETW09	H5TC4G63AFR-PBA	HUMA, A	1600MHz	Hynix (default)
0001	AKD5JGSTL06	AKD5JGSTL07	AKD5JGSTL02	MT41K256M16HA-125:E	V80A/E	1600MHz	Micron
0010	AKD5PGST508	AKD5PGST509	AKD5PGST500	K4B4G1646Q-HYK0	Q	1600MHz	Samsung
0011	AKD5JGST412	AKD5JGST413	AKD5JGST403	EDJ4216EFBG-GN-F	F	1600MHz	Elpida

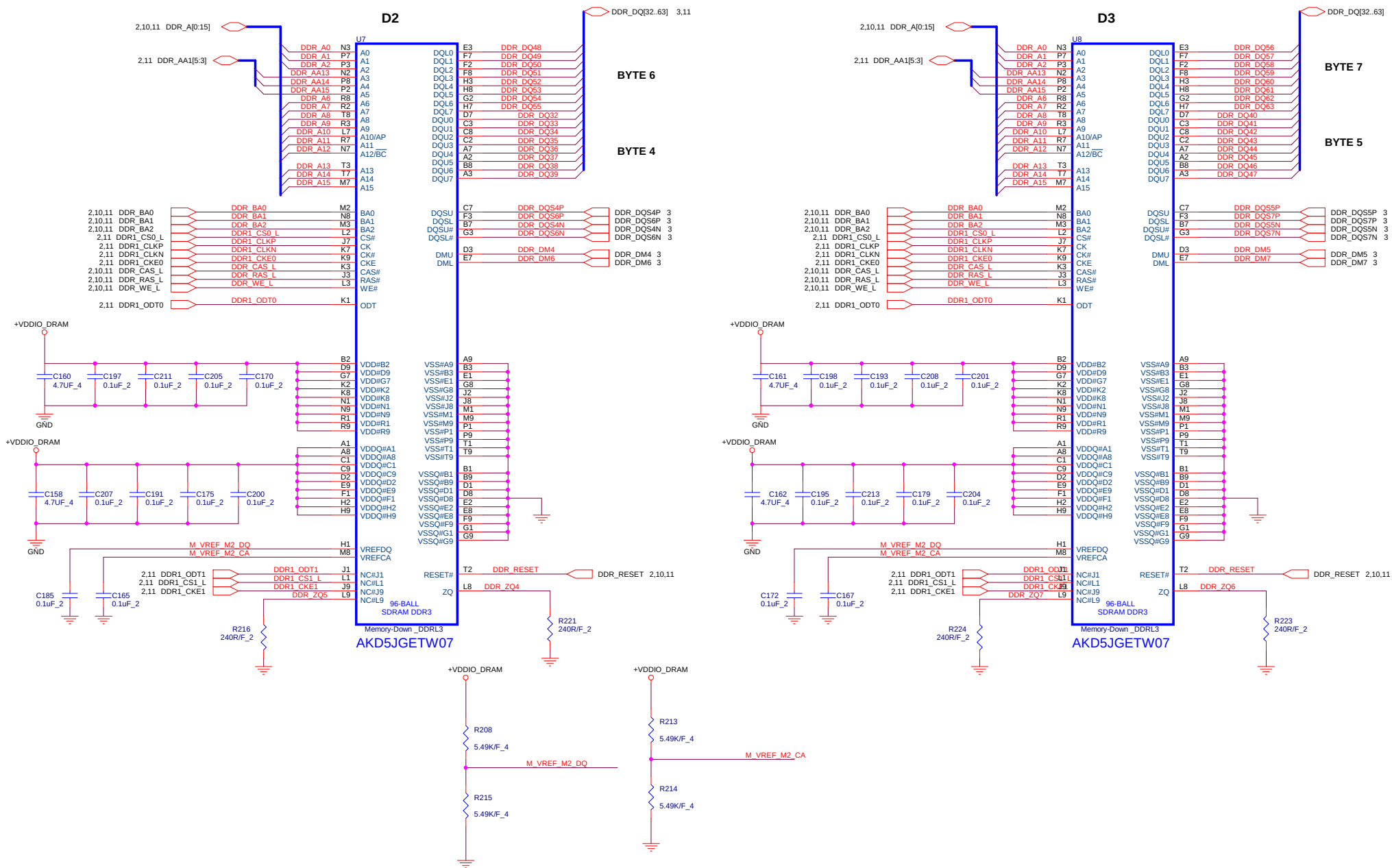
DDR3L 8Gb							
RAMCODE[3:0]	TOPBSQ	QBCON	QCI PN	Vender PN	HW	FW	Supplier
1000	AKD5FGSTW04	AKD5FGSTW05	AKD5FGSTW03	H5TC8G63AMR-PBA	HUMA, A	1600MHz	Hynix (default)
1001	AKD5FGSTL05	AKD5FGSTL06	AKD5FGSTL00	MT41K512M16TNA-125:E	V80A/E	1600MHz	Micron
1010	AKD5QGST503	AKD5QGST504	AKD5QGST502	K4B8G1646Q-MYK0	Q	1600MHz	Samsung
1011	AKD5FGST409	AKD5FGST410	AKD5FGST402	EDJ8416E6MB-GN-F	F	1600MHz	Elpida

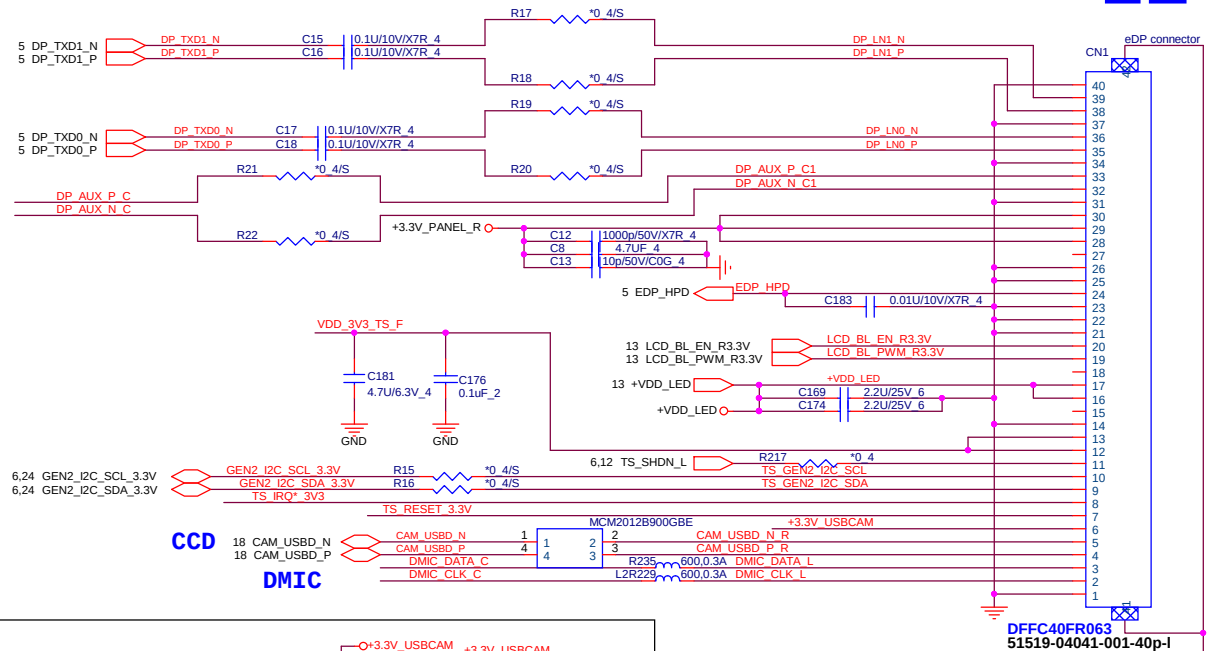
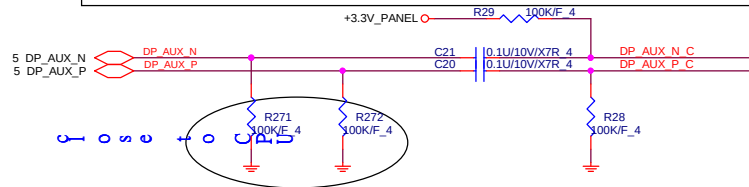
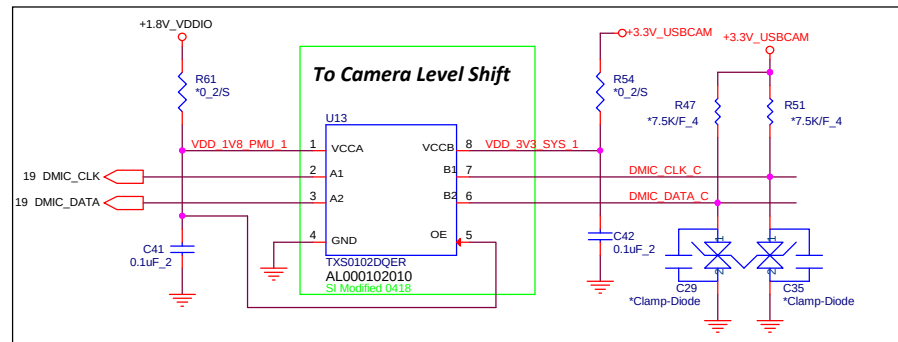
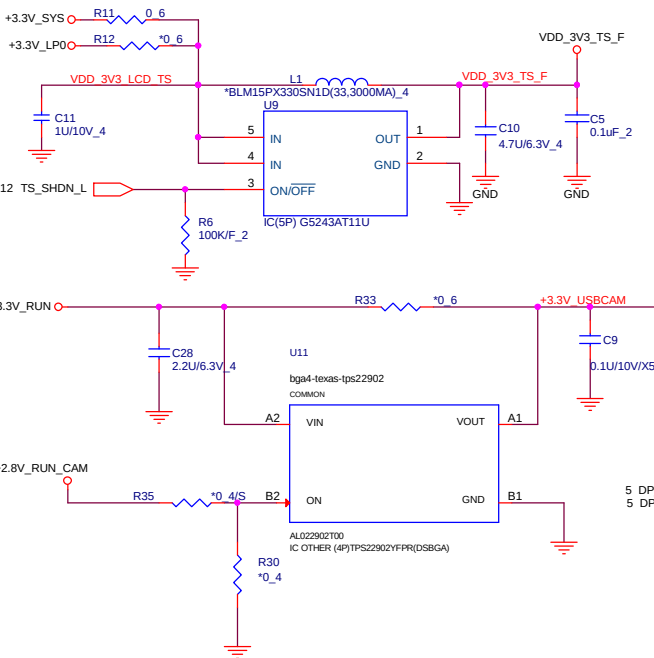
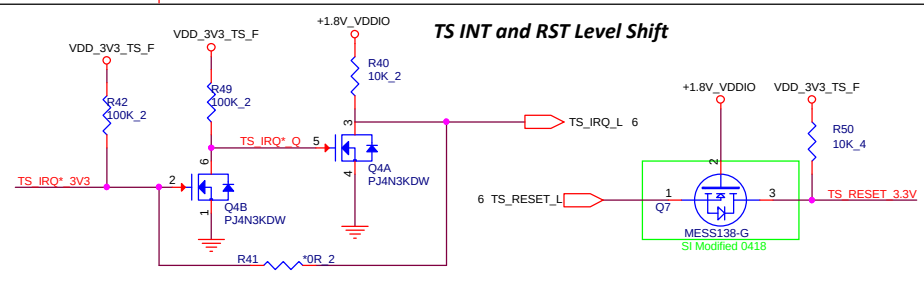
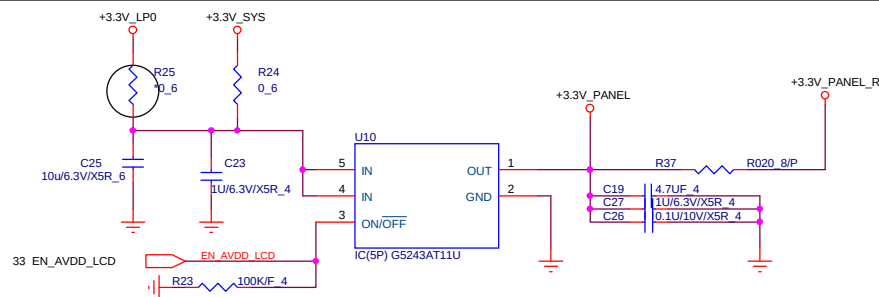


PROJECT : Baileys
Quanta Computer Inc.

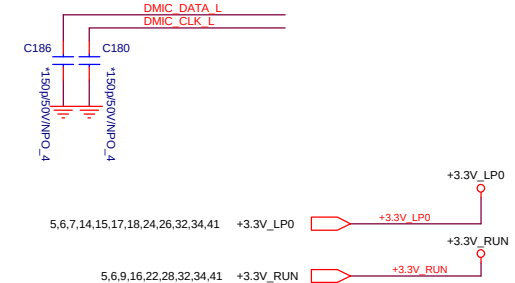
Size Custom	Document Number DDR3L SDRAM(A)	Rev 1A
Date: Tuesday, June 10, 2014	Sheet 10 of 41	

3,34,35,41 +1.35V_LP0 +1.35V_LP0

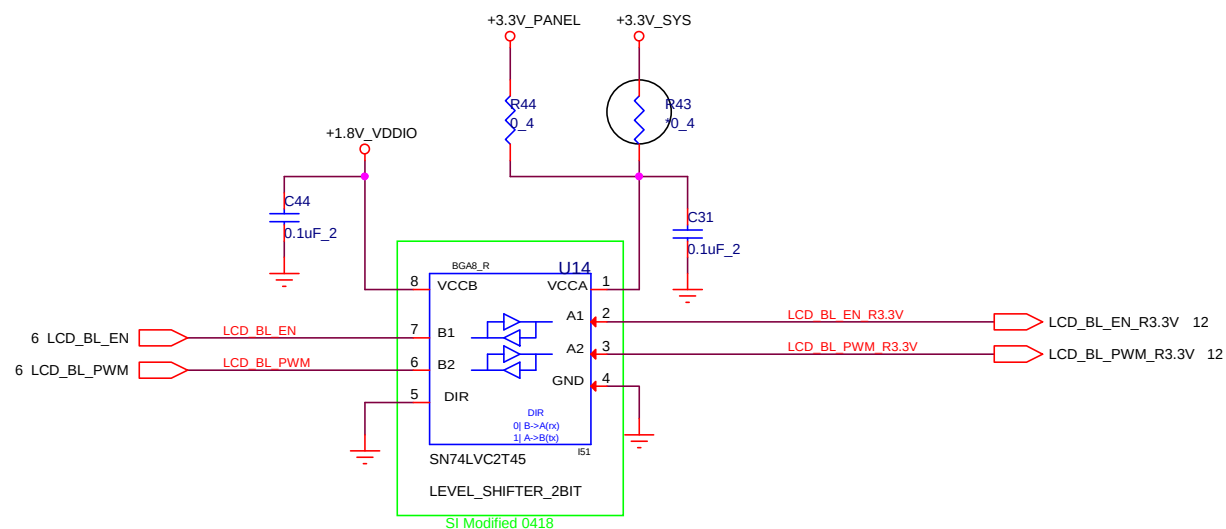
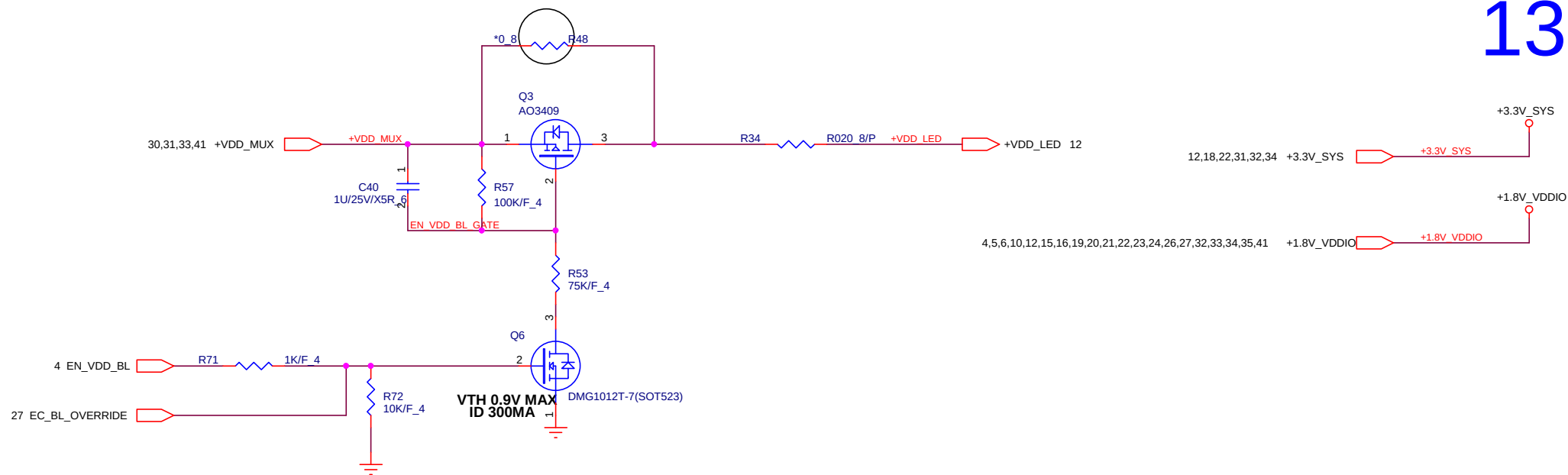




NOTE: THIS CONNECTOR REPRESENTS VARIOUS PANEL CONNECTOR OPTIONS. DEFAULT CONNECTIONS ASSUME THE BACKLIGHT BOOST IS ON THE PANEL USING THE 40PIN CONNECTOR

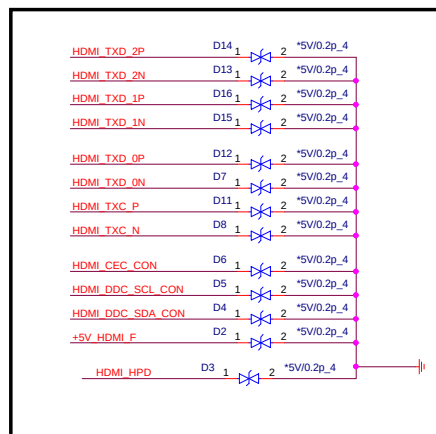


	PROJECT : Baileys Quanta Computer Inc.	
	Size	Document Number
	NB5 HW	eDP CONN/DMIC/CAMERA
	Date: Tuesday, June 10, 2014	Sheet 12 of 41

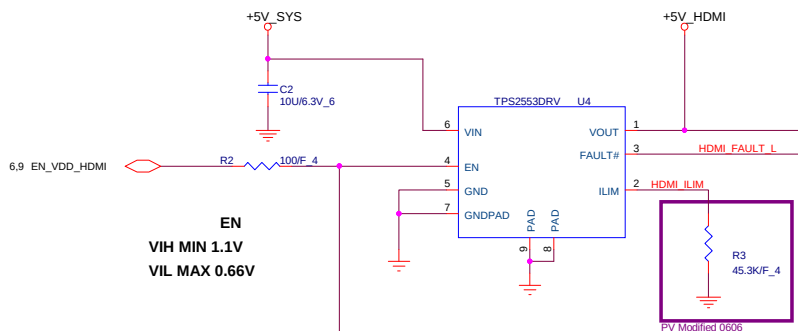
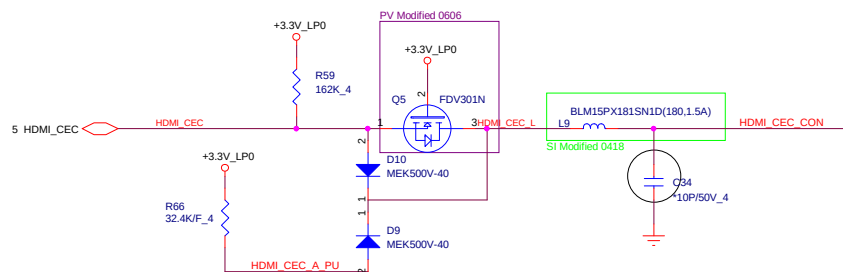


**PROJECT : Baileys
Quanta Computer Inc.**

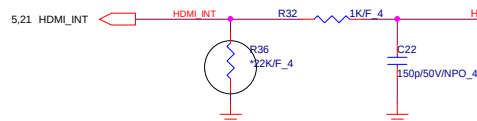
Size	Document Number	Rev
	BACKLIGHT MISC AND GATE	
Date: Tuesday, June 10, 2014	Sheet 13 of 41	



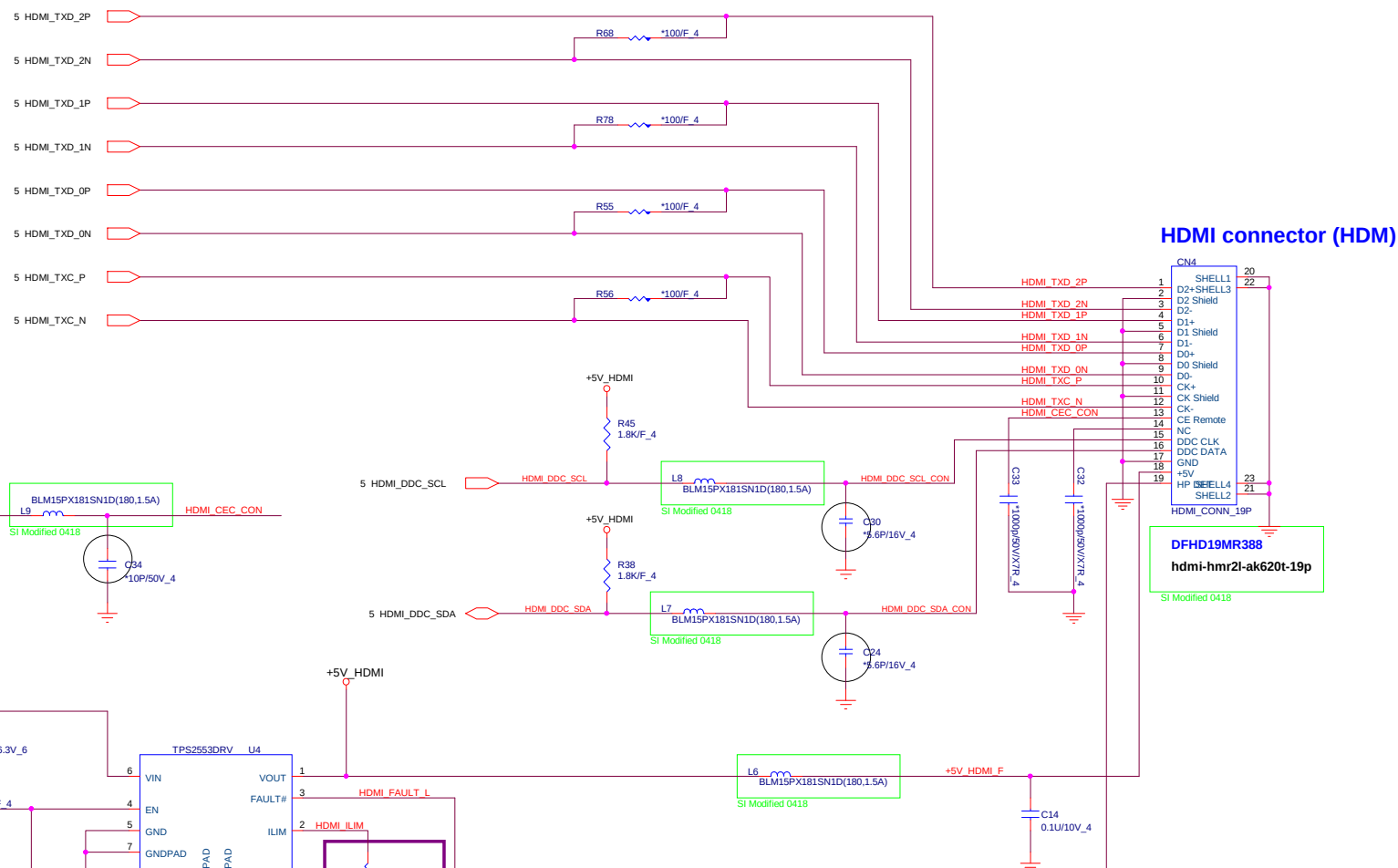
靠近HDMI CONNECTOR(CN7)



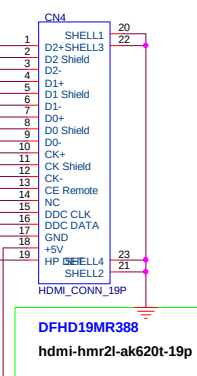
HDMI-detect (HDM)

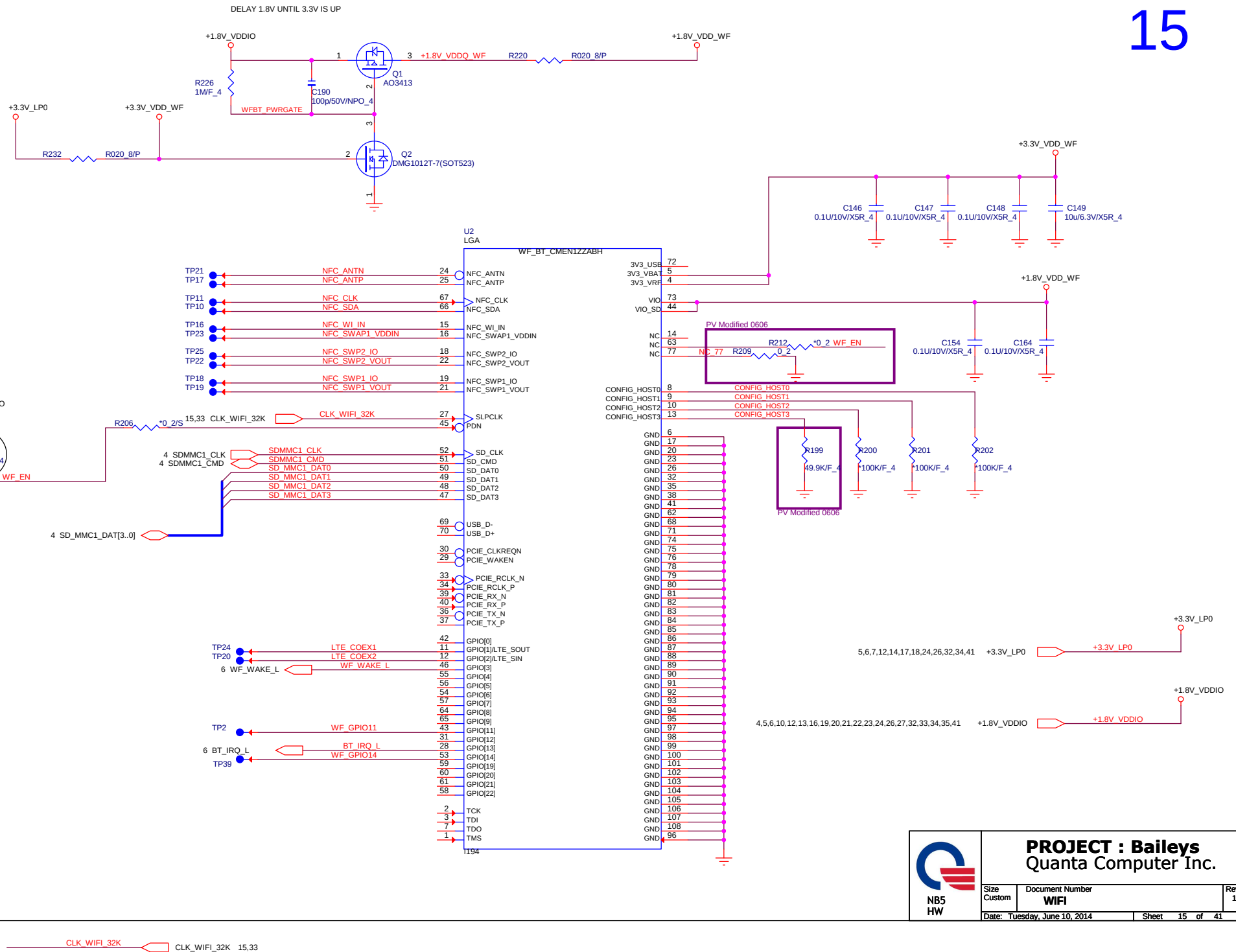


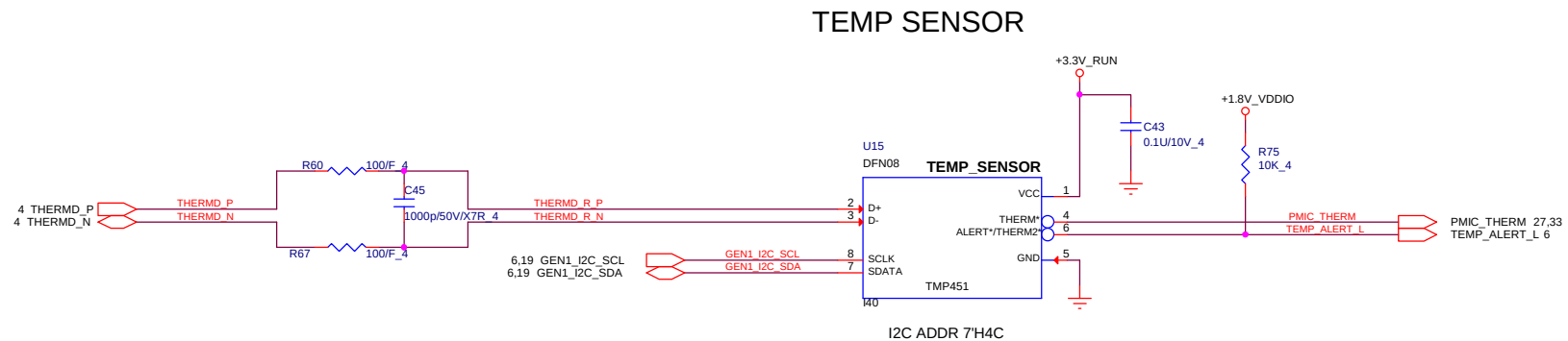
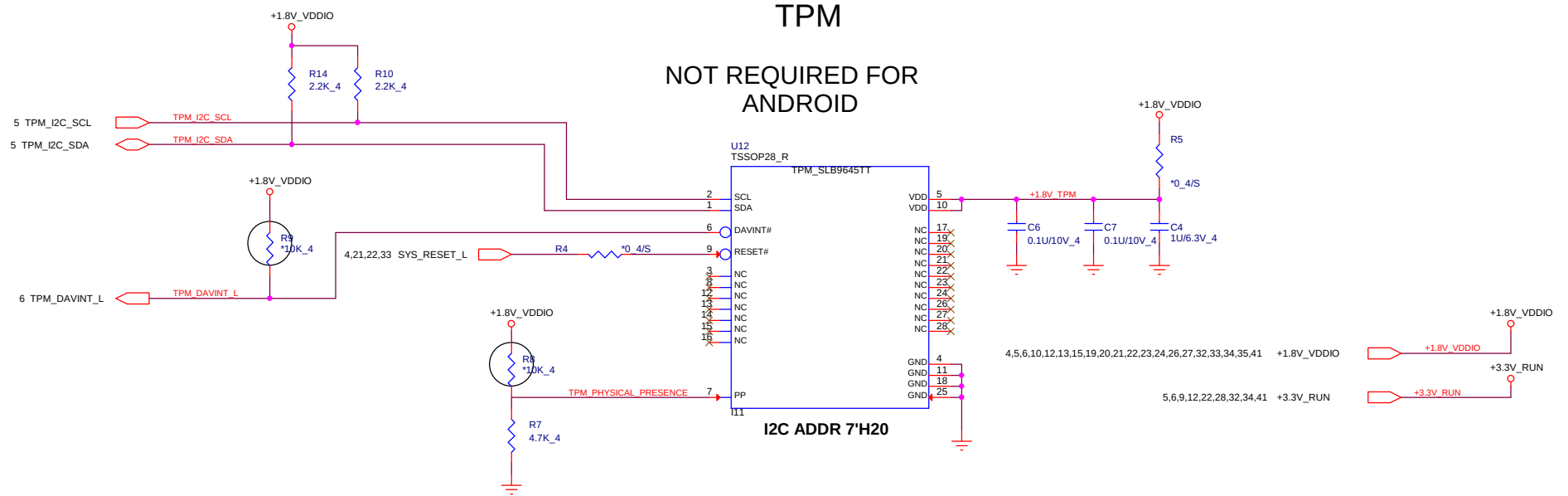
EMI (EMC)

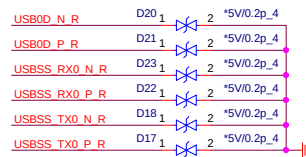
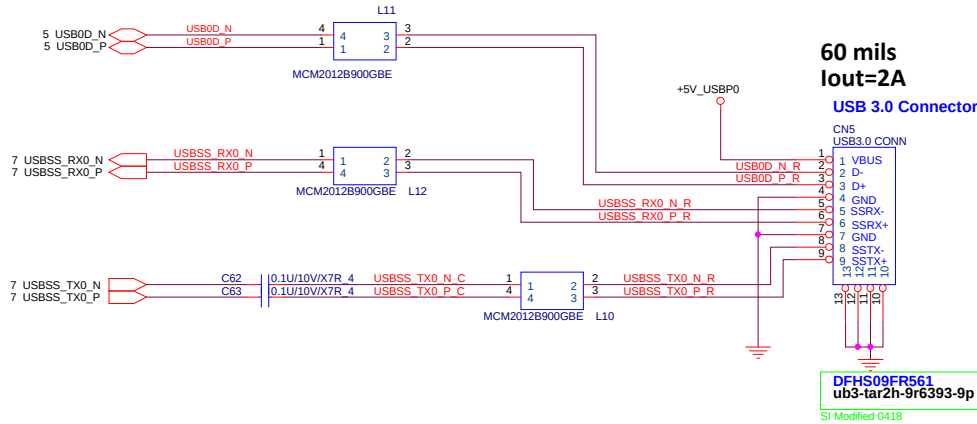
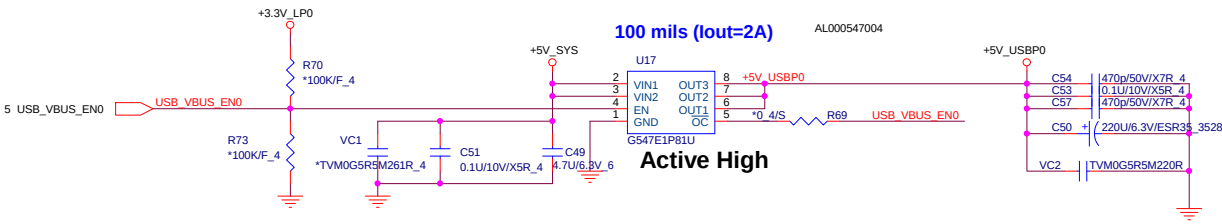


HDMI connector (HDM)

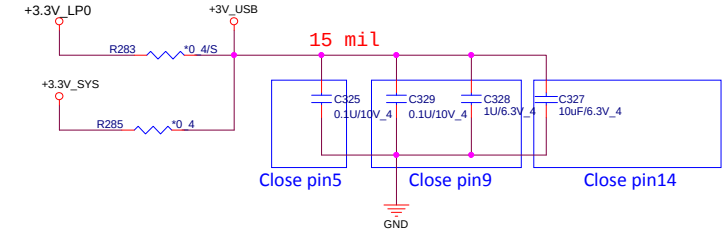
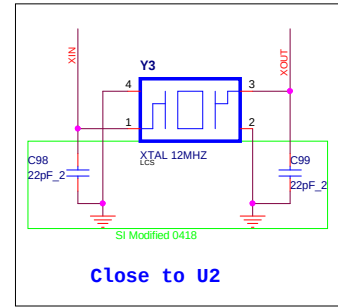
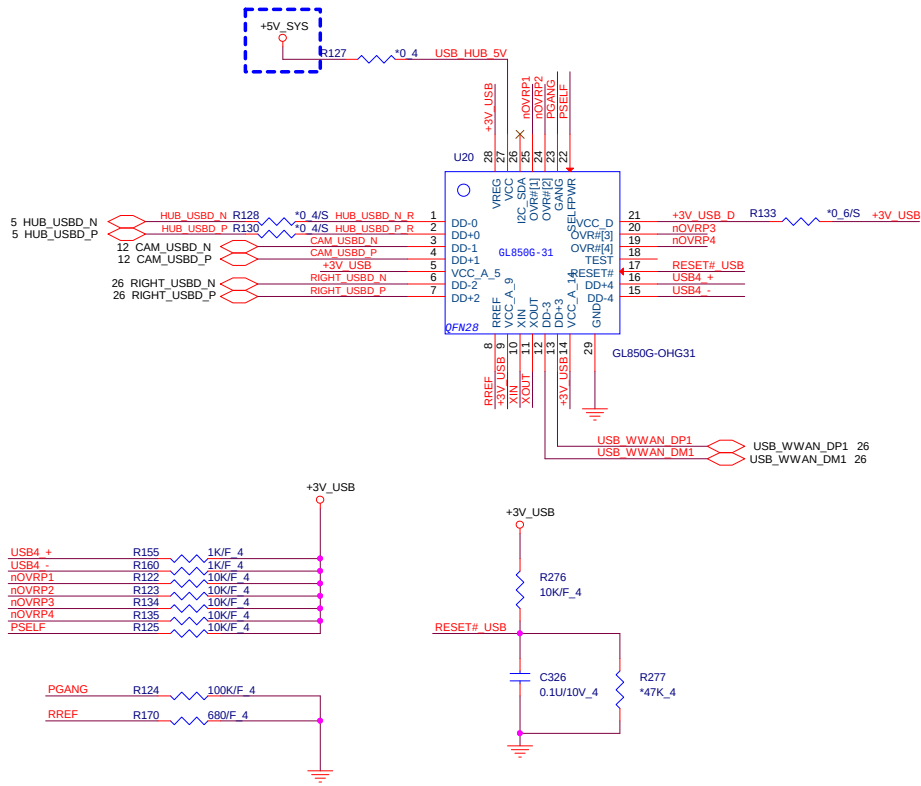




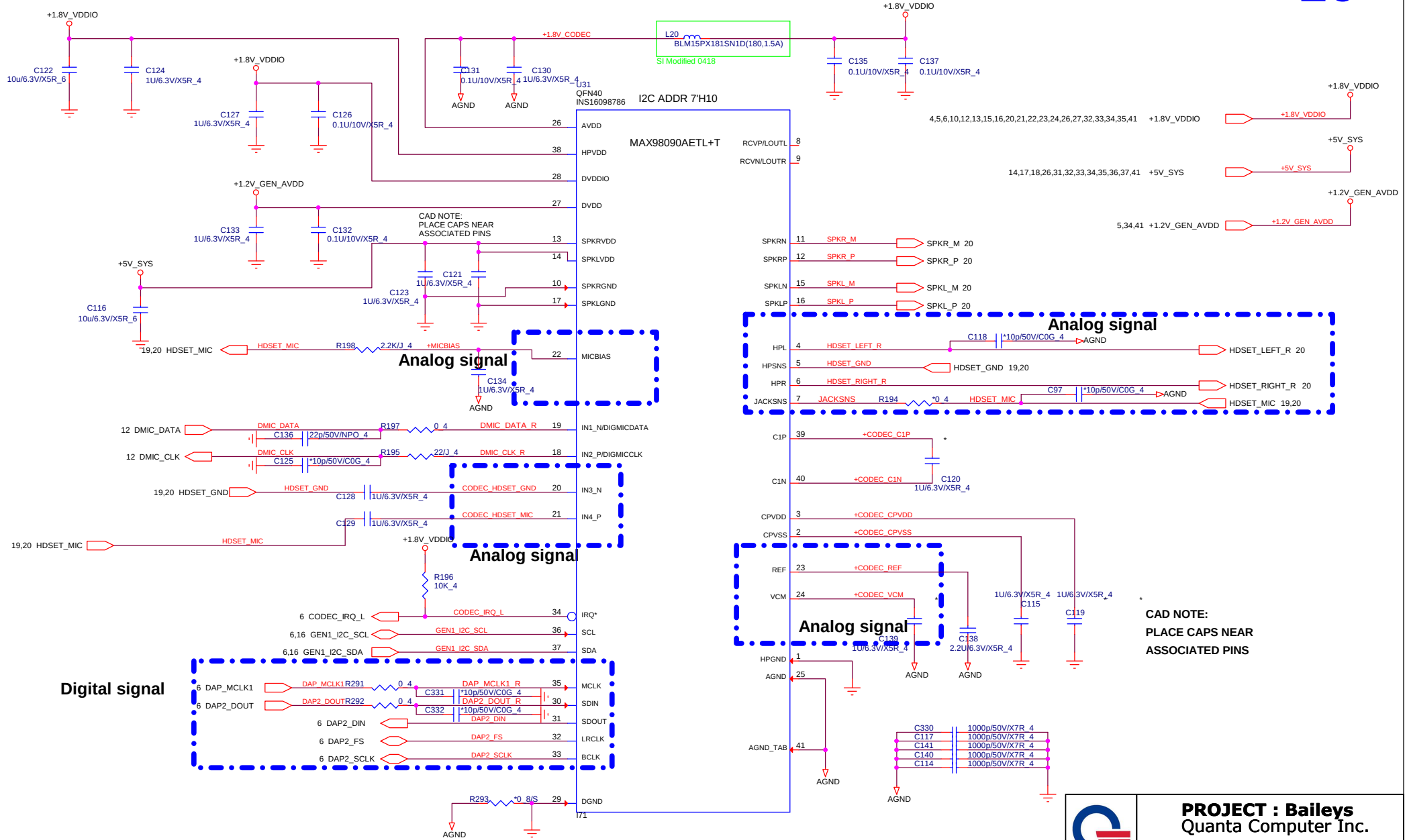


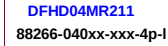


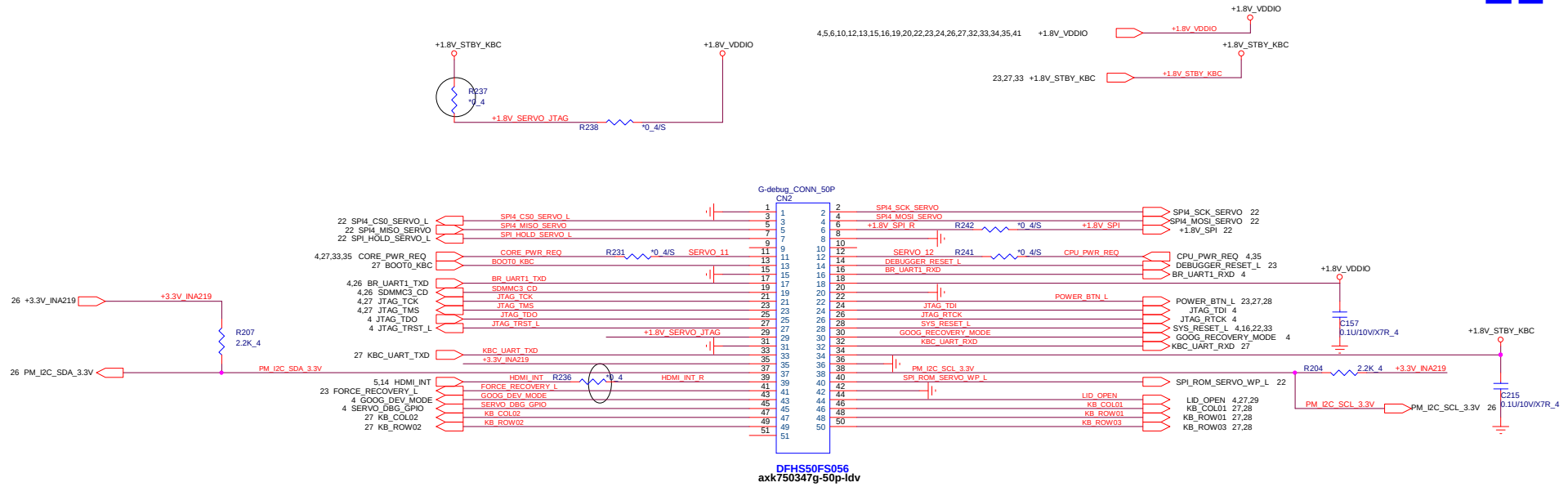
 NB5 HW	PROJECT : Baileys Quanta Computer Inc.		
	Size Custom	Document Number TPM & TEMP SENSOR	Rev 1A
	Date: Tuesday, June 10, 2014		



14,17,19,26,31,32,33,34,35,36,37,41 +5V_SYS





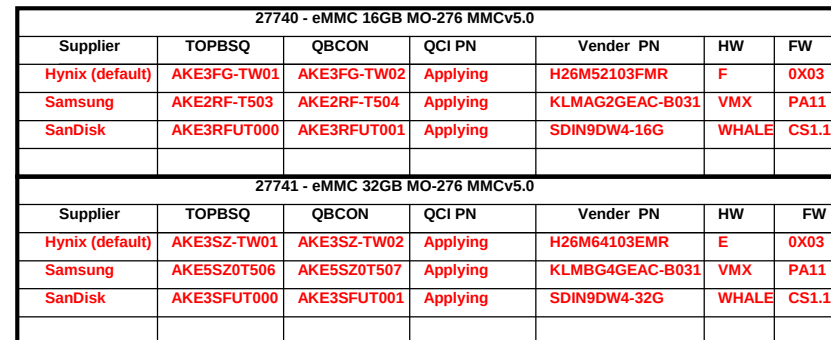
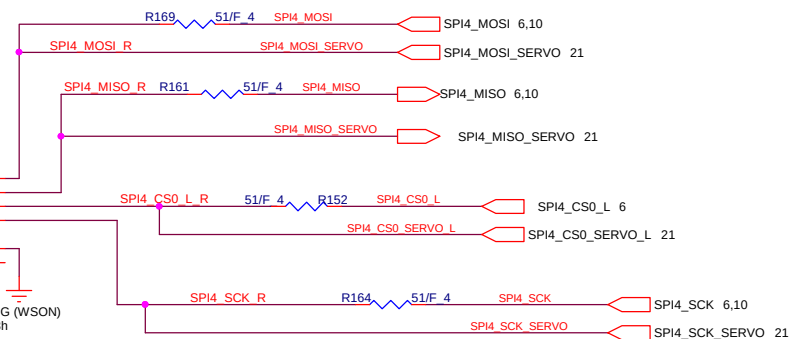


Google DEBU CONNECTOR

PROJECT : Baileys Quanta Computer Inc.		
	Size Custom	Document Number SERVO DEBU CONNECTOR
	Date: Tuesday, June 10, 2014	Rev 1A
Sheet 21 of 41		

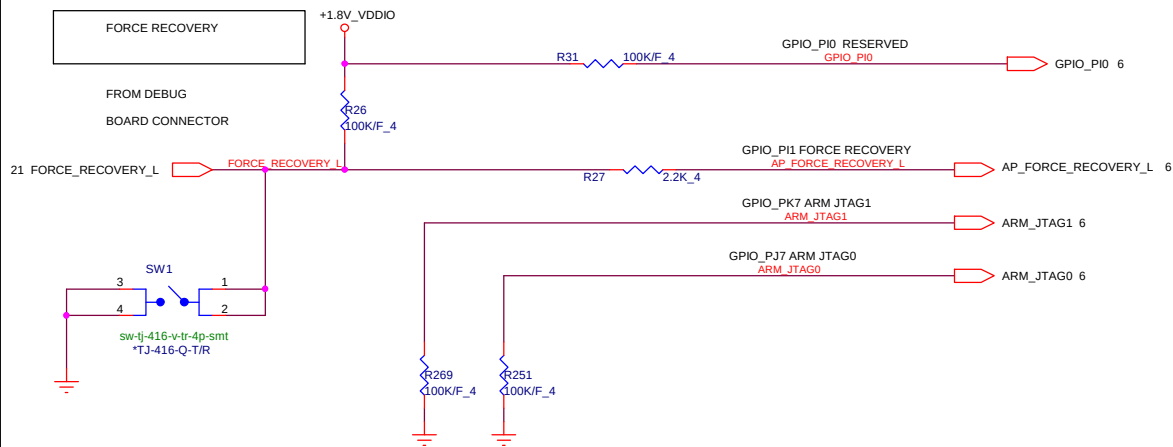
I/O voltage:1.7-1.95v or 2.7-3.6v

Core:2.7-3.6v

[illegible]

PROJECT : Baileys
Quanta Computer Inc.

Size Custom	Document Number EMMC,SPI ROM,MNTG HOLES	Rev 1A
Date: Tuesday, June 10, 2014		Sheet 22 of 41



NOTE:

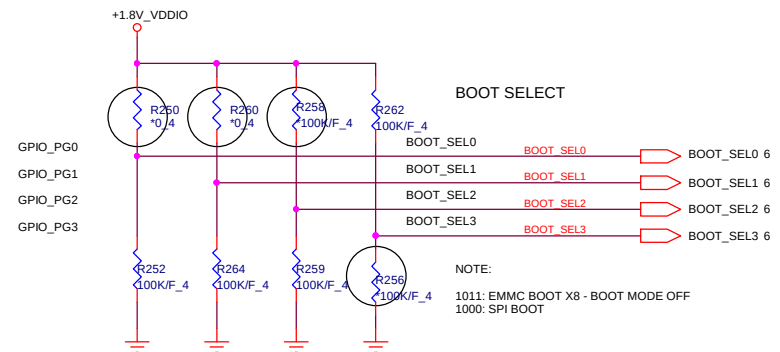
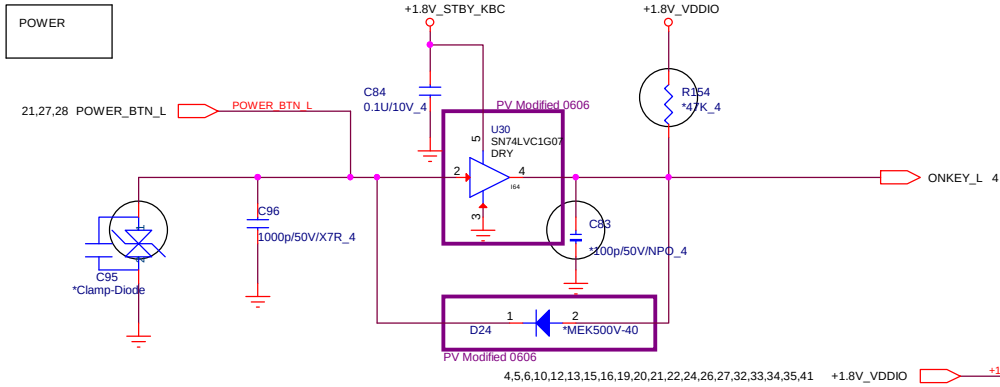
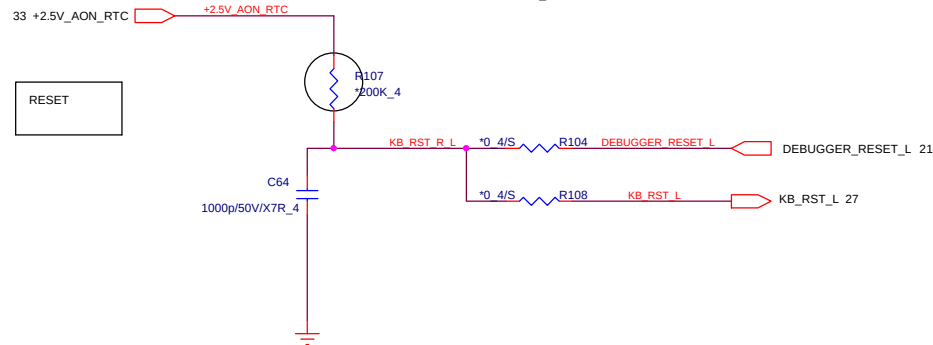
ARM_JTAG[1:0]

2'B00 = SERIAL JTAG CHAIN CPU AND COP

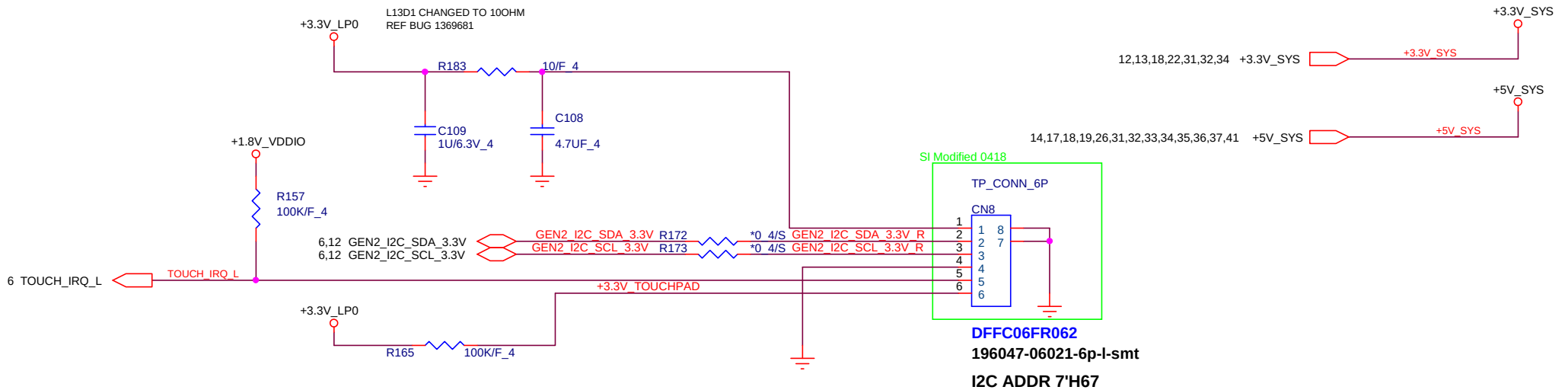
2'B01 = CPU

2'B10 = COP

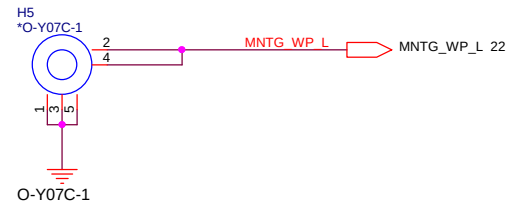
2'B11 = SERIAL_ALT



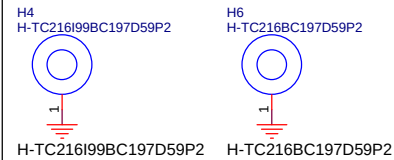
TOUCHPAD BOARD CONN (TPD)



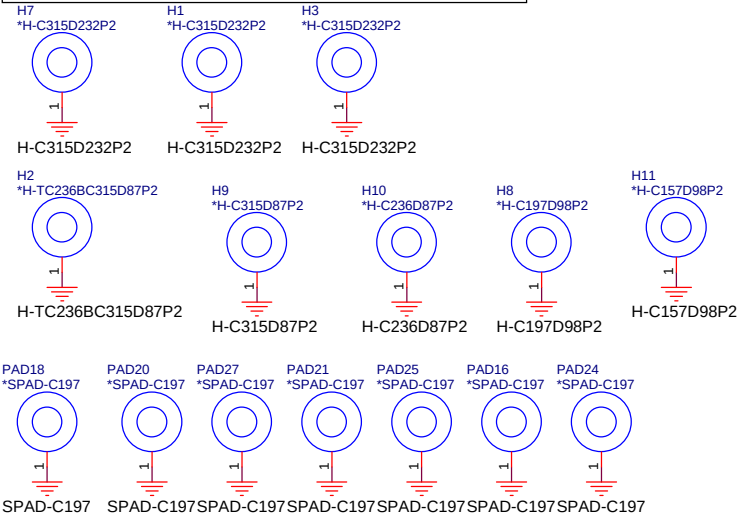
Write-Protect Switch



Thermal Nut

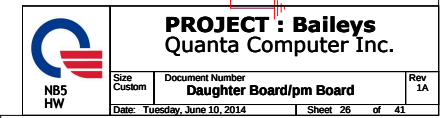
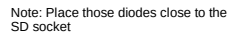


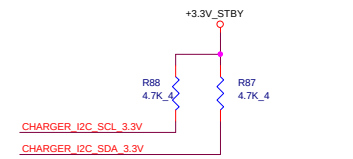
Hole

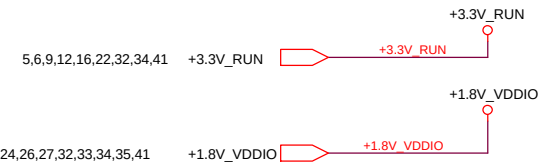
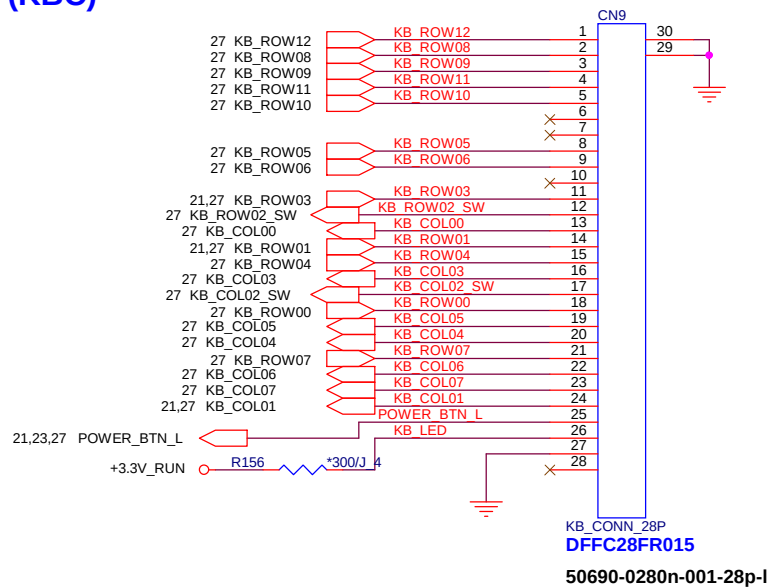


MicroSD Card Socket

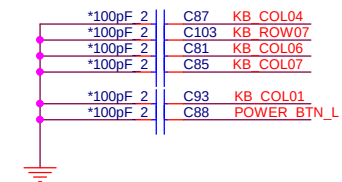
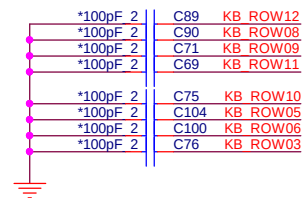
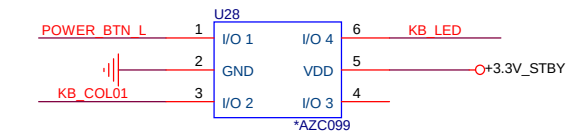
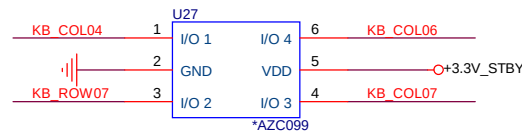
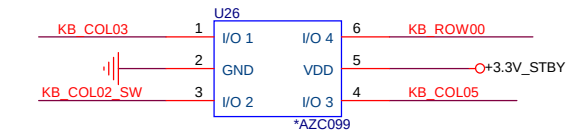
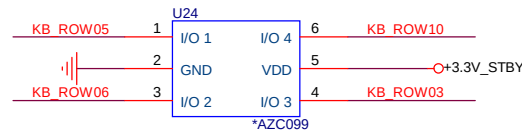
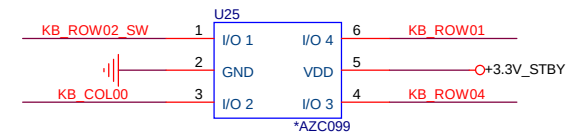
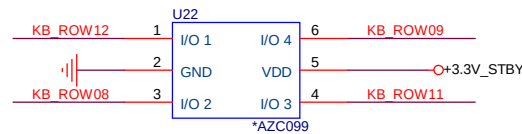
Place close to J3







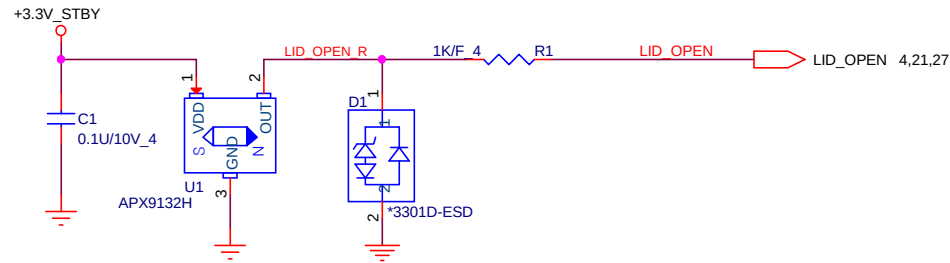
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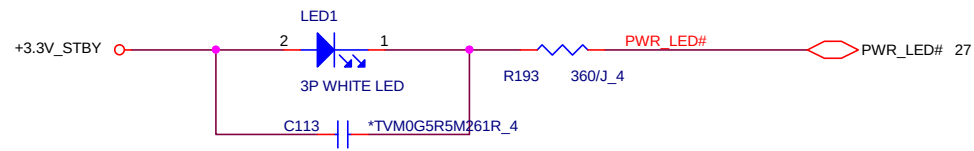
PROJECT : Baileys
Quanta Computer Inc.

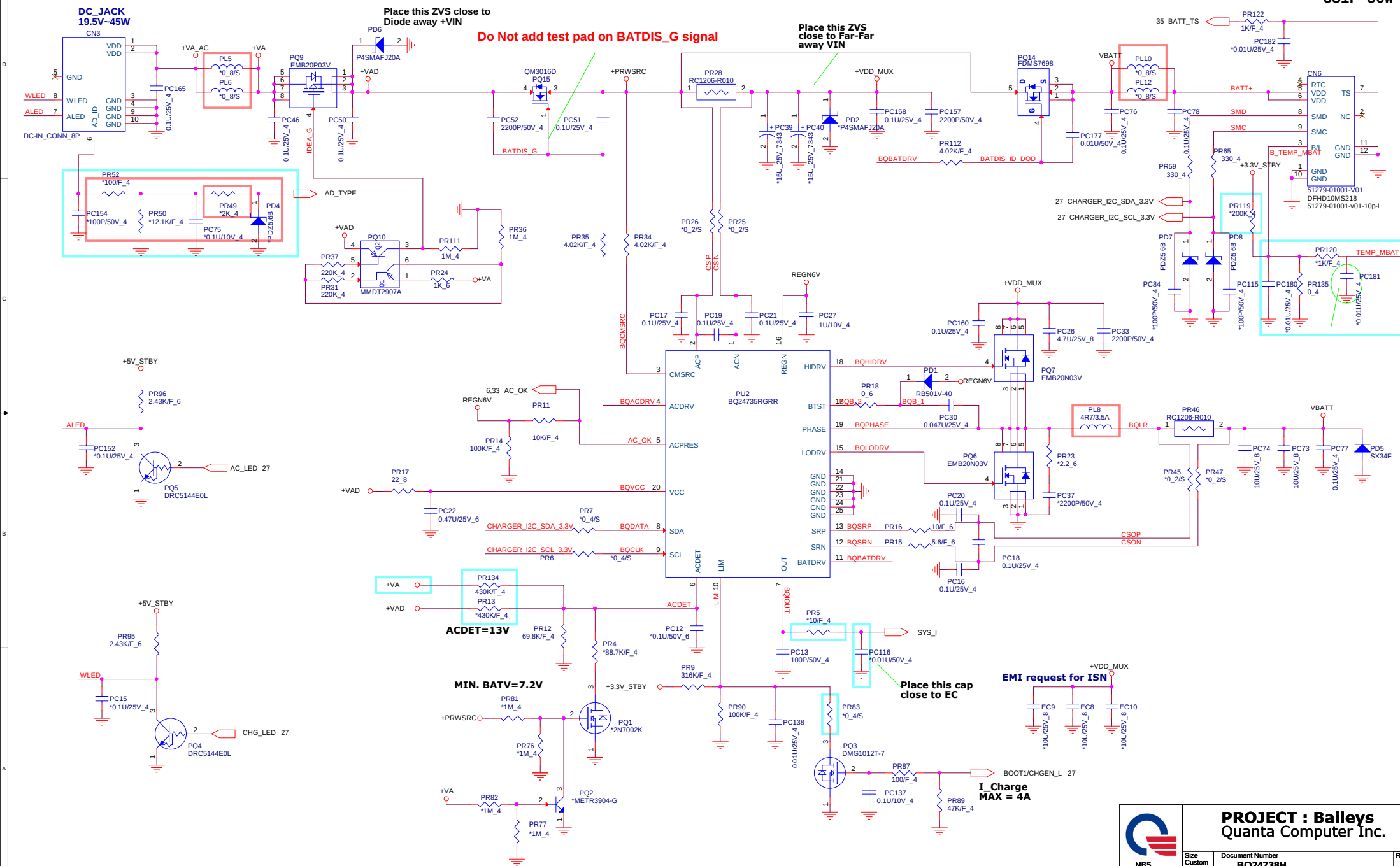
Size Custom	Document Number Keyboard CONNECTOR	Rev 1A
Date: Tuesday, June 10, 2014	Sheet 28	of 41

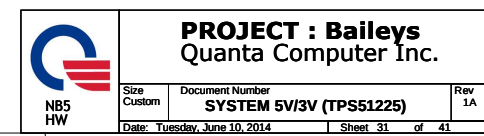
LID SENSOR

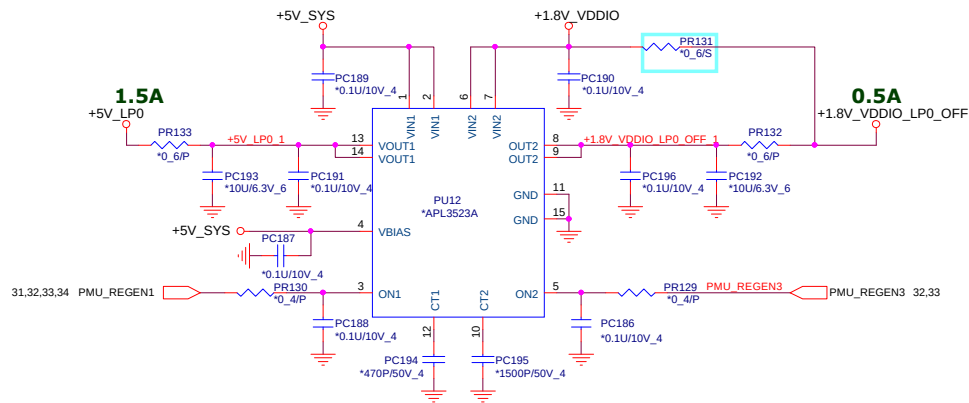
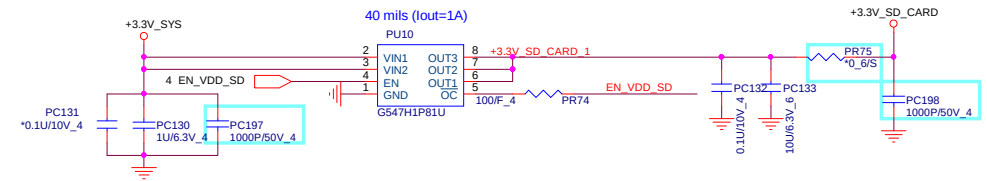
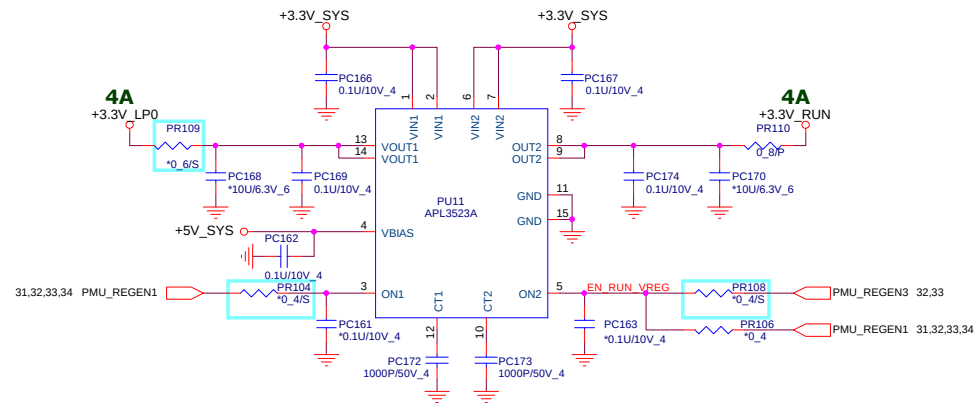


PWR LED

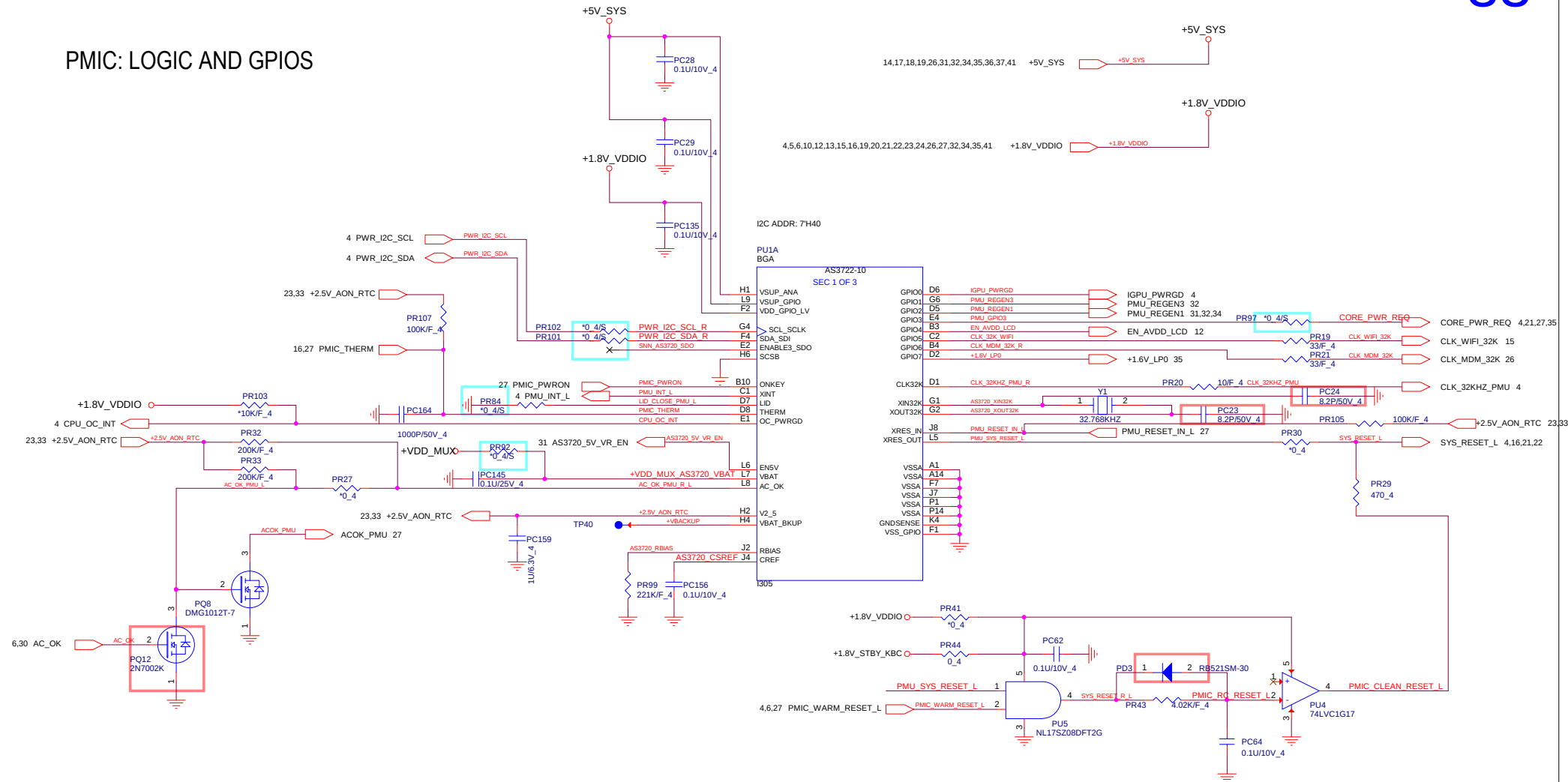




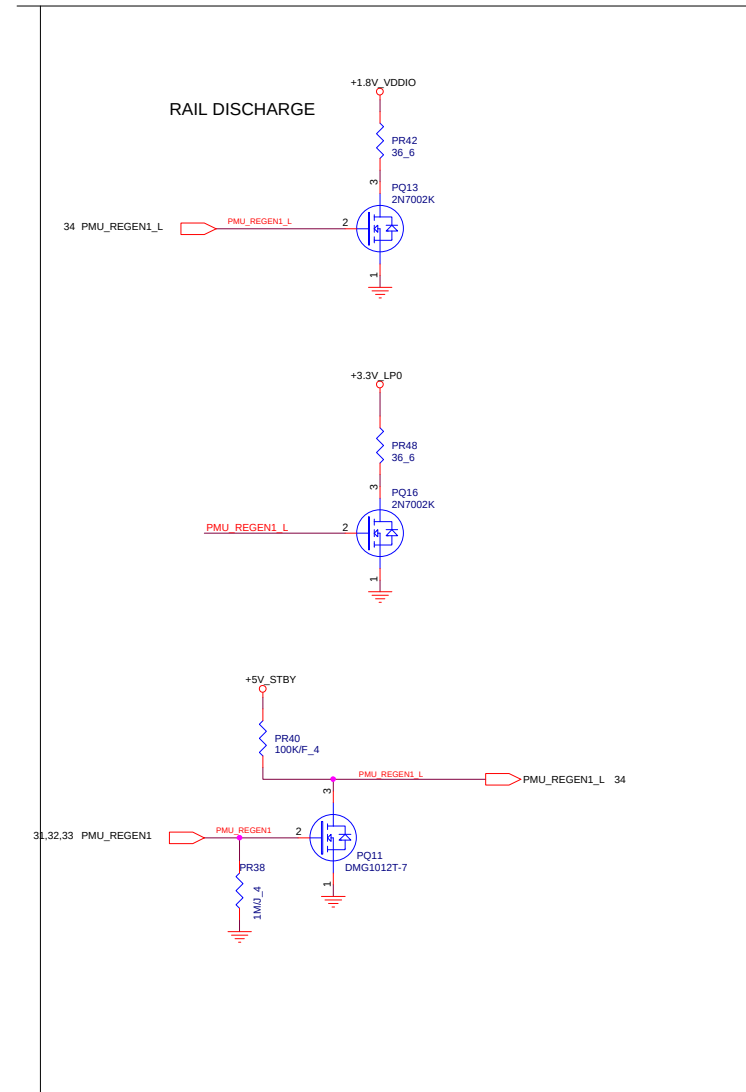
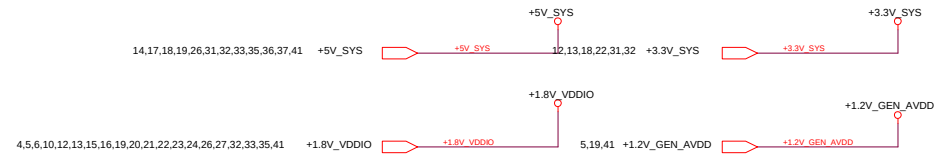
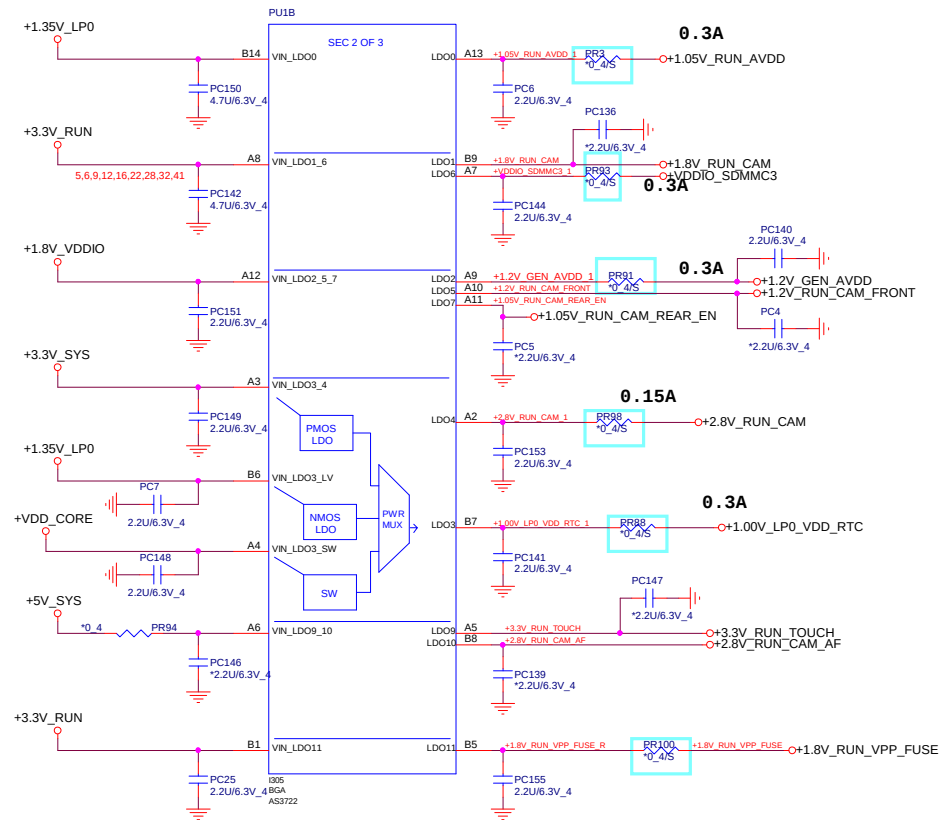


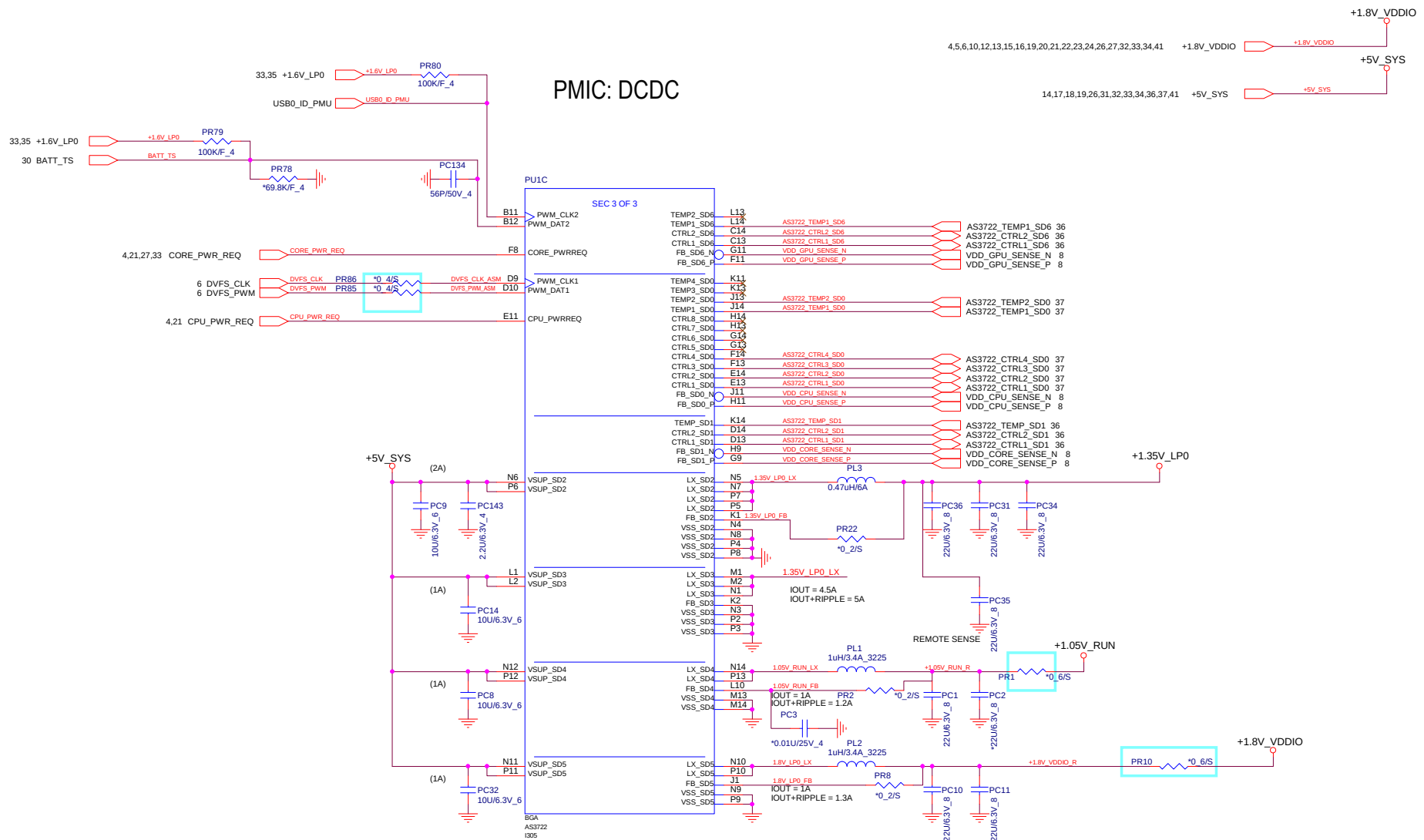


PMIC: LOGIC AND GPIOS



PMIC: CNTL, INT SW, LDOS





LAYOUT NOTE:
PLACE DCDC INPUT AND OUTPUT CAPS
SO THAT THEIR GNDs HAVE A COMMON SHAPE
ONE PAIR IS SHOWN HERE, PLEASE REPEAT
FOR SD2, SD3, SD4

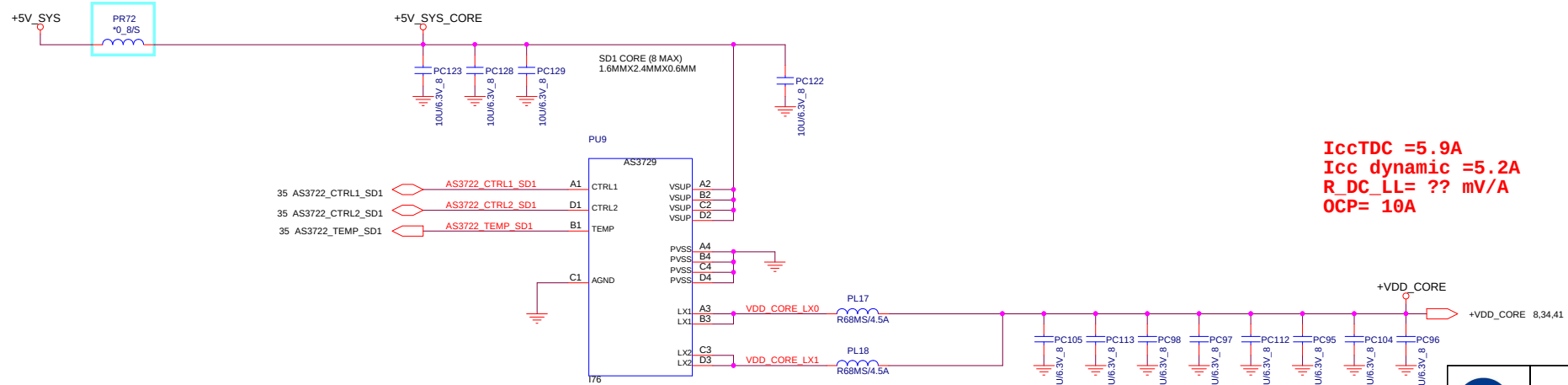
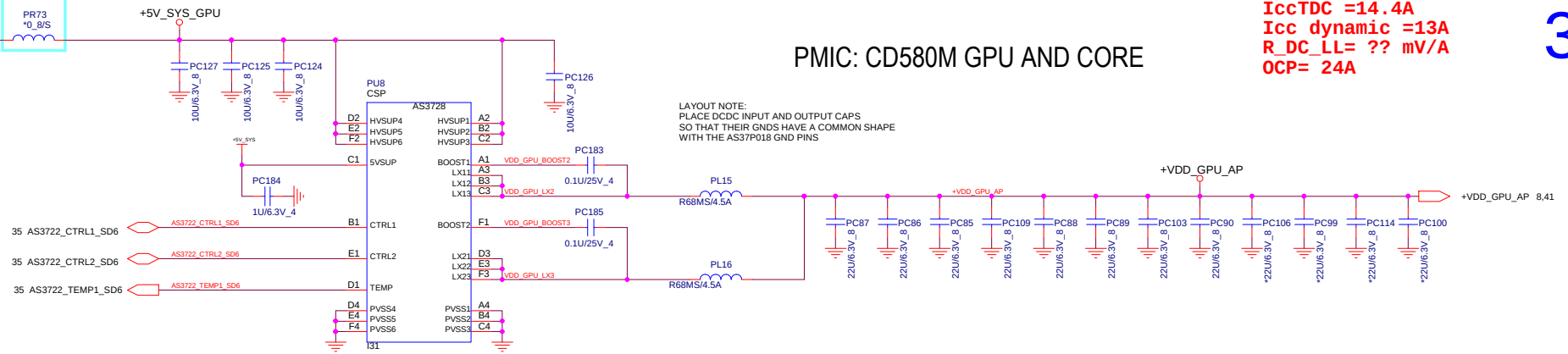


**PROJECT : Baileys
Quanta Computer Inc.**

Size	Document Number	Rev
Custom	PMIC_DCDC	1A
Date: Tuesday, June 10, 2014	Sheet 35	of 41

PMIC: CD580M GPU AND CORE

IccTDC =14.4A
 Icc dynamic =13A
 R_DC_LL= ?? mV/A
 OCP= 24A



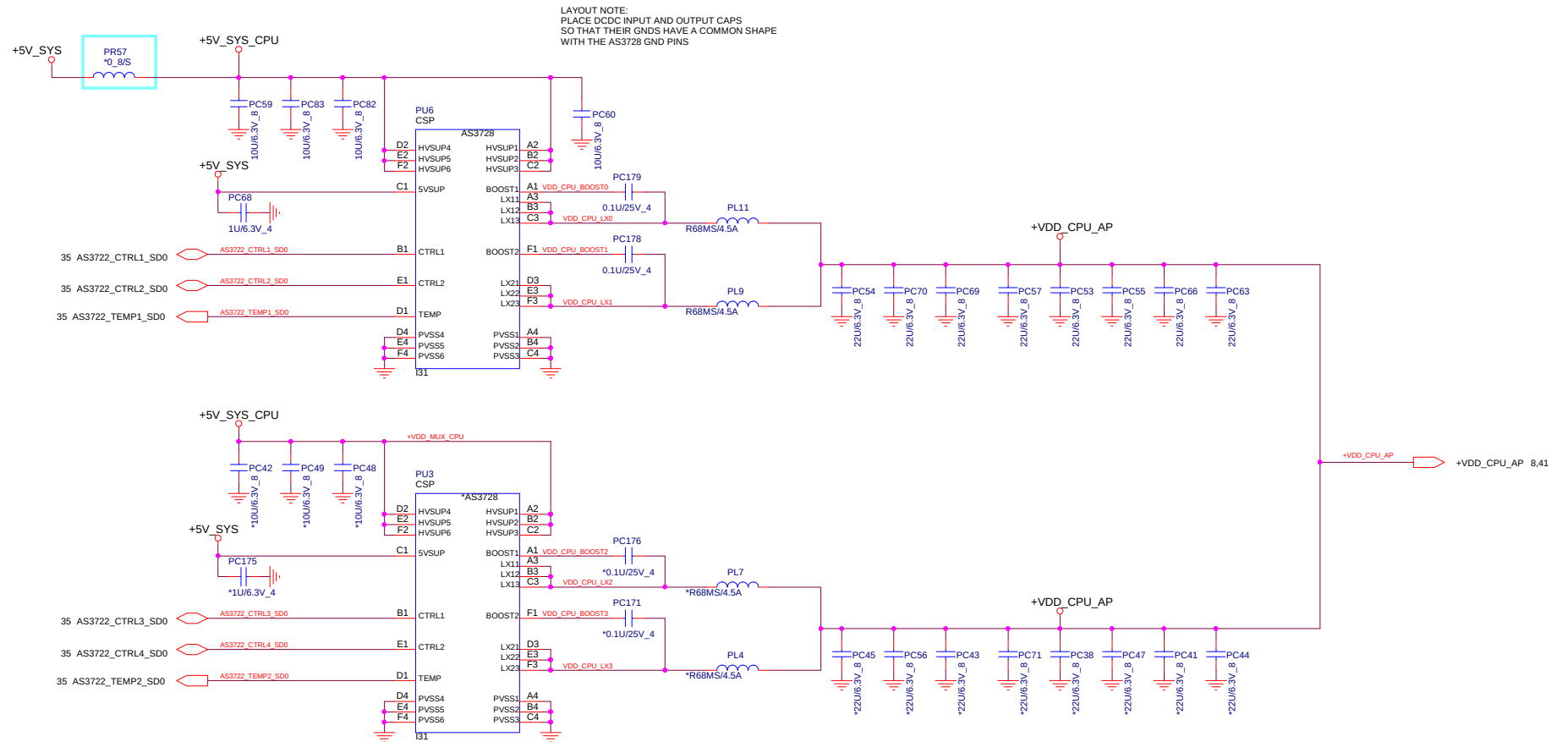
IccTDC =5.9A
 Icc dynamic =5.2A
 R_DC_LL= ?? mV/A
 OCP= 10A



**PROJECT : Baileys
 Quanta Computer Inc.**

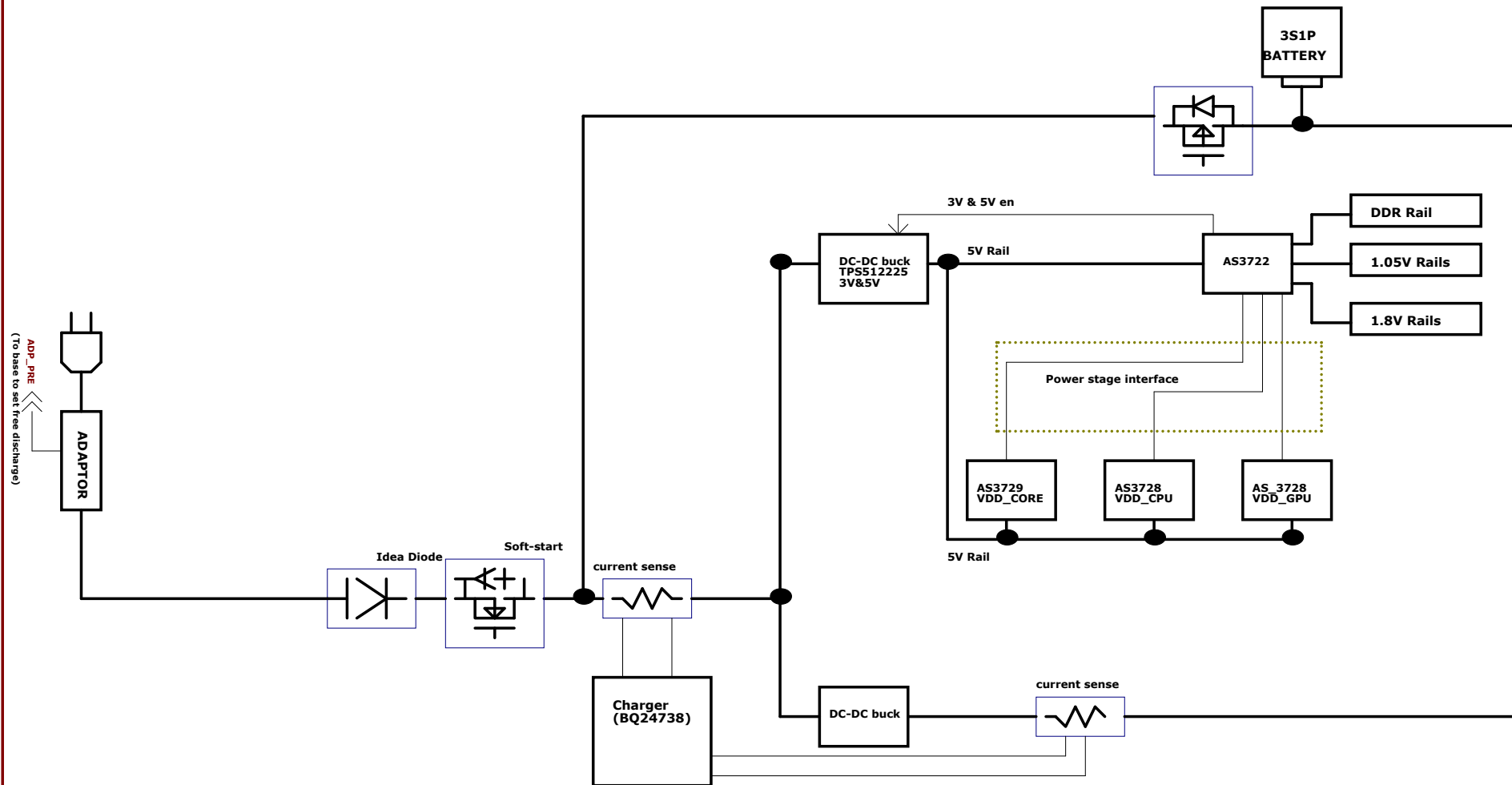
Size Custom	Document Number PMIC_GPU AND CORE	Rev 1A
Date: Tuesday, June 10, 2014	Sheet 36	of 41

VDD_CPU

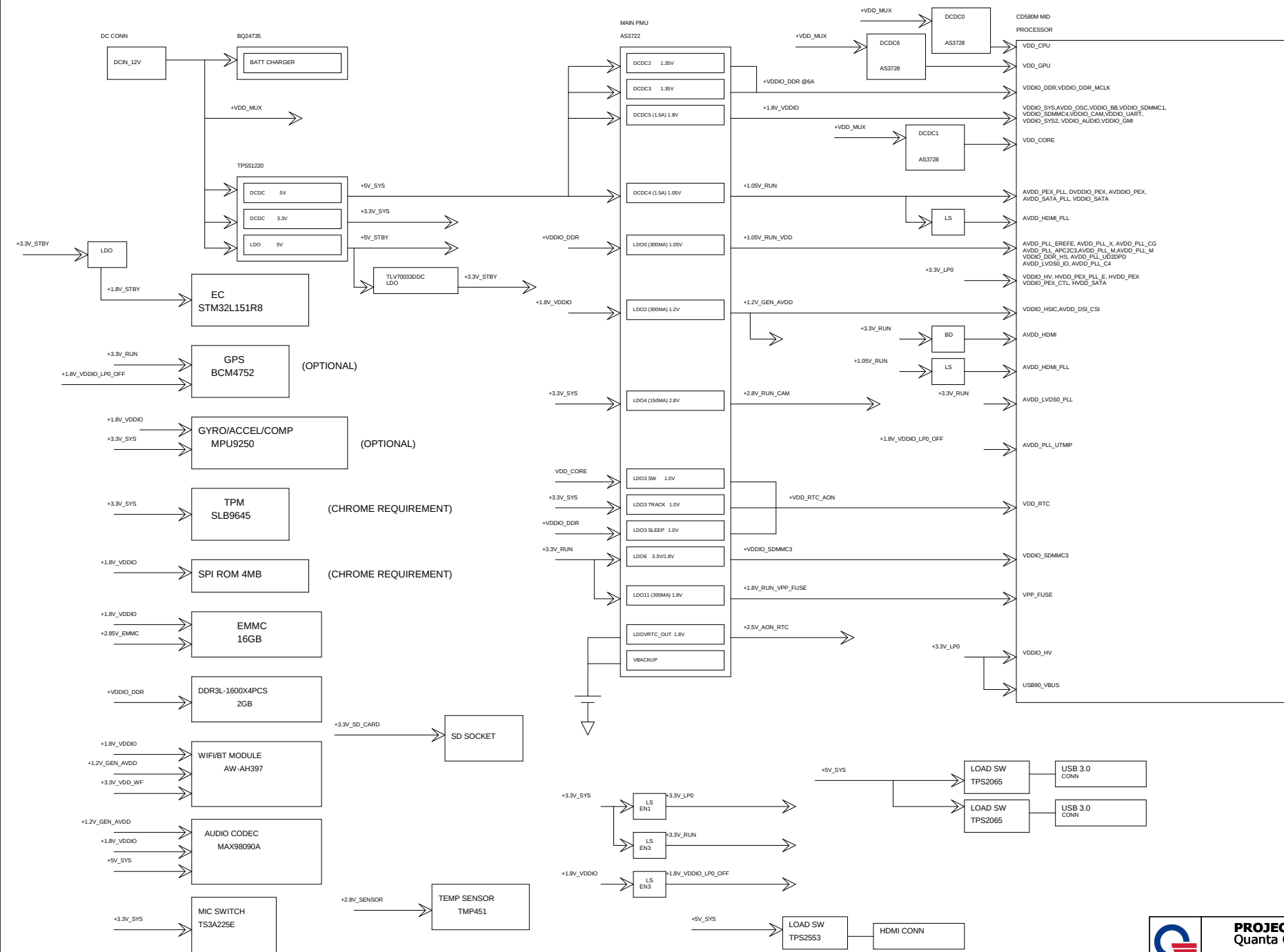


**PROJECT : Baileys
Quanta Computer Inc.**

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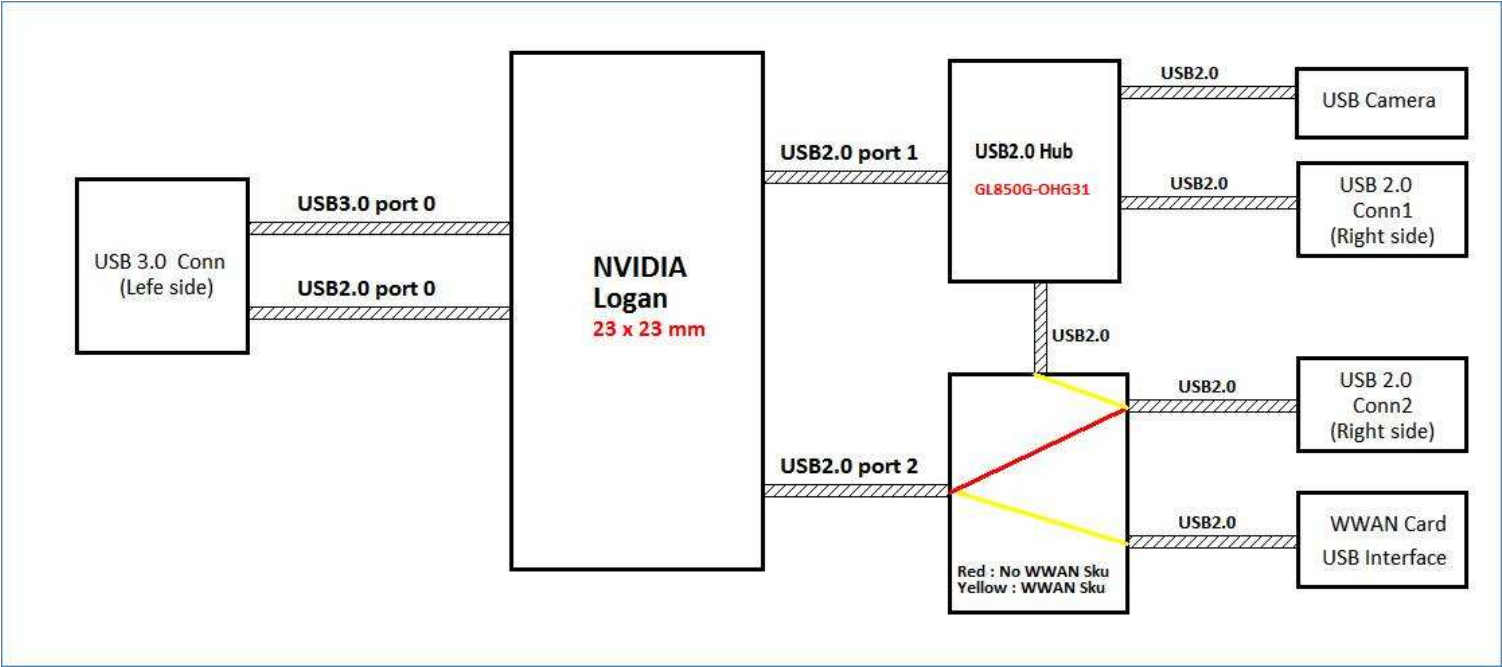


Peach power plane



I2C ADDRESS MAP

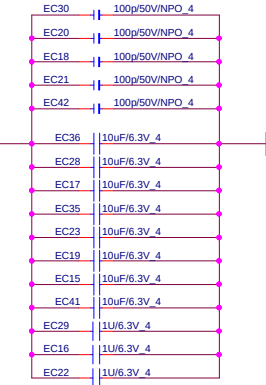
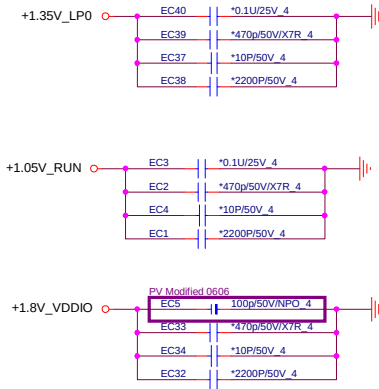
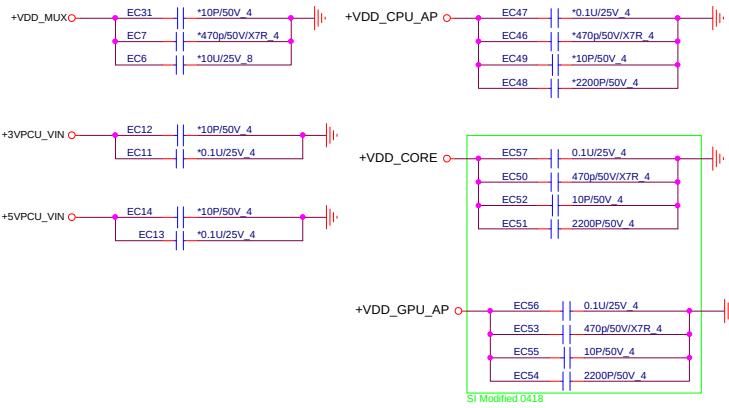
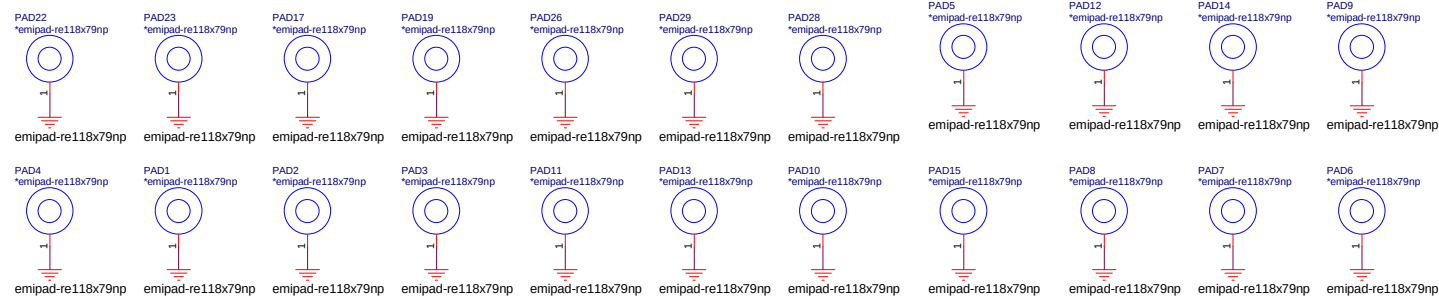
BUS	DEVICE	ADDRESS
GEN1_I2C - 1.8V	AUDIO CODEC	7'H10
	TEMPERATURE SENSOR	7'H4C
GEN2_I2C - 3.3V	Touch Pad	7'H15
	Touch Panel	7'H10
PWR_I2C - 1.8V	PMIC AS3722	7'H40
CAM_I2C - 3.3V	TPM	7'H20
CHARGER_I2C - 3.3V	EMBEDDED CONTROLLER	MASTER
	CHARGER BQ24735	7'H09
	BATTERY PACK	7'H0B



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RF solution



EMI solution

